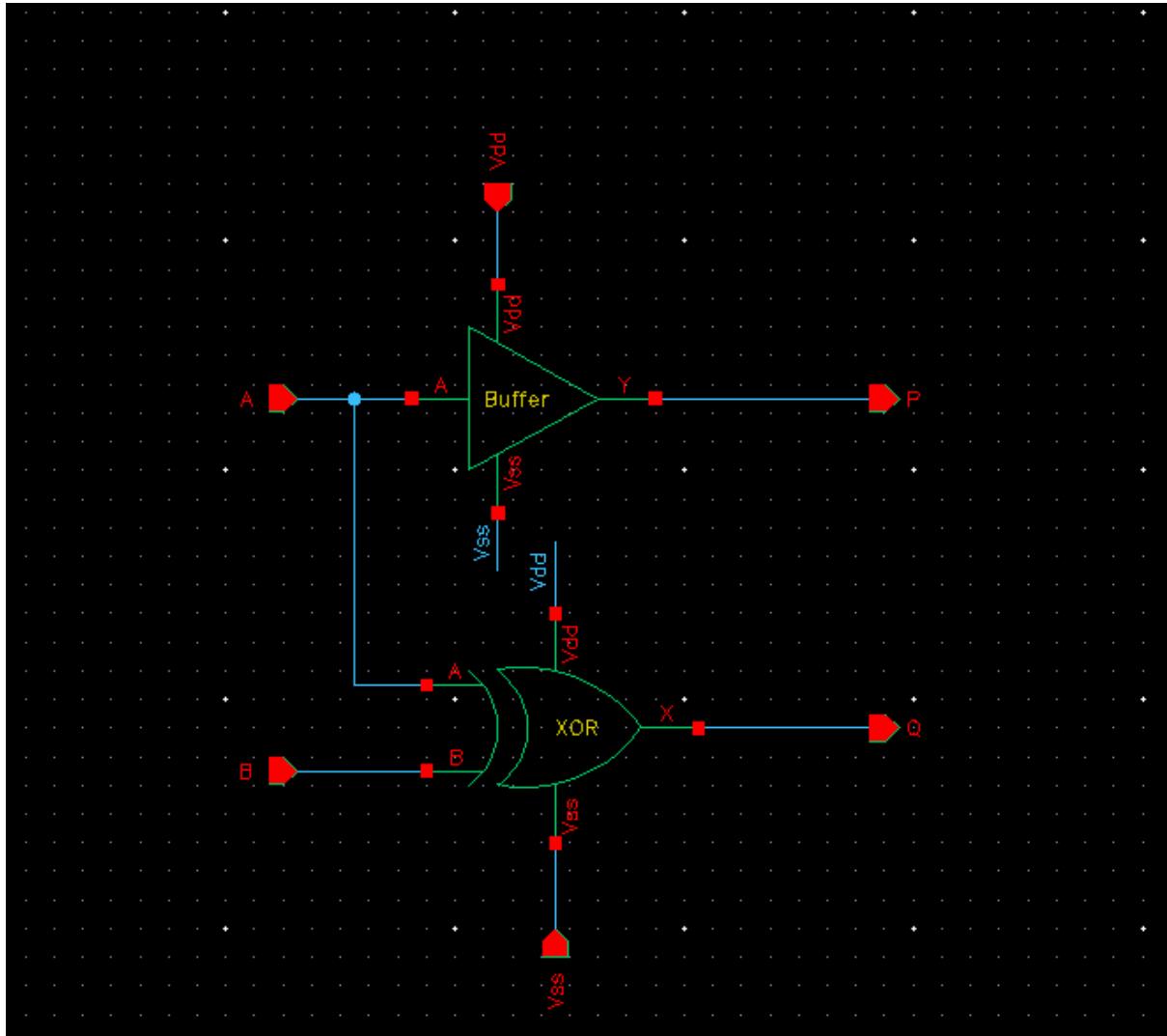


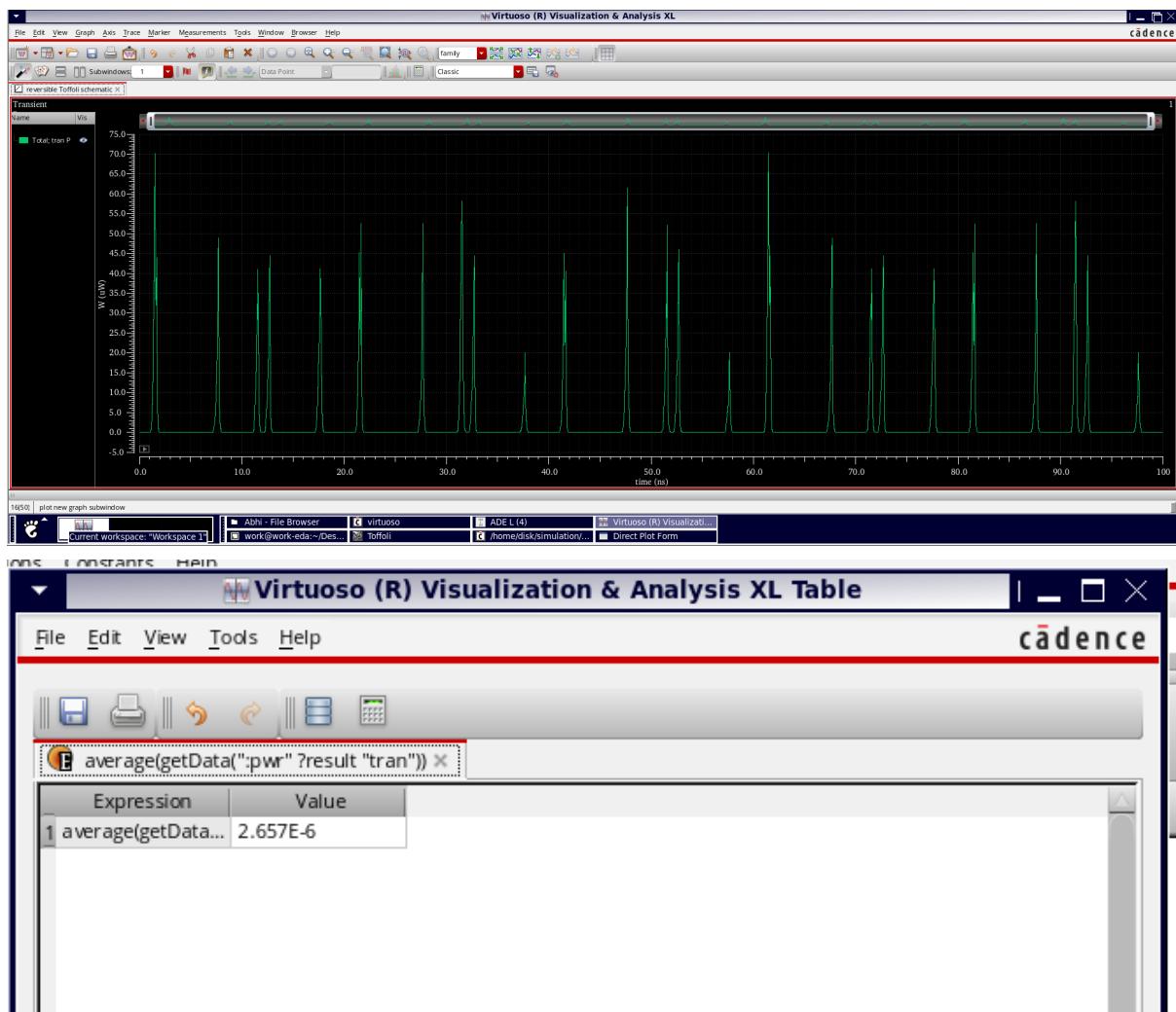
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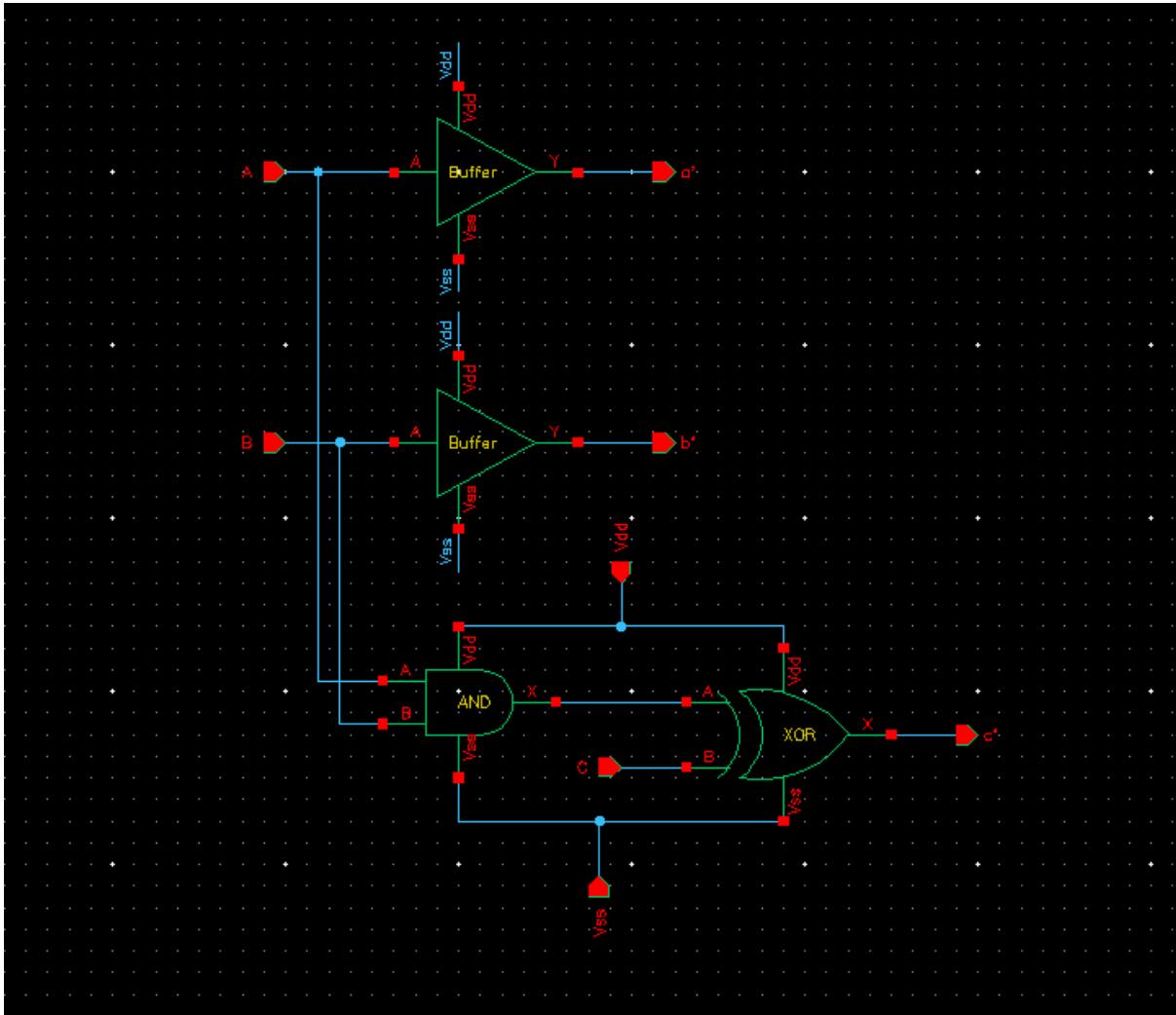




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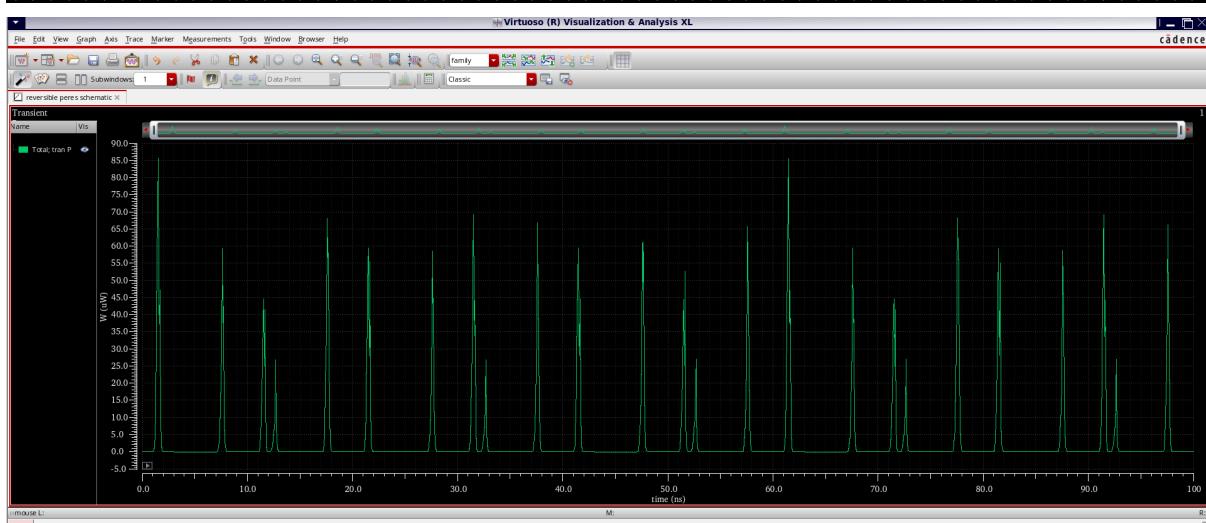
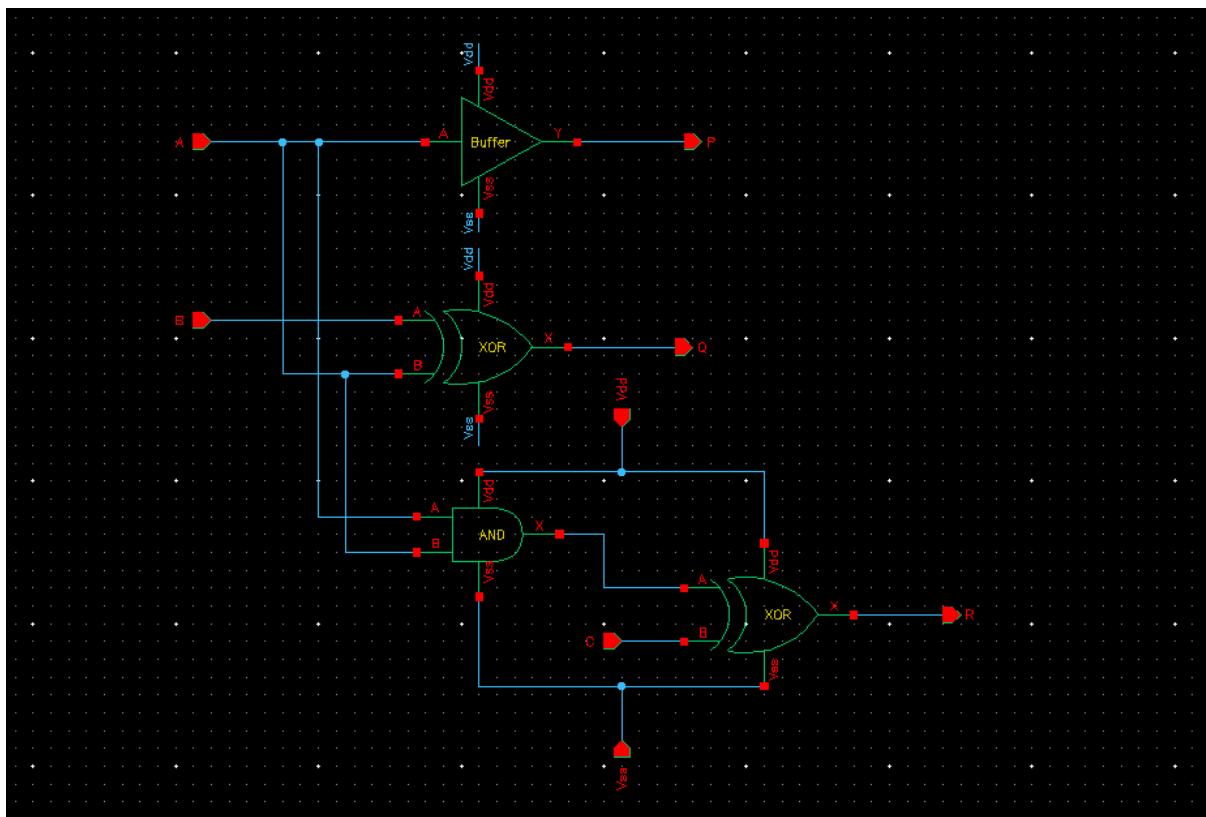
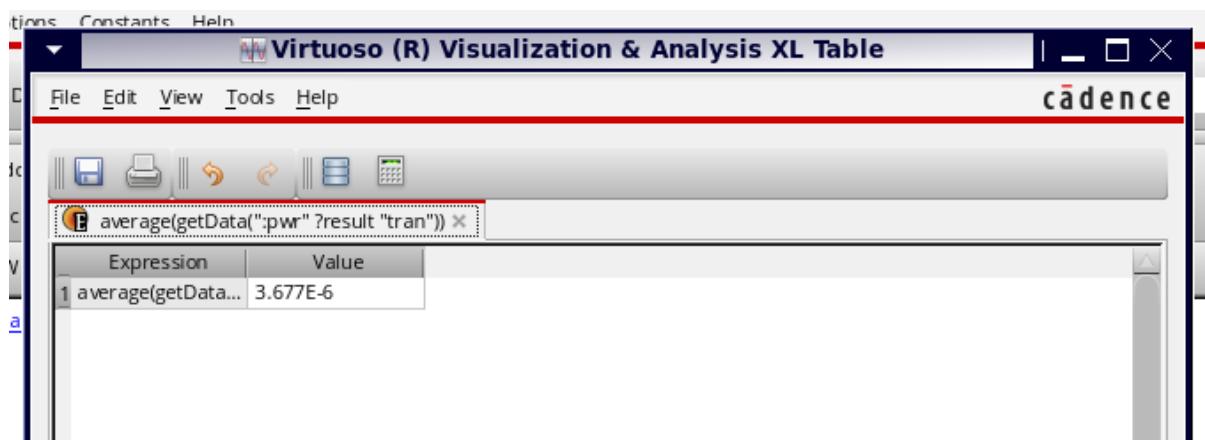




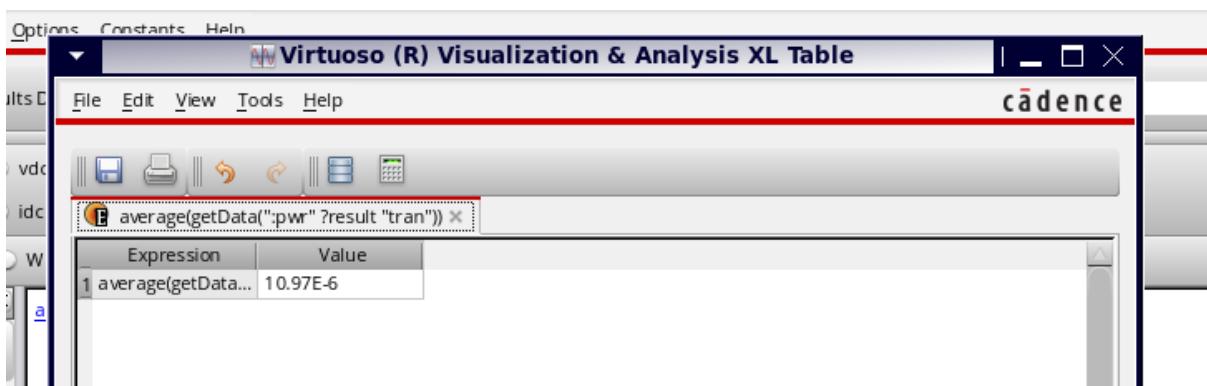
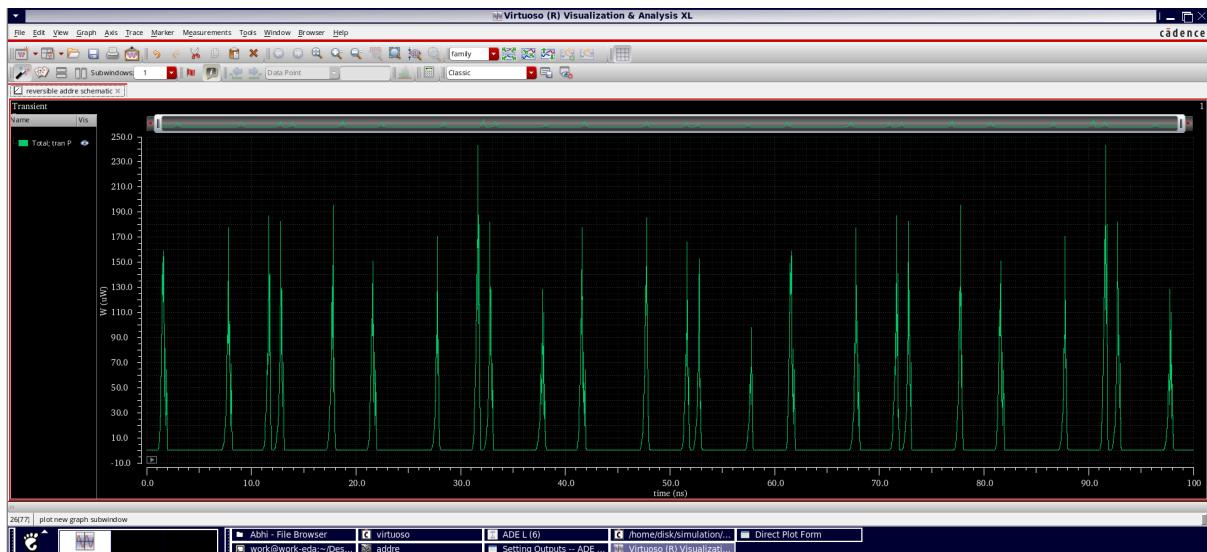


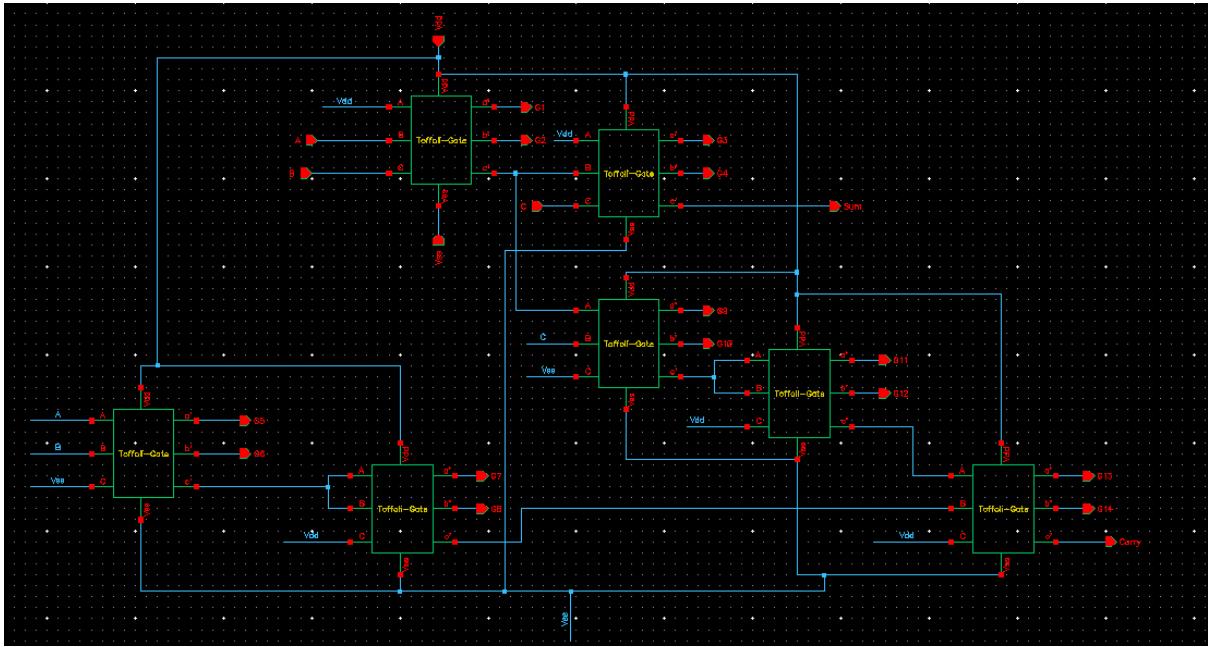
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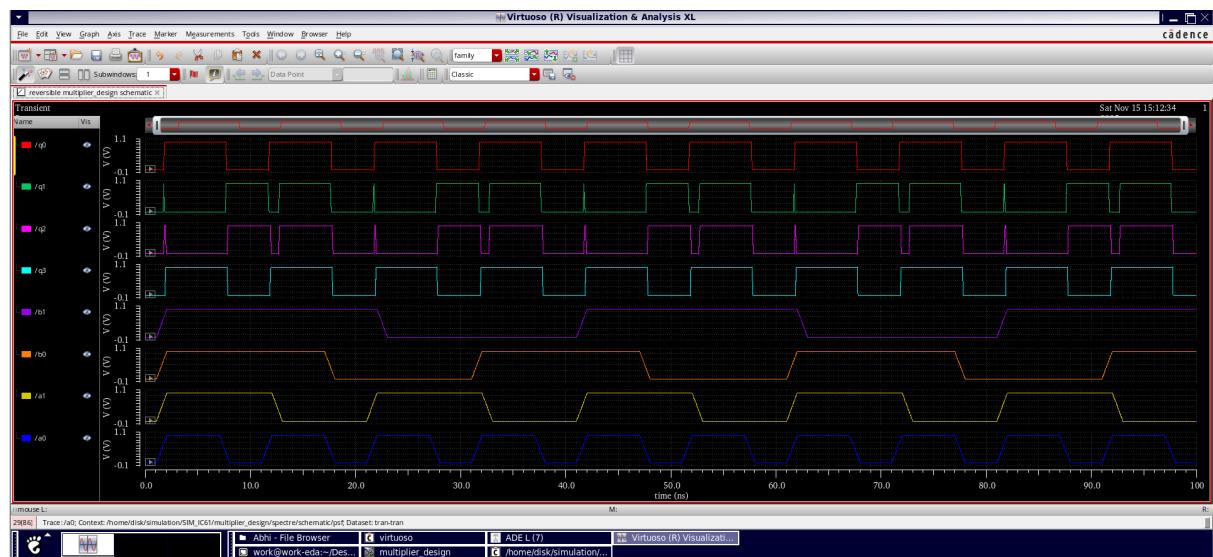


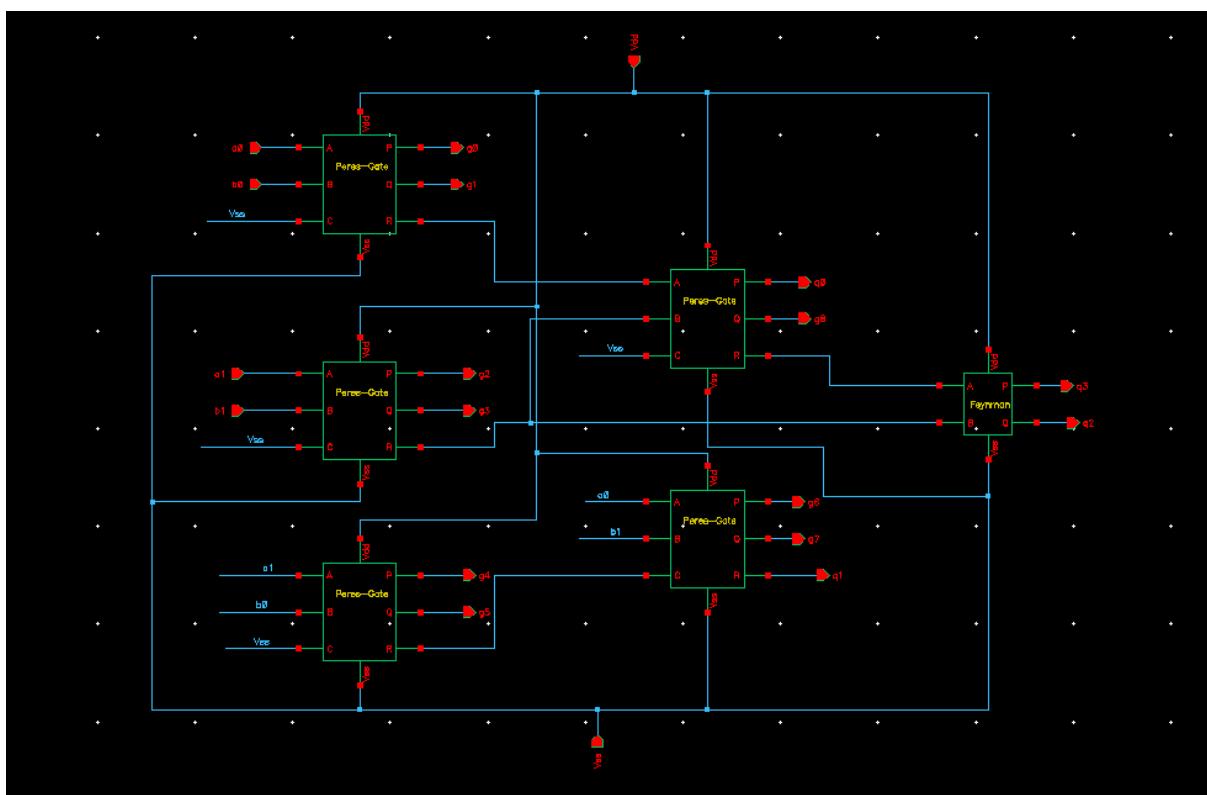
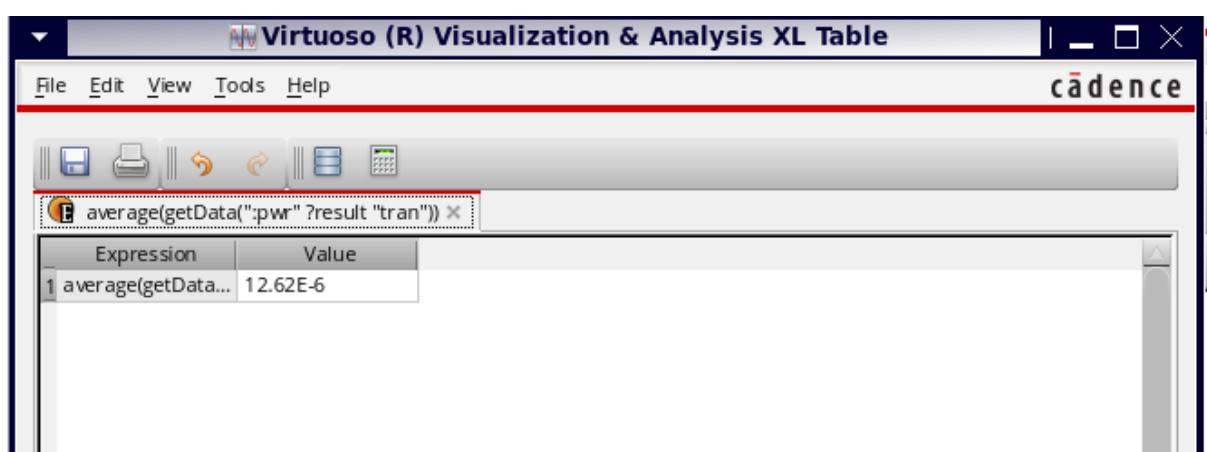
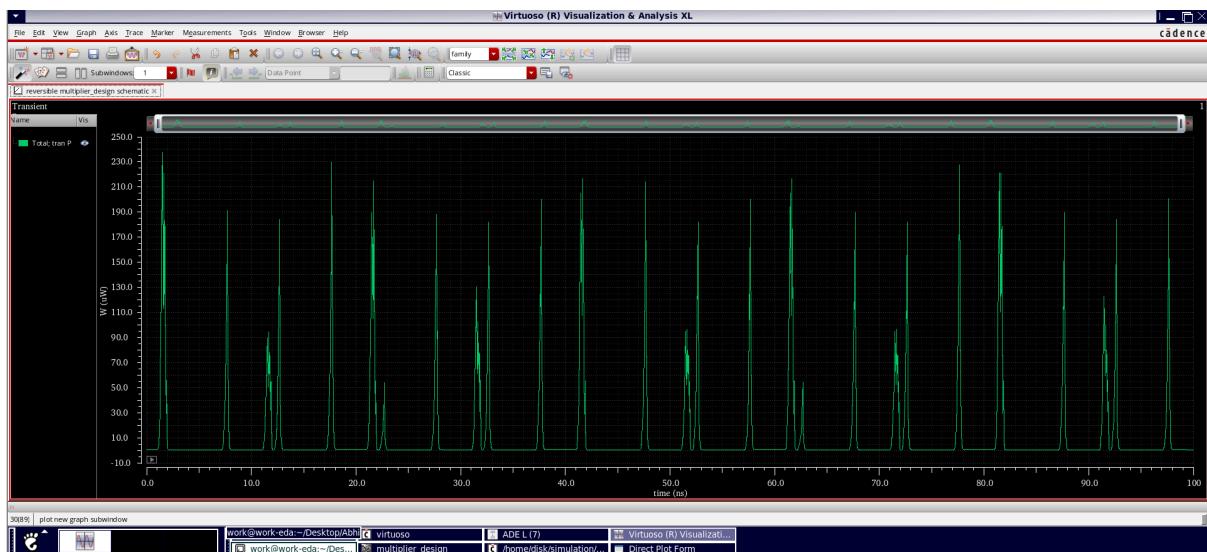
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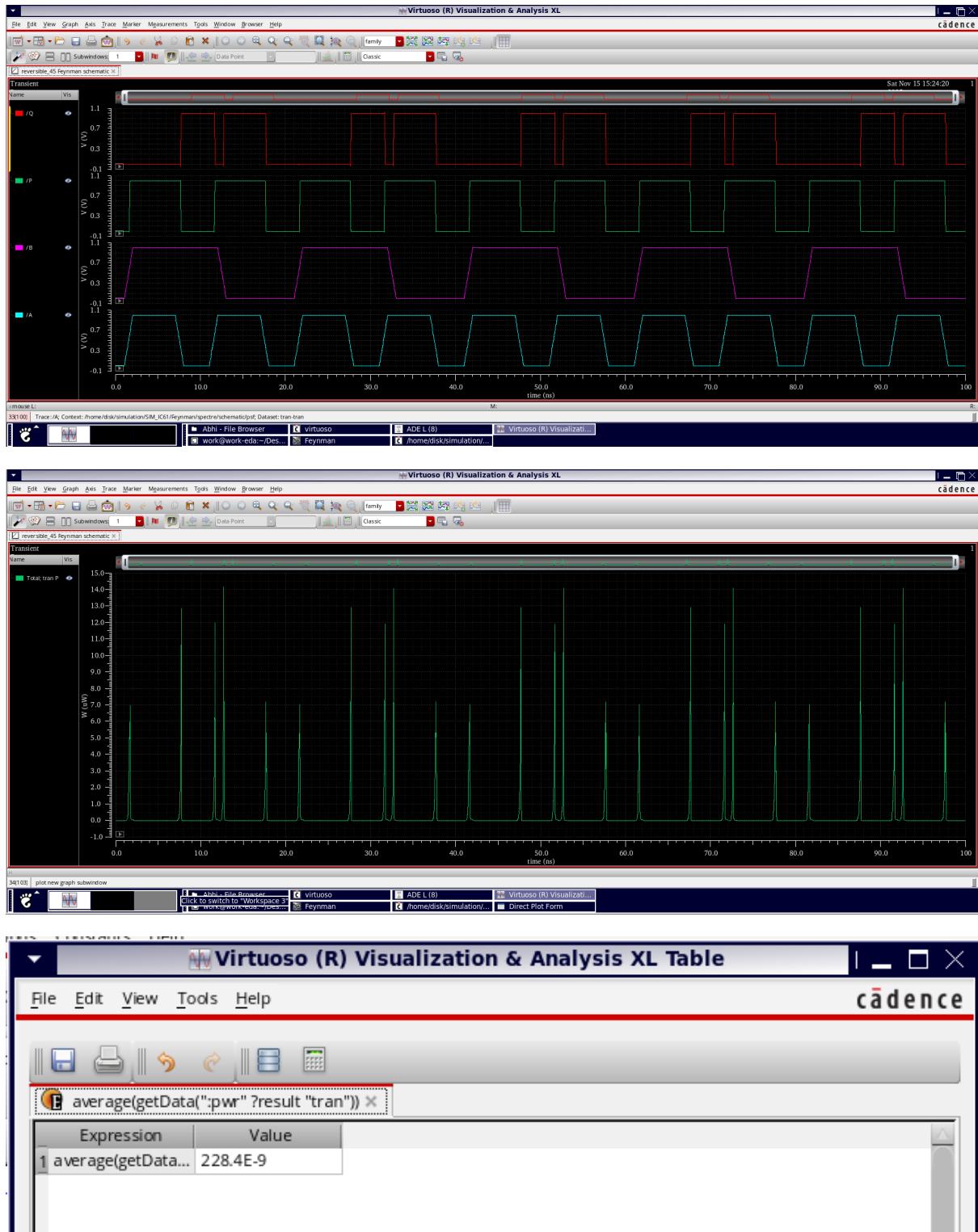


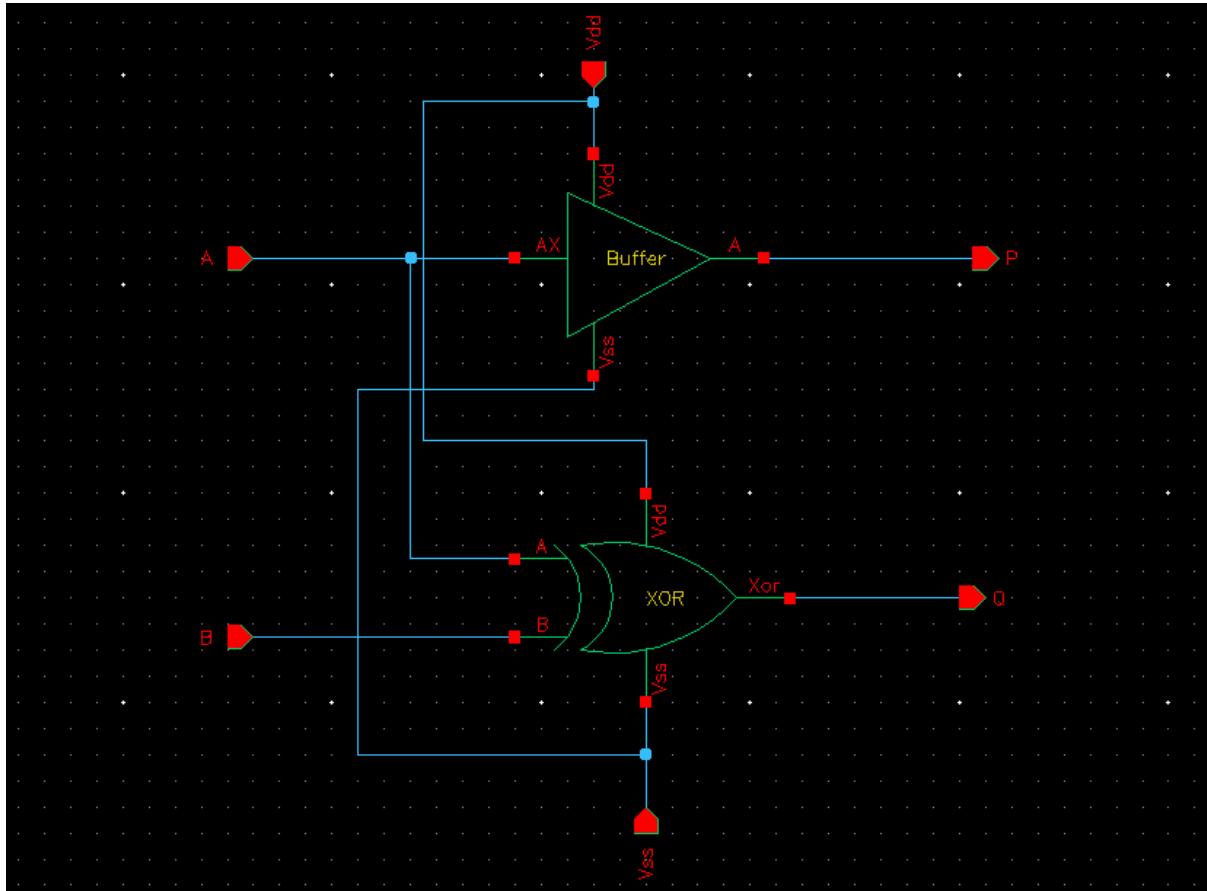
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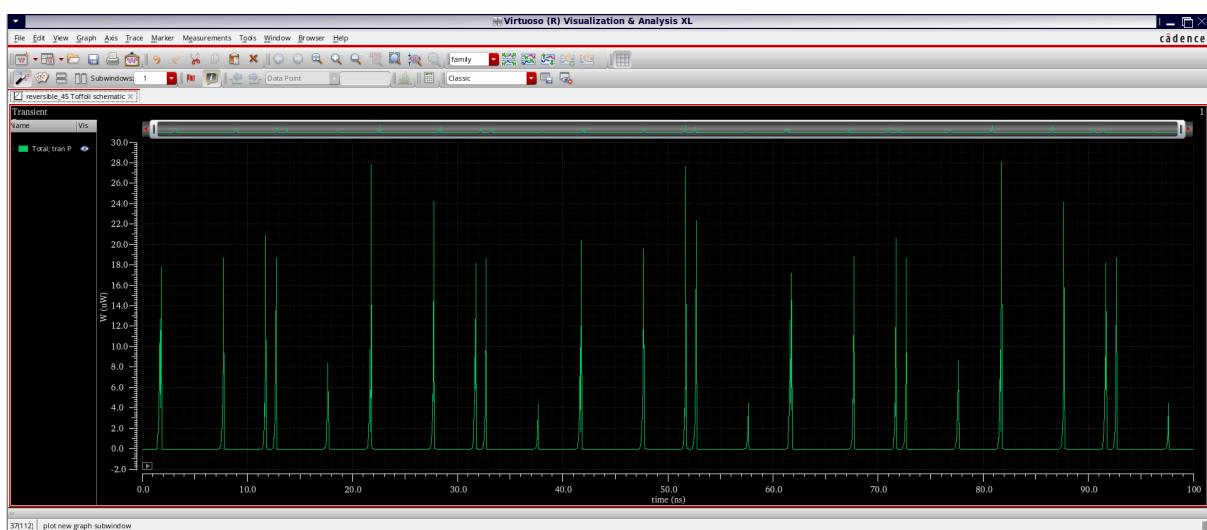
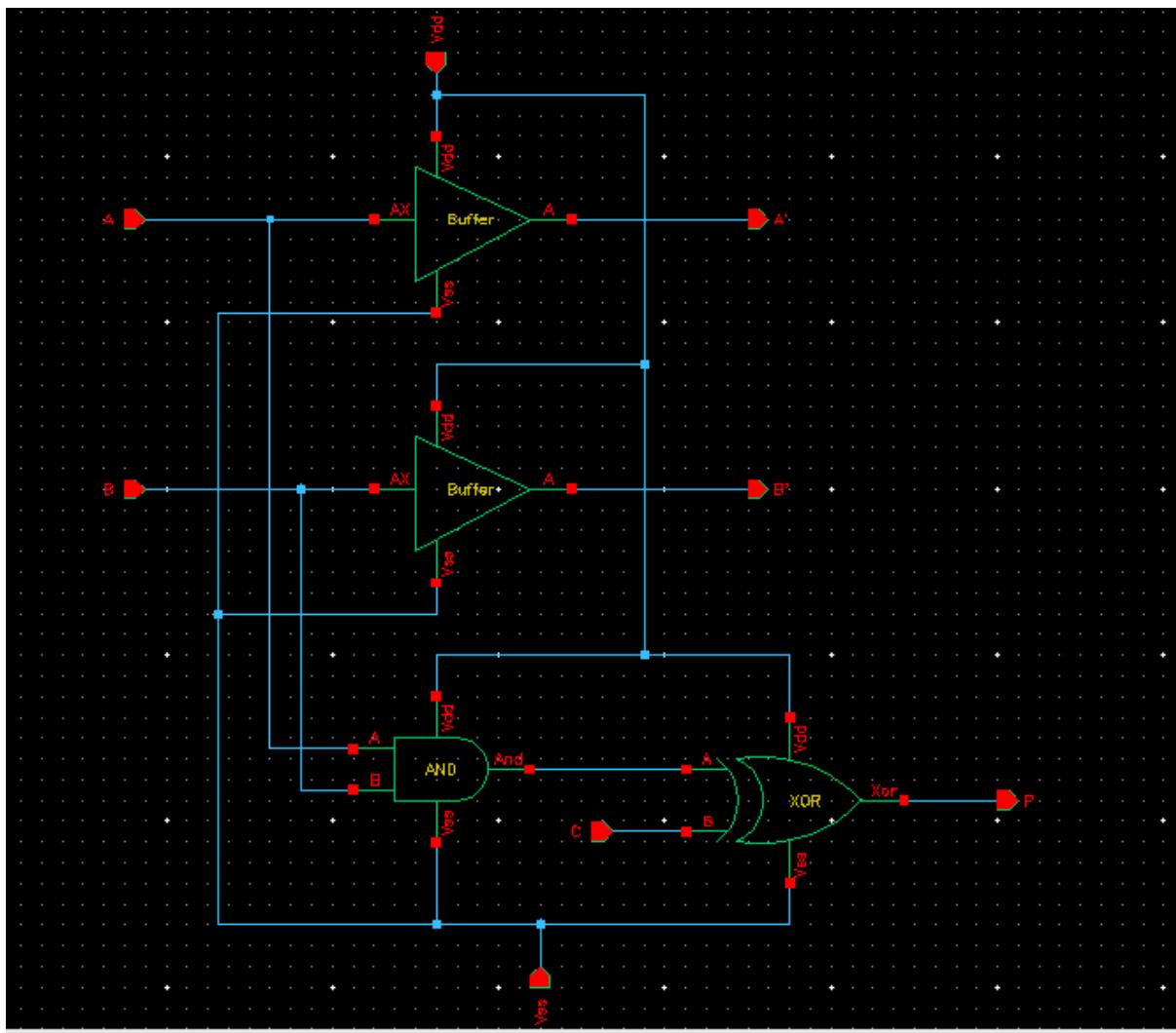
45nmtech: Feynman gate:

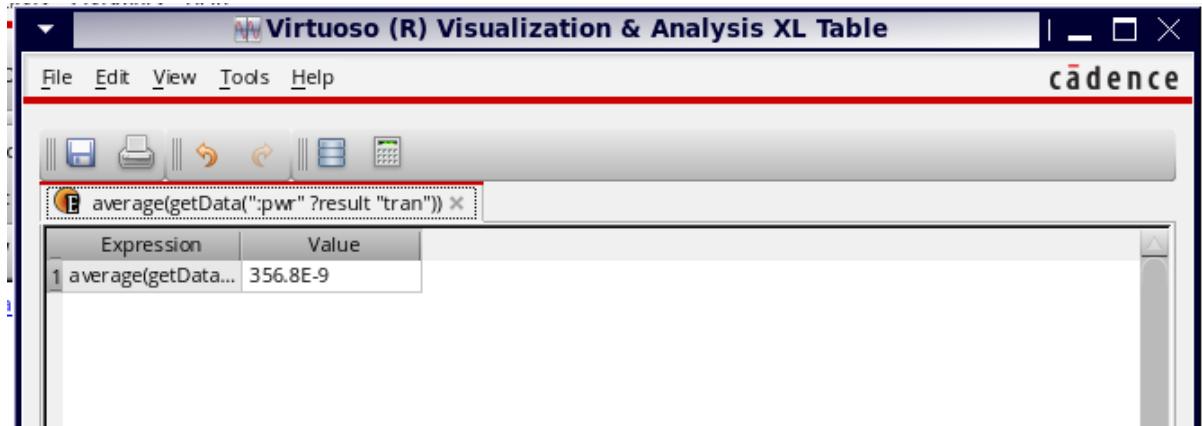




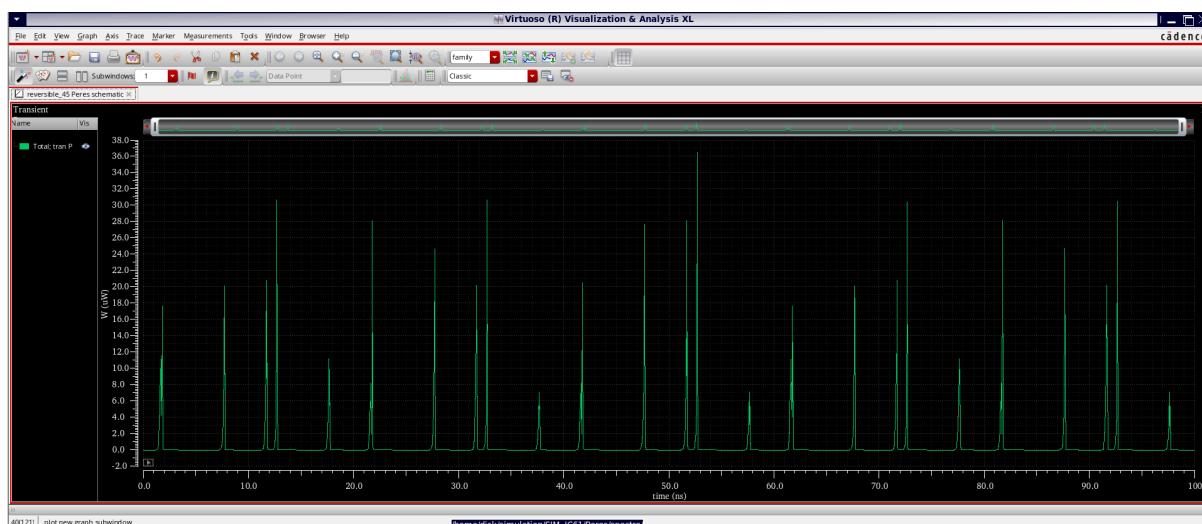
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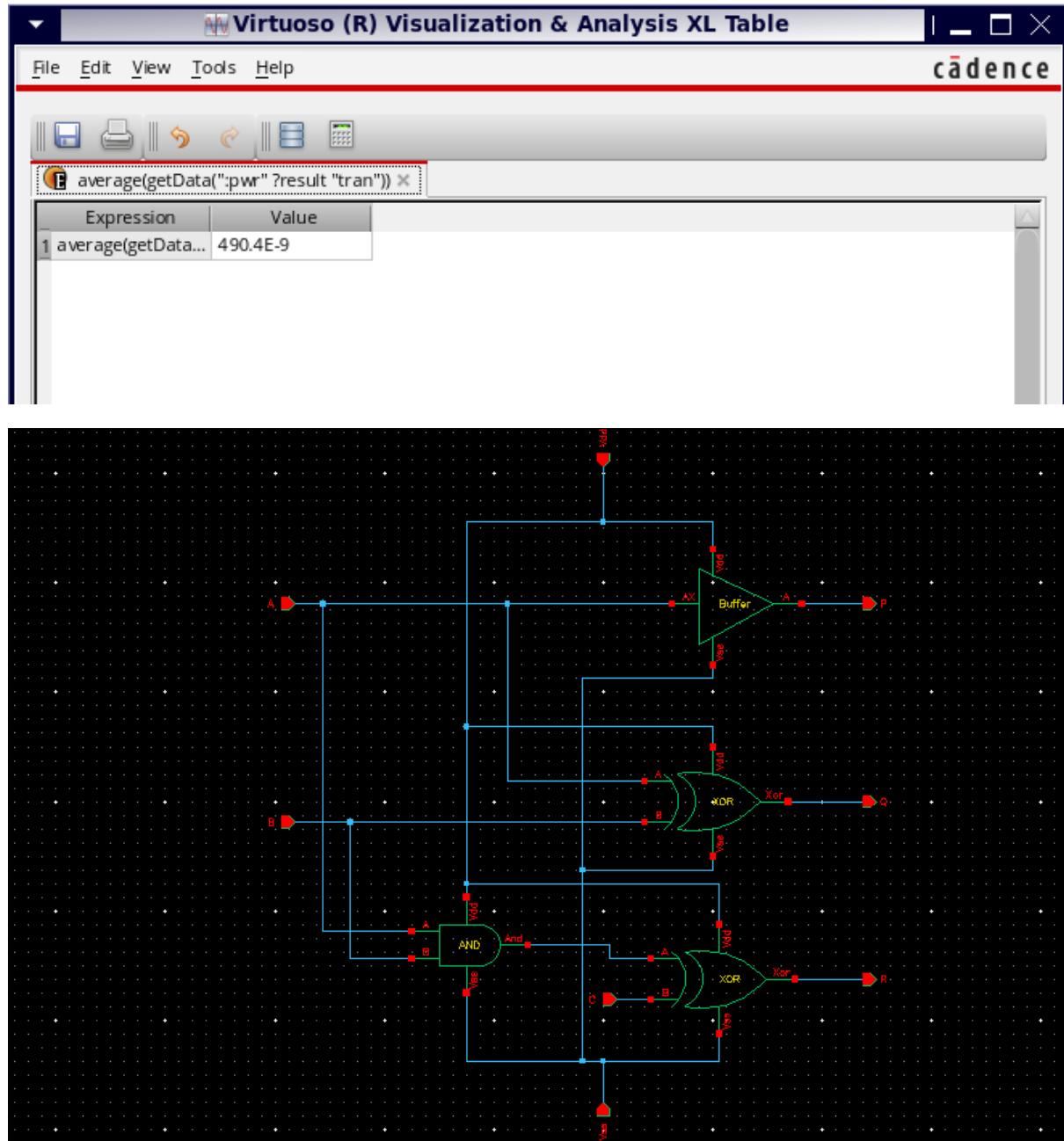






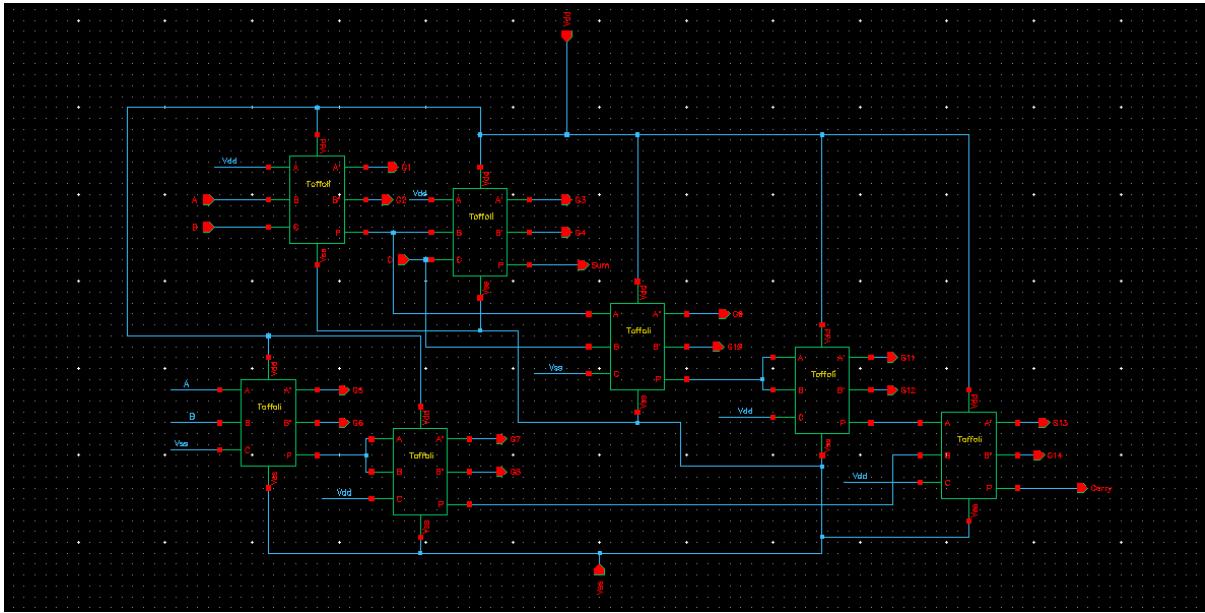
Peres Gate:





Adder_45nm:





Multiplier_45nm:

