ABES ENGINEERING COLLEGE, GHAZIABAD

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEEING



COURSE MATERIAL

Subject Name: Fundamentals of Electronics Engineering.

Subject Code: BEC-101/201

Branch/Semester: All Branches / 1st or 2nd

Session: 2023-24 (Odd & Even-Semester)

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EVALUATION - SCHEME, B. Tech- I YR./ I SEM (AKTU)

B. Tech. First Year, Semester- I

(All Branches except Agriculture Engineering and Biotechnology)

3- WEEKS STUDENT INDUCTION PROGRAMME in the beginning of the session Evaluation Scheme

										Evaluation	1 Scheme		
SN	Subject Code	Subject Name	Туре	Category	P	erio	d		sional conent	Sessional (SW) (TS/PS)	End Semester Examination (ESE)	Total	Credit
					L	Т	P	СТ	TA	CT+TA	TE/PE	SW+ESE	Cr
1.	BAS101/ BAS102	Engineering Physics/ Engineering Chemistry	Т	BS	3	1	0	20	10	30	70	100	4
2.	BAS103	Engineering Mathematics-I	T	BS	3	1	0	20	10	30	70	100	4
3.	BEE101/ BEC101	Fundamentals of Electrical Engineering/ Fundamentals of Electronics Engineering	Т	ES	2	1	0	20	10	30	70	100	3
4.	BCS101/ BME101	Programming for Problem Solving/ Fundamentals of Mechanical Engineering	Т	ES	2	1	0	20	10	30	70	100	3
5.	BAS104/ BAS105	Environment and Ecology/ Soft Skills	Т	BS/ HS	3	0	0	20	10	30	70	100	3
6.	BAS151/ BAS152	Engineering Physics Lab/ Engineering Chemistry Lab	Р	BS	0	0	3	-	50	50	50	100	1
7.	BEE151/ BEC151	Basic Electrical Engineering Lab/ Basic Electronics Engineering Lab	Р	ES	0	0	3	-	50	50	50	100	1
8.	BCS151/ BAS155	Programming for Problem Solving Lab / English Language Lab	Р	ES/ HS	0	0	3	-	50	50	50	100	1
9.	BCE151 / BWS151	Engineering Graphics & Design Lab/ Workshop Practice Lab	Р	ES	0	1	3	-	50	50	50	100	2
					13	5	12			350	550	900	22

UNIT-4

(Digital Electronics)

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- 1.2. Analog vs Digital

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1. Introduction to Digital Electronics

- Digital Electronics is the sub-branch of electronics that deals with digital signals for processing and controlling various systems and sub-systems.
- Digital electronics are the most common representation of Boolean algebra and are the basis of all digital circuits for computers, mobile phones, and numerous other consumer products.
- In various applications like sensors and actuators, usage of digital electronics is increasing extensively.
- The most common fundamental unit of digital electronics is the logic gate. By combining numerous logic gates (from tens to hundreds of thousands) more complex systems can be created.
- The complex system of digital electronics is collectively referred to as a digital circuit.

1.1 Need of Digital Electronics

- The more accurate representation of the digital signal can be generated using more binary digits and has a unique way of representation.
- Moreover, the immunity to noise in the digital system allows the data to be retrieved and stored without any degradation.
- In an analog system, the aging noise may vary due to wear and tear properties in the stored information. But in digital, if the noise is below the suggested level, the data can be restored perfectly. Even if there is any presence of significant noise, the redundancy use allows the restoring of actual data and provides a strong resistance to errors.

1.2 Analog Signal vs Digital Signal

Almost all the signals in the World are analog i.e. they are continuously varying values. There are lots of continuously variable signals or simply analog signals in nature like light, motion, sound, temperature, pressure etc.

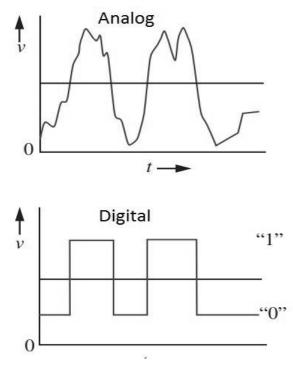


Figure 1.1 Representation of analog and digital signal

Digital Signals vary in discrete levels, in contrast to the continuous representation of analog signals. Generally, the discrete levels in a digital signal are just two values: ON and OFF. Even though all the physical signals of the nature are continuous analog signals, representing signals as discrete values has its own advantages.

Traditionally, all electronic devices processed analog information only but the development in technology has led to using digital signals for easy processing and such techniques are called Digital Signal Processing.

2. Number System and Representation

2.1 Introduction

In digital electronics, the number system is used for representing the information. The number system has different bases and the most common of them are the decimal, binary, octal, and hexadecimal. The base or radix of the number system is the total number of the digit used in the number system.

Table 2.1. Characteristics of Commonly used Number Systems

Number System	Base or radix(b)	Symbols used (d _i or d _{-i})	Weigassign posit	ed to	Example
	10011(0)	(01 01 0-1)	i	-j	
Binary	2	0,1	2^{i}	2 ^{-j}	1101.11
Octal	8	0,1,2,3,4,5,6,7	8^{i}	8 ^{-j}	3647.53
Decimal	10	0,1,2,3,4,5,6,7,8,9	10 ⁱ	10 ^{-j}	3874.65
Hexadecimal	16	0,1,2,3,4,5,6,7,8,9,A,B,C,D,E,F	16 ⁱ	16 ^{-j}	4DAC.54

2.2 Binary to Decimal Conversion

Conversion of binary to decimal (base-2 to base-10) numbers and back is an important concept to understand as the binary numbering system forms the basis for all computer and digital systems.

Examples:

➤ Convert binary (10101)₂ to decimal.

Solution:

$$10101B = 1 \times 2^4 + 0 \times 2^3 + 1 \times 2^2 + 0 \times 2^1 + 1 \times 2^0 = 16 + 4 + 1 = 21$$

➤ Convert binary (10110.001)₂ to decimal.

Solution:

First, multiply each bit of (10110.001)₂ with its respective positional weight, and then we add the products of all the bits with its weight.

$$(10110.001)_2 = (1 \times 2^4) + (0 \times 2^3) + (1 \times 2^2) + (1 \times 2^1) + (0 \times 2^0) + (0 \times 2^{-1}) + (0 \times 2^{-2}) + (1 \times 2^{-3})$$

$$(10110.001)_2 = (1 \times 16) + (0 \times 8) + (1 \times 4) + (1 \times 2) + (0 \times 1) + (0 \times 1/2) + (0 \times 1/4) + (1 \times 1/8)$$

$$(10110.001)_2 = 16 + 0 + 4 + 2 + 0 + 0 + 0 + 0 + 125$$

$$(10110.001)_2 = (22.125)_{10}$$

2.3 Decimal to Binary Conversion

For converting decimal to binary, there are two steps required to perform, which are as follows:

- i. Divide the given number by 2.
- ii. Get the integer quotient for the next iteration.
- iii. Get the remainder for the binary digit.
- iv. Repeat the steps until the quotient is equal to 0.

Examples:

 \triangleright Convert (13)₁₀ to binary:

Solution:

Division by 2	Quotient	Remainder
13/2	6	1(LSB)
6/2	3	0
3/2	1	1
1/2	0	1(MSB)

$$S_{0}$$
, $(13)_{10} = (1101)_2$

 \triangleright Convert $(152.25)_{10}$ to binary

Solution:

Step 1: Divide the number 152 and its successive quotients with base 2.

Divide by 2	Quotient	Remainder
152/2	76	0 (LSB)
76/2	38	0
38/2	19	0
19/2	9	1
9/2	4	1
4/2	2	0
2/2	1	0
1/2	0	1(MSB)

 $(152)_{10} = (10011000)_2$

Step 2: Now, perform the multiplication of 0.25 and successive fraction with base 2.

Operation	Result	carry
0.25×2	0.50	0
0.50×2	0	1

$$(0.25)_{10} = (.01)_2$$

So,
$$(152.25)_{10} = (10011000.01)_2$$

2.4 Binary to Hexadecimal Conversion

The base numbers of binary and hexadecimal are 2 and 16, respectively. In a binary number, the pair of four bits is equal to one hexadecimal digit. There are also only two steps to convert a binary number into a hexadecimal number which are as follows:

- i. In the first step, we have to make the pairs of four bits on both sides of the binary point. If there will be one, two, or three bits left in a pair of four bits pair, we add the required number of zeros on extreme sides.
- ii. In the second step, we write the hexadecimal digits corresponding to each pair.

Examples:

➤ Convert binary (1101100)₂ to hexadecimal.

Solution:

Convert every 4 binary bits (starting from LSB) to hex digit i.e.

$$\begin{array}{c}
\mathbf{6} \\
1101100_{2} = 0 \ 1 \ 1 \ 0 \\
\mathbf{C}
\end{array} = 6 \ \mathbf{C} = (6 \ \mathbf{C})_{16}$$

Note: In the above example, the left side of the binary point has only three bits. To make it a complete pair of 4 bits, add one zero on the extreme side.

Table 2.2. Reference table for binary to hexadecimal equivalent

Binary () ₂	Hex Equivalent () ₁₆	
0000	0	
0001	1	
0010	2	
0011	3	
0100	4	
0101	5	
0110	6	
0111	7	
1000	8	
1001	9	
1010	A	
1011	В	
1100	С	
1101	D	
1110	Е	
1111	F	

> Convert binary (101101011.0011)₂ to hexadecimal.

Solution:

Firstly, make pairs of 4 bits on both sides of the binary point.

111 1010 1011.0011

On the left side of the binary point, the first pair has three bits. To make it a complete pair of four bits, add one zero on the extreme side i.e.

0111 1010 1011.0011

Now, write the hexadecimal digits correspond to each pair 4 bit pair i.e.

 $(0111\ 1010\ 1011.0011)_2 = (7AB.3)_{16}$

2.5 Decimal to Hexadecimal Conversion

For converting decimal to hexadecimal, there are two steps required to perform, which are as follows:

- i. Divide the number by 16.
- ii. Get the integer quotient for the next iteration.
- iii. Get the remainder for the hex digit.
- iv. Repeat the steps until the quotient is equal to 0.

Examples:

 \triangleright Convert $(7562)_{10}$ to hex

Solution:

	-	Remainder (decimal)	Remainder (hex)
7562/16		10	A
472/16	29	8	8
29/16	1	13	D
1/16	0	1	1

So,
$$(7562)_{10} = (1D8A)_{16}$$

➤ Convert (152.25)₁₀ to hex

Solution:

First, divide the number 152 and its successive quotients with base 16.

Operation Quotient Remainder

$$(152)_{10} = (98)_{16}$$

Now, perform the multiplication of 0.25 and successive fraction with base 16.

Operation Result carry

$$0.25 \times 16$$
 0 4

$$(0.25)_{10} = (4)_{16}$$

Hence, the hexadecimal number of the decimal number (152.25)₁₀ is (230.4)₁₆.

2.6 Hexadecimal to Decimal Conversion

A regular decimal number is the sum of the digits multiplied with power of 10. For example, $(137)_{10}$ is equal to each digit multiplied with its corresponding power of 10, i.e.

$$(137)_{10} = 1 \times 10^2 + 3 \times 10^1 + 7 \times 10^0 = 100 + 30 + 7.$$

Likewise, hex numbers are read the same way, but each digit counts power of 16 instead of power of 10.

For hex number with n digits, i.e. dn-1 ... d3 d2 d1 d0

Multiply each digit of the hex number with its corresponding power of 16 and sum:

Hence, decimal =
$$d_{n-1} \times 16^{n-1} + ... + d_3 \times 16^3 + d_2 \times 16^2 + d_1 \times 16^1 + d_0 \times 16^0$$

Examples:

➤ Convert (E7A9)₁₆ to decimal

Solution

E7A9 in base 16 is equal to each digit multiplied with its corresponding 16ⁿ i.e.

$$E7A9_{16} = 14 \times 16^3 + 7 \times 16^2 + 10 \times 16^1 + 9 \times 16^0 = 57344 + 1792 + 160 + 9 = 59305_{10}$$

Hence, $(E7A9)_{16} = (59305)_{10}$

➤ Convert (3B)₁₆ to decimal

Solution

3B in base 16 is equal to each digit multiplied with its corresponding 16ⁿ i.e.

$$3B_{16} = 3 \times 16^1 + 11 \times 16^0 = 48 + 11 = 59_{10}$$

Hence, $(3B)_{16} = (59)_{10}$

Convert (152A.25)₁₆ to decimal

Solution

First, multiply each digit of **152A.25** with its respective positional weight, and then we add the products of all the bits with its weight.

$$(152A.25)_{16}$$
= (1×16^3) + (5×16^2) + (2×16^1) + $(A\times16^0)$ + (2×16^{-1}) + (5×16^{-2})
 $(152A.25)_{16}$ = (1×4096) + (5×256) + (2×16) + (10×1) + (2×16^{-1}) + (5×16^{-2})
 $(152A.25)_{16}$ = 4096 + 1280 + 32 + 10 + $(2\times1/16)$ + $(5\times1/256)$
 $(152A.25)_{16}$ = 5418 + 0.125 + 0.125
 $(152A.25)_{16}$ = 5418.14453125_{10}

Hence, (152A.25)₁₆=(5418.14453125)₁₀

Table 2.3. Reference table for Hexadecimal to Decimal equivalent

Hexadecimal	Decimal (base 10)
(base 16)	Equivalent
0	0
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
A	10
В	11
С	12
D	13
Е	14
F	15

2.7 Octal to Decimal Conversion

The process starts from multiplying the digits of octal numbers with its corresponding positional weights. And lastly, we add all those products.

Example:

➤ Convert (152.25)₈ to decimal

Solution

First, multiply each digit of 152.25 with its respective positional weight, and then we add the products of all the bits with its weight.

$$(152.25)_8 = (1 \times 8^2) + (5 \times 8^1) + (2 \times 8^0) + (2 \times 8^{-1}) + (5 \times 8^{-2})$$

Hence, (152.25)₈=106.328125

2.8 Binary Subtraction using Complements (1s & 2s)

Binary Subtraction Using I's & 2's complement

L's complement :- To Calculate the 1's complement of a binary number just flip each value (bit) of the original binary no .

vie 0 -> 1 , 1 -> 0

EX 010111 1 1's complement 101000

2's complement ? - Just add '1' to 1's complement of that number.

Ex 010111 23 13 complement + 1 complement 101000 + 1

TO TOO T

Note: - ... C2's complement). is & 2's Complement on taken from for only -ve number.

SA Probe 51

SUBTRACTION USING 1's COMPLEMENT

Ex:- Perform (9)10- (4)10 using 1's complement 9-4Ex:- Perform (9)10- (4)10 using 1's complement 9-4[AKTU = 5]

(1001)₂ - (0100)₂

SUESTION).

```
Step 1: Take 1's complement of -ve number i.e (0100)
                                               7077
Step2: Then Add first number with is complement of second no.
  \frac{10001}{1000}
\frac{1011}{1000}
\frac{10001}{1000}
\frac{10001}{1000}
\frac{10001}{1000}
\frac{10001}{1000}
                  (0101) Ans. True form
Ex - Perform (1011001)2 - (1101010)2 using 1's complement
           (7707070)
                  I is complement.
             1010100
        Add both number
                             7077007
Step 2:
                   final [0] 1101110
         Invest all the bills.
                             7 70 7770
                           (00 10001), Ans
```

```
SUBTRACTION USING 2's COMPLEMENT
Ex: Perform following subtraction using 2's complement method.
        (1101)2 - (1001)2
Slaps: Take 2's complement of -ve number (1001)
                                          OI I O (1s complement)
             0110+1 (Add1)
                           2's complement: (0111)
Step2: Add both number (1101) 2. + (0111)2
                             7107
               téral carry. (1) 0.100 pm ûs tre & True form.
           binary subtraction using 2's complement method: -
EX
     Perform
                           (700,17) 3 - (7107) 5
     1. (01101) 2 -> 2's complement. (Make both number of equal digits)
          70010+7 -> TDOTT (VggT)
     2. Add both whe no. 10011
```

final [1 00110] ens is tre (6)

3. Basic and Universal Logic Gates

3.1 Introduction

Logic gates are the basic building blocks of any digital system. It is an electronic circuit having one or more than one input and only one output. The relationship between the input and the output is based on certain logic. Based on this, logic gates are named as AND gate, OR gate, NOT gate etc. Moreover, NAND and NOR gates are called universal logic gates.

3.2 Logic Addition

The possible input and output combinations may be arranged as follows:

$$0 + 0 = 0$$

 $0 + 1 = 1$
 $1 + 0 = 1$

$$1 + 1 = 1$$

3.3 Logical Multiplication

We can define the "•" (logical multiplication) symbol or AND operator by listing all possible combinations for (input) variables let say X and Y and the resulting (output) value of X•Y will be as follows:

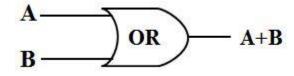
$$0.0 = 0$$
 $0.1 = 0$
 $1.0 = 0$
 $1.1 = 1$

3.4 Logic Gates

- A logic gate is defined as an electronics circuit with two or more input signals and one output signal.
- AND, OR & NOT gates are called basic logic gates.
- NAND and NOR gates are called universal logic gates.

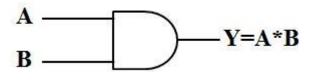
3.4.1 OR Gate

Inp	Output	
A	В	Y= A*B
0	0	0
0	1	1
1	0	1
1	1	1



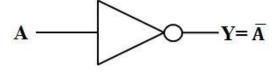
3.4.2 AND Gate

Inp	Output	
A	В	Y= A*B
0	0	0
0	1	0
1	0	0
1	1	1



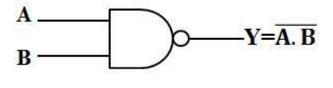
3.4.3 NOT Gate

Input	Output
A	Y
0	1
1	0



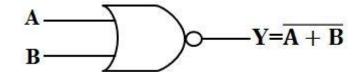
3.4.4 NAND Gate

Inp	Output	
A	В	Y=A.B
0	0	1
0	1	1
1	0	1
1	1	0



3.4.5 NOR Gate

Inp	Output	
A	В	$Y = \overline{A + B}$
0	0	1
0	1	0
1	0	0
1	1	0



3.4.6 EX-OR Gate

Inp	Output	
A	В	Y=A⊕B
0	0	0
0	1	1
1	0	1
1	1	0

$$\begin{array}{c}
A \longrightarrow \\
B \longrightarrow \\
\hline
\end{array}$$

$$Y = A \oplus B = \overline{A}B + A\overline{B}$$

3.4.7 EX-NOR Gate

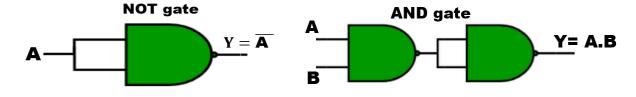
Inp	Inputs Ou		$\mathbf{A} \longrightarrow \mathbf{Y} = \overline{\mathbf{A} \oplus \mathbf{B}} = \mathbf{A} \cdot \mathbf{B} + \overline{\mathbf{A}} \cdot \overline{\mathbf{B}}$
A	В	Y= A⊕B) EXNOR O
0	0	1	B ———//
0	1	0	
1	0	0	
1	1	1	

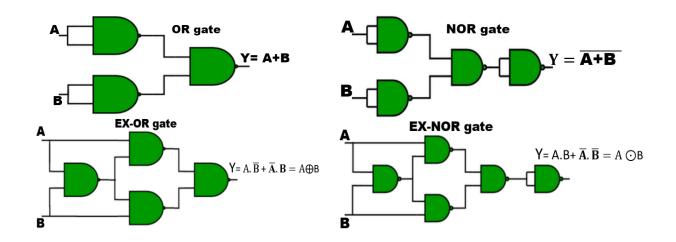
3.5 Realization of Logic Gate Using Universal gates

In Boolean Algebra, the **NAND** and **NOR** gates are called **universal gates** because any digital circuit can be implemented by using any one of these two i.e., any logic gate can be created using NAND or NOR gates only.

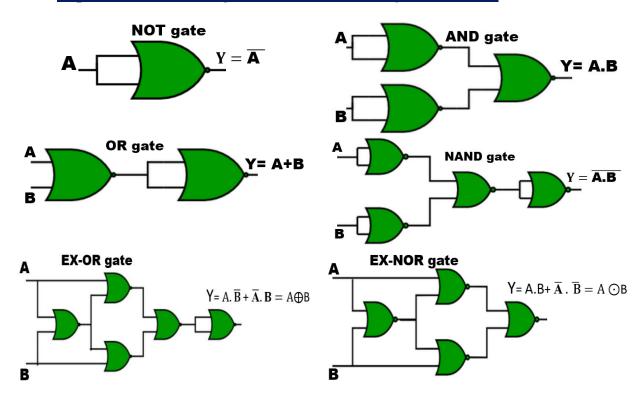
3.5.1 Implementation of NOT, AND, OR, NOR, NAND, EX-OR, & EX-NOR Gate Using Universal gates

A. Implementation Using Minimum Number of NAND Gates





B. Implementation Using Minimum Number of NOR Gates



4. Boolean Algebra Simplification of Boolean Function

4.1 Boolean Algebra

An algebra which deals with the binary number system called Boolean Algebra. Here, we use logic gate concept i.e.

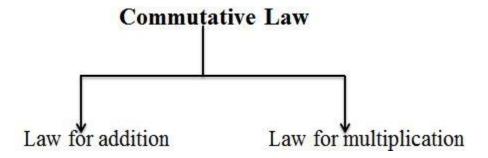
'+' Addition OR Gate

Multiplication → AND Gate

4.2 Laws of Boolean Algebra

- ➤ Commutative Law
- ➤ Associative Law
- > Distributive Law

> Commutative Law



a) Law for addition

It is independent of order, this means if we change the sequence of variables, result will be same i.e.

b) Law for multiplication

It is also independent of order. It means changing variable sequence while AND operation, the final result will not affect i.e.

$$A.B = B.A$$

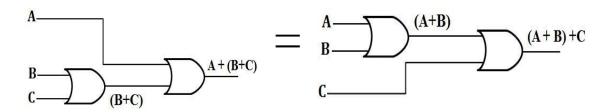


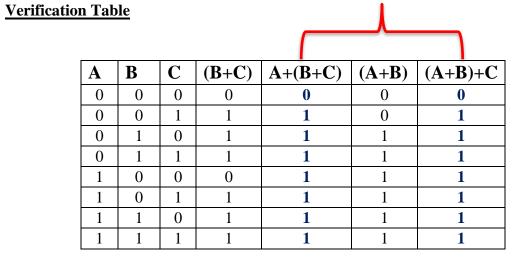
> Associative Law

a) Law for addition

Associative law for addition can be justified as follows:

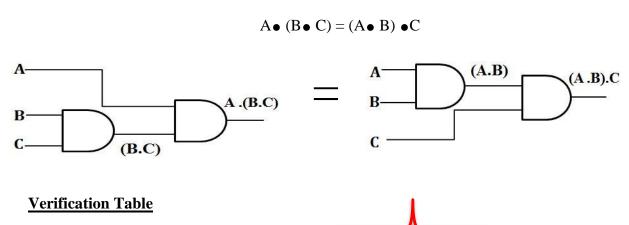
$$A + (B+C) = (A+B) + C$$





b) Law for multiplication

Associative law for multiplication can be justified as follows:



A	В	C	(B.C)	A. (B. C)	(A.B)	(A.B).C
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	0	0	0
0	1	1	1	0	0	0
1	0	0	0	0	0	0
1	0	1	0	0	0	0
1	1	0	0	0	1	0
1	1	1	1	1	1	1

> Distributive Law

a) Law using AND operator

Distributive law using AND operator can be justified as follows:

$$A \bullet (B+C) = A \bullet B + A \bullet C$$

$$\begin{array}{c}
A \\
B \\
C
\end{array}$$

$$\begin{array}{c}
A \\
A \\
C
\end{array}$$

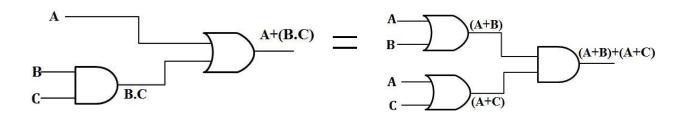
Verification Table

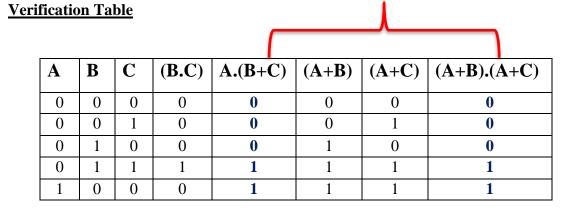
A	В	C	(B + C)	A. (B+C)	(A.B)	(A.C)	(A.B)+(A.C)
0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0
0	1	0	1	0	0	0	0
0	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0
1	0	1	1	1	0	1	1
1	1	0	1	1	1	0	1
1	1	1	1	1	1	1	1

b) Law using OR operator

Distributive law using OR operator can be justified as follows:

$$A + (B \bullet C) = (A+B) \bullet (A+C)$$





1	0	1	0	1	1	1	1
1	1	0	0	1	1	1	1
1	1	1	1	1	1	1	1

4.3 Duality in Boolean Algebra

Starting with a Boolean relation, we can derive another Boolean relation by-

- i. Changing each OR (+) sign to an AND (.) sign
- ii. Changing each AND (.) sign to an OR (+) sign.
- iii. Complementary each 0 and 1

For example:

$$A + 0 = A$$

The dual relation is $A \cdot 1 = A$

Since, A(B + C) = AB + AC (by distributive law).

Its dual relation is A + B C = (A + B) (A + C)

4.4 De Morgan's Theorem

This theorem can be justified as follows:

a)
$$\overline{A + B} = \overline{A} \cdot \overline{B}$$

b)
$$\overline{A \cdot B} = \overline{A} + \overline{B}$$

4.5 Boolean Algebra Theorems

Name	AND Form	OR Form	
Identity Law	A.1 = A	A+0=A	
Null Law	A.0 = 0	A+1 = 1	
Idempotent Law	A.A = A	A+A=A	
Inverse Law	$A. \overline{A} = 0$	$A+\overline{A}=1$	
Commutative Law	AB=BA	A+B=B+A	
Associative Law	(AB)C = A(BC)	(A+B)+C=A+(B+C)	
Distributive Law	A+BC=(A+B)(A+C)	A(B+C) = AB+AC	
Absorption Law	A(A+B)=A	A+AB=A	
De Morgan's Law	$\overline{A.B} = \overline{A} + \overline{B}$	$\overline{A + B} = \overline{A}.\overline{B}$	

4.6 Simplification of Boolean expressions

In this section, we will discuss some working examples to simplify different Boolean expressions.

Example 1:

Simplify the Boolean expression $AB\overline{C}\overline{D} + AB\overline{C}D + ABC\overline{D} + ABCD$.

Solution:

Given, $AB\overline{C}\overline{D} + AB\overline{C}D + ABC\overline{D} + ABCD$

 \Longrightarrow AB $\overline{C}(\overline{D} + D) + ABC(\overline{D} + D)$

 \implies AB \overline{C} . 1 + ABC. 1; (As we know D + \overline{D} = 1)

 \implies AB \overline{C} + ABC

 \implies AB($\overline{C} + C$)

Hence, the answer is AB

Example 2:

Simplify the Boolean expression AB(\overline{A} B \overline{C} + A \overline{B} \overline{C} + \overline{A} B C).

Solution:

Given, $AB(\overline{A} B \overline{C} + A \overline{B} \overline{C} + \overline{A} B C)$

 \Longrightarrow A \overline{A} B B \overline{C} + A A B \overline{B} \overline{C} + \overline{A} A B B C

 $\Longrightarrow 0 + 0 + 0$; (As we know A $\overline{A} = 0$)

Hence, the final answer is 0

Example 3:

Simplify the Boolean expression ($\overline{A} \overline{B} + A B + \overline{A} B$).

Solution:

Given,
$$(\overline{A} \overline{B} + A B + \overline{A} B)$$

$$\Longrightarrow \overline{A} \overline{B} + B(A + \overline{A})$$

$$\Longrightarrow \overline{A} \overline{B} + B.1$$

$$\Longrightarrow B + \overline{A} \ \overline{B}$$

$$\Longrightarrow (B + \overline{A})(B + \overline{B})$$

$$\Longrightarrow (B + \overline{A}).1$$

Hence, the answer is $\overline{\mathbf{A}} + \mathbf{B}$

Example 4:

Simplify the Boolean expression $(A + B)(A + \overline{B})$.

Solution:

Given,
$$(A + B)(A + \overline{B})$$

$$\implies$$
 A. A + A. \overline{B} + A. B + B. \overline{B}

$$\Longrightarrow$$
 A. A + A(\overline{B} + B) + 0

$$\Longrightarrow$$
 A. A + A(\overline{B} + B)

$$\implies$$
 A + A

Hence, the answer is A

Example 5:

Simplify the Boolean expression AB + A(C D + C \overline{D}).

Solution:

Given,
$$AB + A(CD + C\overline{D})$$

$$\Longrightarrow$$
 AB + AC(D + \overline{D})

$$\Longrightarrow$$
 AB + AC. (1)

Hence, the answer is A(B + C)

Example 6:

Simplify the Boolean expression $(\overline{B} \overline{C} + \overline{A} D)(\overline{A} \overline{B} + \overline{C} \overline{D})$.

Solution:

Given,
$$(B\overline{C} + \overline{A}D)(A\overline{B} + C\overline{D})$$

$$B \overline{C} A \overline{B} + B \overline{C} C \overline{D} + \overline{A} D A \overline{B} + \overline{A} D C \overline{D}$$

$$B \overline{B} \overline{C} A + \overline{C} C B \overline{D} + \overline{A} A D \overline{B} + \overline{A} C D \overline{D}$$

$$0 + 0 + 0 + 0 = 0$$

Hence, the answer is 0

4.7 SOP and POS Representation for Logic Expressions

Sum of products (SOP) form! -

It the boolean function f = AB + AC + BCSum

This in the forms of Sum of three larms

AB, Ac and BC with lead individual Sum of products (Sop) from

terms is product of two variables

therefore, such expressions are known as expression in soften. The sums and products in the soft forms are not the actual addition or multiplication. In fact, they are the OR and AND functions

Some more examples of sop forms! -

(i) fi = ABC + BCD + ABD

(ii) F2 = xy + xy + xy

(iii) f2= PQ+ PQR+ PQR

Products of Sum (pos) form! -

Let the boolean fundion $f = (A+B) \cdot (B+C) \cdot (A+C)$ St is in the ferm of product

of three terms $(A+B) \cdot (B+C)$ products of Sum (pos) form

and (A+C) with each terms

is in the form of sum of two variables.

some more exp. examples of pos forms: -

Cononical or standard form! -

Elach Sumborn consists of
all the literals in the complimental or uncomplimental
form.

Some more examples of standard or canonical sopemd pos form

legical expression

1. f = AB + ABC + ABC

2. f = AB + AB + AB

3. f = (A+B) (A+B) (A+B)

4. f = (A+B) (A+B+C)

5. f (A,B,C) = ABC + ABC + ABC

6. f (A,B,C) = BC + ABC + ABC

Minterm and Maxterm for three variables:

Minterm

Maxterm

Maxt

			Minterin	1	Maxa	
N	y	Z	Term	Designation	Term	ausenālion
0	0	0	xyz	mo	xtytz	morsignation
0	O	1	XYZ	ml	(x+y+z)	LUI
0	1	0	えりて	m2	(x+ y+z)	M2
0	1	1	x yz	m 3	$(\chi + \sqrt{y} + \overline{z})$	M3
1	0	0	nýž	my	(x+y+z)	My
1	0	1	nyz	ms	(x+y+z)	MS
1	1	0	xyz	m6	(x+y+z)	MG
1	1	1	nyz	m7	(え+ダナモ)	MI

Ex! Express the given table in minterm (SOP) and Martern (POS Dicimal oc F where autput (F) is L, these o we minterm (SOP) 0 1 F= xyz+nyz+nyz 0 & f = m1 + m4 + m7 f= Em & 1, 4, 7} 0 where output (F) is 0, these 6 1 1 0 are maxterm (POS) 1 71 1 1 F= (x+y+z)(x+y+z)(x+y+z) (x+y+z) (x+y+z) F = Mo. M2, M3. M5. M6 or F= TTM 20, 2, 3, 5,6%

Steps to convert Non Standard form to Standard form! -

1. Find missing literals in each terms.

2. And each product term having missing literals like (x+E)

3. Expand the terms by distributive law.

4. Write the repeatitive terms only once.

Ex: convert the given expression in standard form (SOP) F(A,B,C) = AC + AB + BC $Steps.1 Missing literals <math>B \subset A$ Step.2 F(A,B,C) = AC(B+B) + AB(C+C) + BC(A+A) = ABC + ABC + ABC + ABC + ABC + ABC F(A,B,C) = ABC + ABC + ABC + ABC + ABC Standard (SOP)

```
too bos toom; -
       Step ! Find the missing literaly
       Step . 2 Add that one with the required term as x.x
       Step 3 Expand the expression.
       Step 4 write the repeatitive terms once.
Ex: convert the given expression into the standard posform
       F(A,B,C) = (A+B) (B+C) (C+A)
        missing literal &
 Add the missing literal = (A+B+CO) (B+C+AA) (C+A+BB)
       F(A, B, c) = (A+B+C). (A+B+E). (B+C+A) (B+C+A)
                    (CTA+B) (CHA+B)
     F(A,B,C) = (A+B+C) (A+B+C) (A+B+C) (A+B+C)
                                    Standard pos form.
conversion sop to pos and pos to sop:
   SOP F = Em 20, 1, 2,5}
   POS F = Tm & 3,4,6,7}
Conversion from pos to sop; -
Ex: convert F2 from pos to sop function.
        f2 = x (y+z) (x+y+z)
 Pake compliment
          F2 = n(y+z)(x+y+z)
          \bar{F}_2 = \bar{\chi} + (y \cdot \bar{z}) + (\chi y Z)
      F3 = F2 = x + yz + nyz | sop representation of F2
Ex! convert the function form pos to sof.
            F= TTM 20, 6, 73
   Take Compliment F = [ Tm 20, 6, 73]
               f = Zm (1,2,3,4,5) sop form.
```

Conversion from sop to pos:

Ex! convert the following function from sop to pos $F_1 = y + ny + \overline{n} y \overline{z}$ take Compliment of Fi $F_2 = \overline{F_1} = (y + ny + \overline{n} y \overline{z})$ $F_2 = \overline{F_1} = y \cdot (\overline{n} + \overline{y}) \cdot (x + \overline{y} + \overline{z})$ pos representation of Fi

For Sop $F_1 = y \cdot (\overline{n} + \overline{y}) \cdot (x + \overline{y} + \overline{z})$ pos (or-AND) left $\overline{z} = sop(AND - or)$ lift diagram $\overline{z} = sop(AND - or)$ lift diagram $\overline{z} = sop(AND - or)$ lift diagram

5. Karnaugh map (K-Map)

5.1 Introduction

Karnaugh map or K-map is a map of a function used in a technique used for minimization or simplification of a Boolean expression. It results in less number of logic gates and inputs to be used during the fabrication.

5.2 Rules of Minimization in K-Map

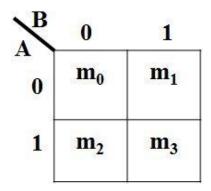
- While grouping, you can make groups of 2ⁿ number where n=0, 1, 2, 3...
- You can either make groups of 1's or 0's but not both.
- Grouping of 1's lead to Sum of Product form and Grouping of 0's lead to product of Sum form.
- While grouping, the groups of 1's should not contain any 0 and the group of 0's should not contain any 1.'
- Groups can be made vertically and horizontally but not diagonally.
- Groups made should be as large as possible even if they overlap.
- All the like term should be in a group even if they overlap.
- Uppermost & lowermost squares can be made into a group together as they are adjacent (1-bit difference). Same goes for the corner squares.
- Each group represents a term in the Boolean expression. Larger the group, smaller and simple the term.
- The product of those literals that remains unchanged in a single group makes the term of the expression.
- Don't care "x" should also be included while grouping to make a larger possible group.

5.3 2-Variable K-Map

2 variables have $2^n = 2^2 = 4$ minterms. Therefore, there are 4 cells (squares) in 2 variable K-map for each minterm.

Consider A & B as two variables.

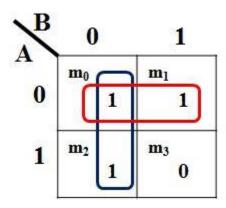
Grouping in 2 variables K-map is easy as there are few squares shown below



Example of 2 Variable K-Map-

$$\mathbf{F}=\sum{(m_0,\,m_1,\,m_2)}=\overline{A}\overline{B}+\!\overline{A}B+\!A\overline{B}$$

Now, K-map from Truth table



Hence, the simplifies expression will be the sum of these two terms as given below,

$$\mathbf{F} = \overline{\mathbf{A}} + \overline{\mathbf{B}}$$

5.4 3-Variable K-Map

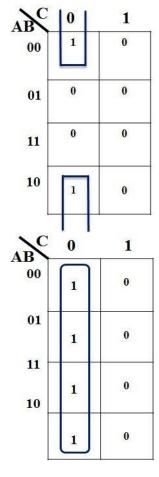
3 variables make $2^n=2^3=8$ min terms, so the K- map of 3 variables will have 8 squares(cells) as shown in the figure given below.

0	1
\mathbf{m}_0	\mathbf{m}_1
m ₂	m ₃
m ₆	m ₇
m ₄	m ₅
	m ₀ m ₂ m ₆

3C	00	01	11	10
	\mathbf{m}_0	\mathbf{m}_1	m ₃	m ₂
	m ₄	m ₅	m ₇	m ₆

Moreover, we can make groups of 2, 4 & 8 cells having same 1s or 0s.

ABC	0	1
00	1	0
01	1	0
11	0	1
10	0	1
ABC	0	1
		0
00	0	0
00	1	1
00		
01	1	1

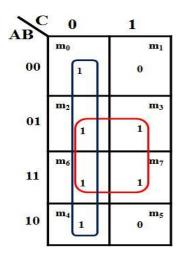


Example of 3 Variable K-Map:

Simplify the given function using K-map.

$$F(A, B, C) = \sum (m_0, m_2, m_3, m_4, m_6, m_7)$$

Solution:



The sum of these two terms will make the simplified expression of the function as given below.

$$\mathbf{F} = \mathbf{B} + \overline{\mathbf{C}}$$

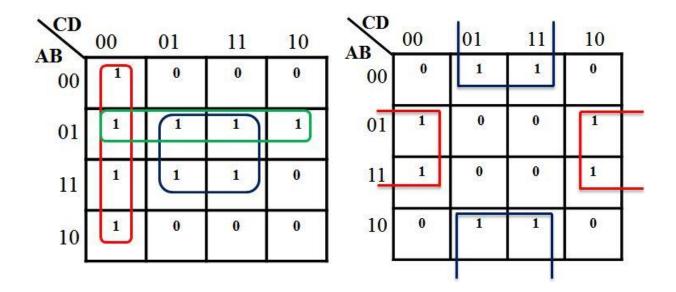
5.5 4-Variable K-Map

Four variables have $2^n=2^4=16$ minterms. So, a 4-variable k-map will have 16 cells as shown in the figure given below.

AB	00	01	11	10
00	\mathbf{m}_0	m ₁	m ₃	m ₂
01	m ₄	m ₅	m ₇	m ₆
11	m ₁₂	m ₁₃	m ₁₅	m ₁₄
10	m ₈	m ₉	m ₁₁	m ₁₀

Each cell (min term) represents the variables in front of the corresponding row & column.

Some examples of grouping in 4-variable k-map is given below:

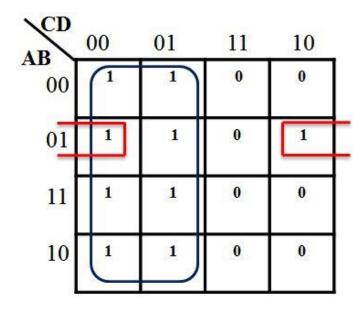


Example of 4 Variable K-Map:

Simplify the given function using K-map.

$$F(A,B,C,D) = \sum (m_0, m_1, m_4, m_5, m_6, m_8, m_9, m_{12}, m_{13})$$

Solution:



Group of 8 will give a term of 1 literal that remains unchanged i.e. $\overline{\boldsymbol{C}}$

So the expression will $F = \overline{C} + \overline{A}B\overline{D}$

5.6 5-Variables K-Map

5 variables have 32 min terms, which mean 5 variable k- map has 32 squares (cells).

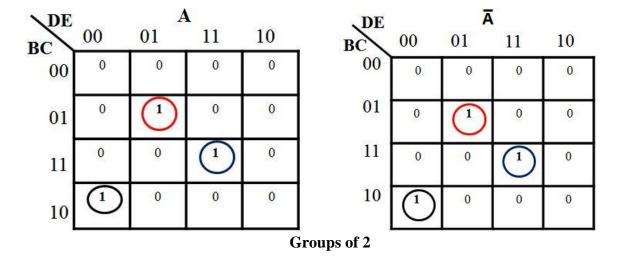
A							
01	11	10	BC DE	00	01	. 11	10
n ₁ r	n ₃	m ₂	00	m ₁₆	m ₁₇	m ₁₉	m ₁₈
n ₅ n	n ₇ n	n ₆	01	m ₂₀	m ₂₁	m ₂₃	m ₂₂
m ₁₃ n	n ₁₅ n	n ₁₄	11	m ₂₈	m ₂₉	m ₃₁	m ₃₀
m ₉ n	n ₁₁ n	n ₁₀	10	m ₂₄	m ₂₅	m ₂₇	m ₂₆
	n ₁ n n ₅ n m ₁₃ n	m ₁ m ₃ m ₅ m ₇ nm ₁₃ m ₁₅ n	m ₁ m ₃ m ₂ m ₅ m ₇ m ₆ m ₁₃ m ₁₅ m ₁₄	m_1 m_3 m_2 m_3 m_4 m_5 m_6 m_{13} m_{15} m_{14} m_{14} m_{15} m_{14} m_{15} m_{14} m_{15} m_{14} m_{15} m_{15} m_{14} m_{15}	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$egin{array}{cccccccccccccccccccccccccccccccccccc$	$egin{array}{cccccccccccccccccccccccccccccccccccc$

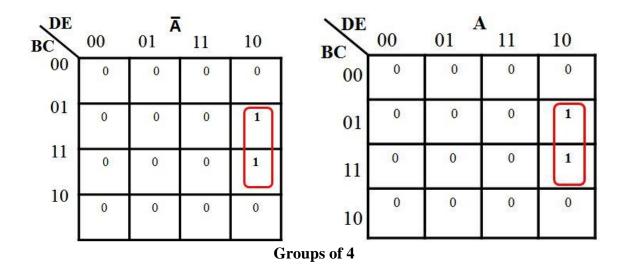
A 5-variable K-map is made using two 4-variable K-maps. Consider 5 variables A,B,C,D,E and their 5 variable K-map is given below.

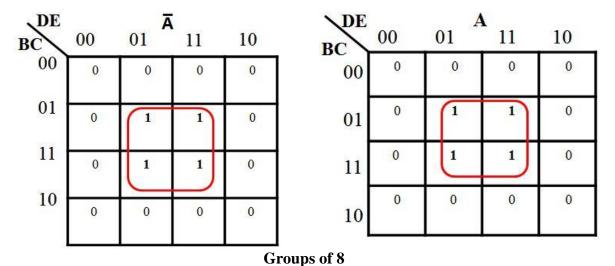
These both 4-variable Karnaugh map together represents a 5-variable K-map for variable A,B,C,D,E. Notice variable A over the top of each 4-variable K-map. For A=0, the left K-map is selected and right map for A=1.

Each corresponding squares (cells) of these two 4-variable K-maps are adjacent. Visualize these both K-maps on top of each other. m_0 is adjacent to m_{16} , so is m_1 to m_{17} so on until the last square.

The rule (method) of grouping is same for each of the 4-variable k-maps. However, you also need to check the corresponding cells in both K-maps as well. A few example of grouping is given below.





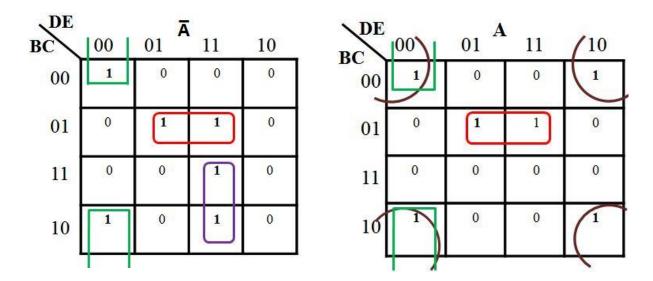


Example of 5 Variables K-Map

Simplify the given function using K-map.

 $F\left(A,\,B,\!C,\!D,\!E\right) \,=\, \sum \,\left(\,\,m_{0},\,m_{2},\,m_{5},\,m_{7},\,m_{8},\,m_{10},\,m_{16},\,m_{21},\,m_{23},\,m_{24},\,m_{27},\,m_{31}\,\right)$

Solution:



This is the 5-variable k-map for the function given above. There are four groups made in this K-map. Each group has a different color to differentiate between them.

The red color group is a group of 4 min terms made between both 4-variable k-maps because they are adjacent cells and it overlaps the green group.

The yellow group is also a group of 4 min terms made between adjacent cells of the 4-variable kmaps.

The green group is a group of 4 min terms made in the left 4-variable k-map. The blue group is of 2 min-terms made in the right 4-variable k-map because there are no common adjacent cells in the other k-map.

Green color group of 4 min term will produce the term $\overline{A}\overline{C}\overline{E}$. The individual 4-variable K-map will produce $\overline{C}\overline{E}$ as they are not changing in the group but variable A should also be taken into account because this individual 4-variable k-map is being represented by \overline{A} .

The red color group will produce $\overline{C}\overline{D}\overline{E}$. This group is made between both K-maps which means variable A changes and in individual K-map, B changes so these both variables will be eliminated from the term. Only $\overline{C}\overline{D}\overline{E}$ remains unchanged in this group.

The yellow group will produce $\overline{B}CE$ because these literals are not changing in this group.

Blue group of 2 min terms will produce the term ABDE as they remain unchanged in this group.

The simplified expression will be the sum of these 4 terms, which is given below:

$$\mathbf{F} = \overline{\mathbf{A}}\overline{\mathbf{C}}\overline{\mathbf{E}} + \overline{\mathbf{C}}\overline{\mathbf{D}}\overline{\mathbf{E}} + \overline{\mathbf{B}}\mathbf{C}\mathbf{E} + \mathbf{A}\mathbf{B}\mathbf{D}\mathbf{E}$$

5.7 6-Variable K-Map

6-variable k-map is a complex k-map which can be drawn. Visualizing 6-variable k-map is a little bit tricky.

6 variables make 64 min terms, this means that the k-map of 6 variables will have 64 cells. Its geometry becomes difficult to draw as these cells are adjacent to each other in all direction in 3-dimensions i.e. a cell is adjacent to upper, lower, left, right, front and back cells at the same time. We can draw it like 5-variable k-map as shown in the figure below.

EF			B	
CD	00	01	11	10
00	\mathbf{m}_0	\mathbf{m}_1	m ₃	m ₂
01 Ā	m ₄	m ₅	m ₇	m ₆
11	m ₁₂	m ₁₃	m ₁₅	m ₁₄
10	m ₈	m ₉	m ₁₁	m ₁₀

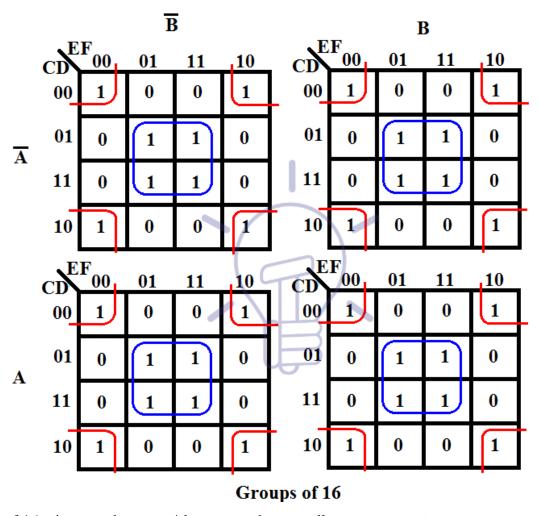
EF			В	
CD	00	01	11	10
00	m ₁₆	m ₁₇	m ₁₉	m ₁₈
01	m ₂₀	m ₂₁	m ₂₃	m ₂₂
11	m ₂₈	m ₂₉	m ₃₁	m ₃₀
10	m ₂₄	m ₂₅	m ₂₇	m ₂₆
			1161	

00	01	11	10
m ₃₂	m ₃₃	m ₃₅	m ₃₄
m ₃₆	m ₃₇	m ₃₉	m ₃₈
m ₄₄	m ₄₅	m ₄₇	m ₄₆
m ₄₀	m ₄₁	m ₄₃	m ₄₂
	m ₃₂ m ₃₆ m ₄₄	m ₃₂ m ₃₃ m ₃₆ m ₃₇ m ₄₄ m ₄₅	m ₃₂ m ₃₃ m ₃₅ m ₃₆ m ₃₇ m ₃₉ m ₄₄ m ₄₅ m ₄₇

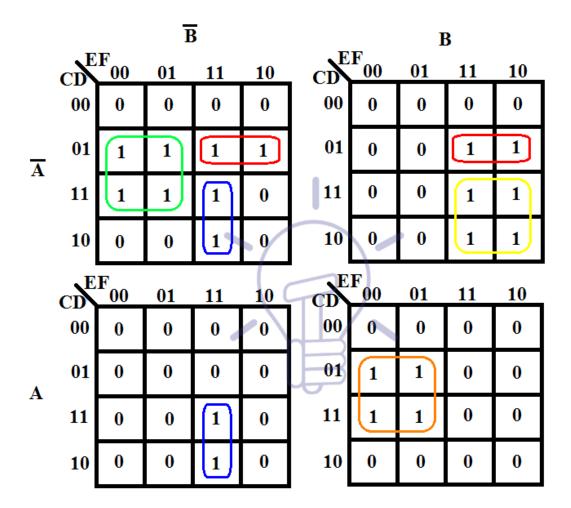
CD	00	01	11	10
00	m ₄₈	m ₄₉	m ₅₁	m ₅₀
01	m ₅₂	m ₅₃	m ₅₅	m ₅₄
11	m ₆₀	m ₆₁	m ₆₃	m ₆₂
10	m ₅₆	m ₅₇	m ₅₉	m ₅₈

The 6-variable k-map is made from 4-variable 4 k-maps. As you can see variable A on the left side select 2 k-maps row-wise between these 4 k-maps. A=0 for the upper two K-maps and A=1 for the lower two K-maps. Variable B on top of these K-maps select 2 k-maps column-wise. B=0 for left 2 K-maps and B=1 for right 2 K-maps.

Some examples of grouping in 6-variable K-map are given below.



Group of 16 min-terms between 4 k-maps as they are all adjacent. Visualize these k-maps on top of each other.



In the above example, there are 5 groups of 4 min-terms. Notice the min-terms in the diagonal K-maps, they make a separate group because these K-maps are not adjacent.

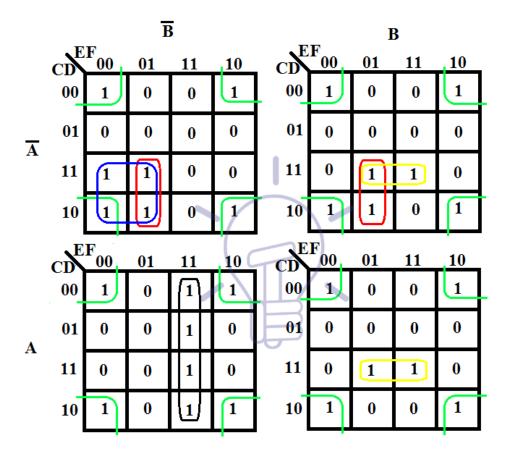
Example of 6 Variable K-Map maping:

Simplify the given function using K-map.

 $F = \sum \left(\ m_0, \ m_2, \ m_8, \ m_9, \ m_{10}, \ m_{12}, \ m_{13}, \ m_{16}, \ m_{18}, \ m_{24}, \ m_{25}, \ m_{26}, \ m_{29}, \ m_{31}, \ m_{32}, \ m_{34}, \ m_{35}, \ m_{39}, \\ m_{40}, \ m_{42}, \ m_{43}, \ m_{47}, \ m_{48}, \ m_{50}, \ m_{56}, \ m_{58}, \ m_{61}, \ m_{63} \right)$

Solution:

Its 6 variable K-map is given below-



There are 5 groups (discussed below) in this K-map each colored different.

- Green group is made of 16 min-terms between all 4 individual K-maps. In this group, AB keeps changing so they will be eliminated from the term. C & E are also changing so they will be eliminated from the term too. So the term will become \overline{DF} because they remain unchanged throughout the group.
- Red group is made of 4 min-terms. In this group, B is changing so it will be eliminated. D
 is also changing. So the only remaining unchanged literals will make the term which is
 ACEF.
- Blue group is also made of 4 min-terms. The only changing variables are DF throughout this group so they will be eliminated from the term. The non-changed literal in this group are \overline{ABCE} which will be the term produced by this group.
- The Yellow group is also a group of 4 min-terms and the changing variables in this group are AE. The literal that remains unchanged are BCDF in this group.

• The black group is of 4 min-terms too. This group produces the term ABEF because they are the unchanged literals in this group.

Hence, the simplified expression of the function will be the sum of these 5 terms from these groups. The expression is given below:

$$\mathbf{F} = \overline{\mathbf{D}}\overline{\mathbf{F}} + \overline{\mathbf{A}}\mathbf{C}\overline{\mathbf{E}}\mathbf{F} + \overline{\mathbf{A}}\overline{\mathbf{B}}\mathbf{C}\overline{\mathbf{E}} + \mathbf{B}\mathbf{C}\mathbf{D}\mathbf{F} + \mathbf{A}\overline{\mathbf{B}}\mathbf{E}\mathbf{F}$$

UNIT-4

Short Type Questions (AKTU)

- 1. What is a Logic gate?
- 2. What is meant by a bit, pair, and quad?
- 3. What are the various logic gates, give the representation along with the truth table.
- 4. What is DeMorgan's theorem?
- 5. Define minterms and maxterms.
- 6. What are universal gates? Why are they called so?
- 7. Draw the circuit of a 2 input EX-OR gate using 2 input NAND gates.
- 8. What is the use of don't care combinations? Is it an advantage or disadvantage to include them in a map?
- 9. Distinguish between SOP and POS.
- 10. What is the difference between canonical form and standard form.

Long Type Questions (AKTU)

- 1. Convert the following to the corresponding bases
 - i. $(9BCD)_{16} = ()_8$
 - ii. $(323)_4 = ()_5$
 - iii. $(53.625)_{10} = ()_2$
 - iv. $(3FD)_{16} = ()_2$
 - v. $(A69.8)_{16} = ()_{10}$
 - vi. $(246.8)_{10} = ()_2, ()_8 & ()_{16}$
- 2. Perform Binary subtraction of the following:
 - i. $(1011001)_2 (1101010)_2$ using 1's complement
 - ii. $(1101)_2 (1001)_2$ using 2's complement
- 3. Simplify the following logic expression using Boolean Algebra:
 - i. F = AB + A(B+C) + B(B+C)
 - ii. F = AB'C'D + A'B'D + BCD' + A'B + BC'
- 4. Consider the following Boolean expression:
 - i. F(A, B, C) = A + AB + ABC, convert it into canonical SOP form
 - ii. F(A, B, C) = (A+B)(B+C)(C+A), convert it into canonical POS form
- 5. Using K map method, determine the minimal SOP expression for the following using decimal notation $F=\Sigma$ m (1,4,7,9,12,14) + Σ d (2,13). Also, implement using logic gates.
- 6. Simplify the expression $F(a,b,c,d,e)=\sum m(1,2,4,7,12,14,15,24,27,29,30,31)$ using K-map. Also, implement using logic gates.
- 7. Simplify the following function using K map:
 - $F(A, B, C, D) = \sum m(1, 3, 4, 5, 6, 7, 9, 11, 13, 15)$. Also, implement the simplified function using basic gates only.
- 8. Minimize using K-map and realize output using gates.
 - $F(A, B, C, D) = \Sigma m (1, 4, 8, 12, 13, 15) + d (3, 14)$
- 9. Reduce the following function using K-Map.

 $F(A, B, C, D, E) = \Sigma m (1,4,8,10,11,20,22,24,25,26) + d (0,12,16,17)$

- 10. Simplify following logic function using K Map and realize using NOR gates.
 - i. $f(w,x,y,z)=\pi M(1,2,3,7,10,11)$
 - ii. $f(w,x,y,z) = \pi M (3,4,5,6,7,10,11,15)$
- 11. Simplify the following function with the help of K map:

 $F(A, B, C, D) = \Sigma m (3,5,9,11,15) + d (2,4,6,10)$

12. Minimize the following using K-map:

F(A, B, C, D) = AB'C' + A'BC + A'B'CD + ABCD + d(1,5)

- 13. Implement EX-OR and EX-NOR gates using minimum NAND gates
- 14. Implement EX-OR and EX-NOR gates using minimum NOR gates.
- 15. Define Universal Gates. Implement AND, OR, NOR by using NAND gates only.

Previous Year asked AKTU Questions and their solutions

1. Convert the following to the corresponding bases

```
vii. (73.12)_{10} = ()_2
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viii.
$$(110110.011)_2 = ()_{16}$$

ix.
$$(231.36)_{10}=()_2$$

$$x.$$
 (11011.10)₂=()₁₀

xi.
$$(6FB.67)_{16}=()_{10}$$

xii.
$$(437)_8 = ()_{10}$$

xiii.
$$(1110111.11011)_2 = ()_8$$

xiv.
$$(1011.10110)_2 = ()_{16}$$

xv.
$$(743.15)_8 = ()_2$$

xvi.
$$(9AC.1B)_{16}=()_2$$

Solution:

i.

ii.

$$36 \times 2 = |0.72$$

 $72 \times 2 = |0.72$
 $44 \times 2 = |0.80$
 $80 \times 2 = |1.76$
 $(36)_{10} = (0.00)_{---}$

iv.

 \mathbf{v}_{ullet}

$$= \frac{16^{2} \text{ k}^{1} \text{ k}^{6} \text{ k}^{1} \text{ k}^{6}}{6 \text{ FB} \cdot 6 \text{ 7}}$$

$$= \frac{6 \text{ FB} \cdot 6 \text{ 7}}{(15)(11)}$$

$$= \frac{6 \times 16^{2} + 15 \times 16^{1} + 11 \times 16^{0} + 6 \times 16^{-1} + 7 \times 16^{-2}}{15 \times 16^{1} + 15 \times 16^{1} + 11 \times 16^{0} + 6 \times 16^{-1} + 7 \times 16^{-2}}$$

$$= 1536 + 240 + 11 + 375 + 0273$$

$$= (1707 - 4023) 10$$

vi.

$$= 4 \times 0^{.2} + 3 \times 0^{1} + 7 \times 0^{6}$$

vii.

$$\frac{(1011.10110)_{2}}{1011.0000} = (?)16$$

$$\frac{1011.10110000}{8421} = (3.80)16$$

ix.

X.

2. Simplify the following logic expression using Boolean Algebra:

iii.
$$F=(A+C)(AD+A\overline{D})+AC+C$$

iv.
$$F = ((A.B)' + C)'$$
. B

Solution:

i.

$$(A+C) A(D+\overline{D}) + C[A+I].$$

$$= (A+C) A(1) + C(\cdot 1)$$

$$= AA + AC + C$$

$$= A + C[A+I]$$

$$= A + C$$

ii.

3. Consider the following Boolean expression:

iii. $F(A, B, C) = \overline{A} + B\overline{C} + \overline{A}BC$, convert it into standard SOP form

iv. $F(A, B, C) = A.(\overline{B} + \overline{C}). (A + \overline{B} + \overline{C}),$ convert it into standard POS form

v. $F(A, B, C) = (A+\overline{B})$. (B+C), convert it into standard POS form

Solution

ī

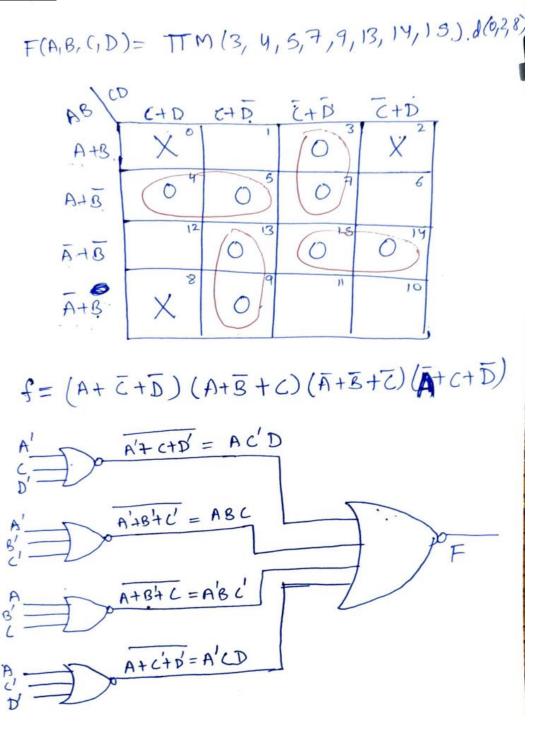
$$F(A, b, c) = A + BC + \overline{A}BC$$

$$= A(B+\overline{b})(C+\overline{c}) + BC(A+\overline{A}) + \overline{A}BC$$

$$= ABC + ABC + \overline{ABC} + \overline{$$

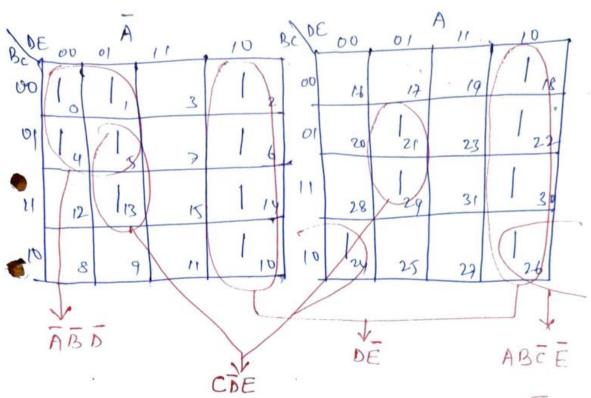
4. Using K-map method, determine the minimal POS expression for the following using decimal notation F (A, B, C, D) = π M (3,4,5,7,9,13,14,15). d (0, 2, 8). Also, implement using NOR gates only.

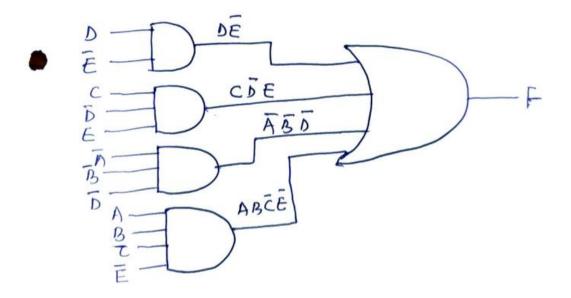
Solution



K-map. Also, imple	ssion $F(a,b,c,d,e) = \sum m(0,1)$ ment using logic gates.	,2,4,5,6,10,13,14,18,21,	.22,24,26,29,30) usi
Solution			

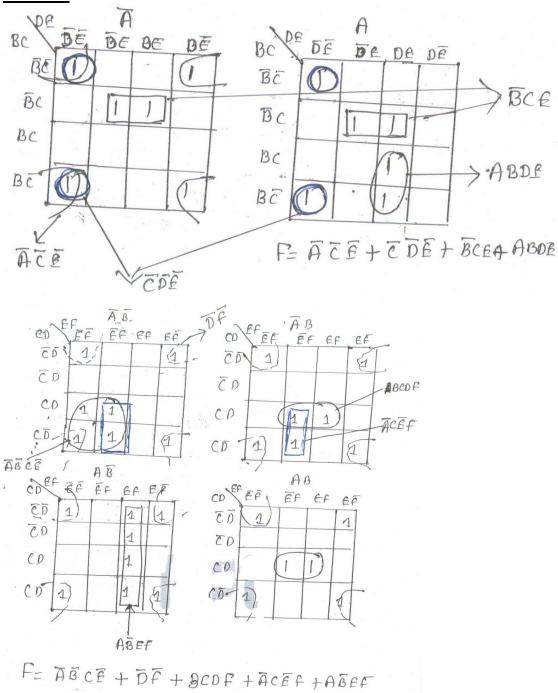
F(AIRICIDIC) = Em(0,1,2,4,5,6,10,13,14,
18,21,22,24,26,29,30)



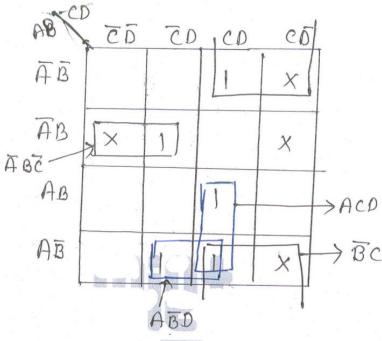


6. Simplify the following function using K map: $F(A, B, C, D, E) = \sum m(0.2.5.7.8.10.16.21.23.24.27.31)$. Simplify using K-map.

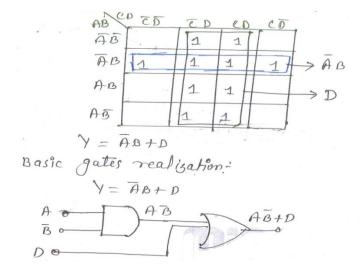
Solution



7. Simplify the following using K-map. $F(A, B, C, D) = \sum m(3.5.9, 11.15) + d(2.4.6.10)$



8. Reduce the following function using K-Map. Also, implement with basic gates only. $F(A, B, C, D, E) = \sum m(1,3,4,5,6,7,9,11,13,15)$



9. Simplify following logic expression using K-Map and realize using NOR gates only. $F(A, B, C, D, E) = \overline{A} \overline{B} \overline{C} + A \overline{C} \overline{D} + A \overline{B} + ABC\overline{D} + \overline{A} \overline{B} C$

Solution

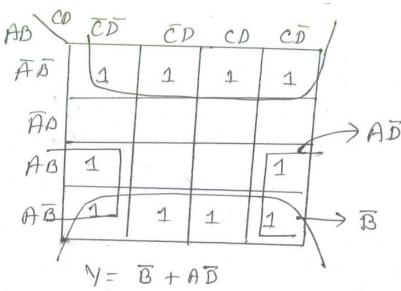
$$F(A, b, C, D) = \overline{A} \overline{B} \overline{C} + A \overline{C} \overline{D} + A \overline{B} + A B C \overline{D} + \overline{A} \overline{B} C$$

$$F(A, b, C, D) = \overline{A} \overline{B} \overline{C} (D + \overline{D}) + A \overline{C} \overline{D} (B + \overline{B}) + A \overline{B} (C + \overline{C}) (D + \overline{D}) + A \overline{B} C \overline{D}$$

$$+ \overline{A} \overline{B} \overline{C} \overline{D} + A \overline{B} \overline{C} \overline{D}$$

$$+ \overline{A} \overline{B} \overline{C} \overline{D} + \overline{A} \overline{B} \overline{C} \overline{D}$$

$$+ \overline{A} \overline{B} \overline{C} \overline{D} + \overline{A} \overline{B} \overline{C} \overline{D}$$



NOR realisation &

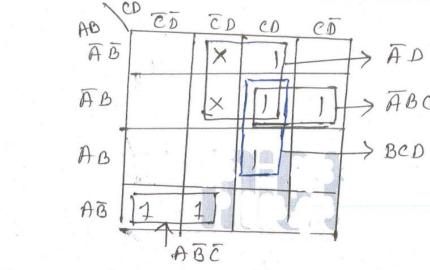
$$Y = \overline{B} + A\overline{D}$$

$$= \overline{B} + A\overline{D} = \overline{B} + \overline{A} + \overline{D}$$

10. Minimize the following using K-map: F(A, B, C, D) = AB'C' +A'BC + A'B'CD +ABCD + d(1,5)

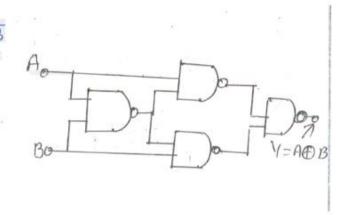
$$F(A,B,C,0) = ABC(D+D) + \overline{ABC(D+D)} + \overline{ABCD+ABCD}$$

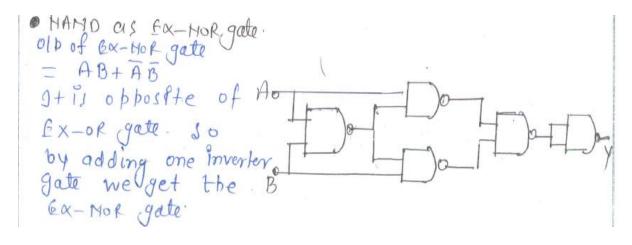
$$= ABCD + \overline{ABCD} + \overline{$$



11. Implement EX-OR and EX-NOR gates using minimum NAND gates.

olb of
$$Ex-oR$$
 gate = $\overline{A}B + \overline{A}B$
= $A\overline{A} + \cdot A\overline{B} + \overline{A}B + \overline{B}B$
= $A(\overline{A}+\overline{B}) + B(\overline{A}+\overline{B})$
= $A\cdot(\overline{A}\cdot\overline{B}) + B(\overline{A}\cdot\overline{B})$
= $\overline{A\cdot(\overline{A}\cdot\overline{B})} + B\cdot(\overline{A}\cdot\overline{B})$
= $\overline{A\cdot(\overline{A}\cdot\overline{B})} \cdot B\cdot(\overline{A}\cdot\overline{B})$
= $\overline{A\cdot(\overline{A}\cdot\overline{B})} \cdot B\cdot(\overline{A}\cdot\overline{B})$





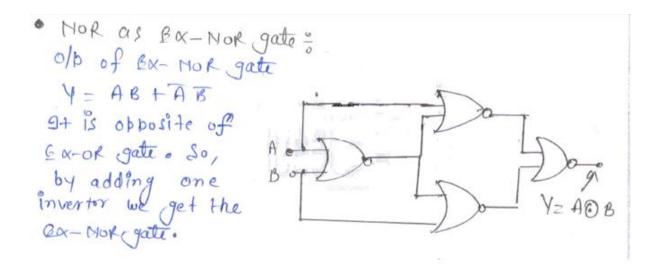
12. Implement EX-OR and EX-NOR gates using minimum NOR gates.

olb of
$$Ex-OR$$
 gate
$$Y = \overline{AB} + \overline{AB} + \overline{AA} + \overline{BB}$$

$$= \overline{A}(A+B) + \overline{B}(A+B) \quad Ao$$

$$= \overline{A}(A+B) + \overline{B}(A+B) \quad Bo$$

$$= \overline{A}(A+B) + \overline{B}(A+B) \quad Ao$$



13. Define Universal Gates. Implement AND, OR, NOR by using NAND gates only.

