

OS

Unit-II

Material

1 Mark Questions

(b)

Define Non-Preemptive Scheduling?

→ Here, there is no choice for the Processor in term of Scheduling, it has to take the next instruction.

→ Whenever CPU terminates or enters into waiting state, it has to take the new process which is present in the Queue.

(c)

Define 'Turn Around Time' (TAT).

→ It's the total time of the process inside the Queue.

(d)

Define Mutual Exclusion?

→ Mutual Exclusion means only one process can use a resource at a time.

→ The process can enter into the Critical section and that can be executed.

(a)

Define Deadlock?

- It is a situation when two processes share the same resource are effectively preventing each other from accessing the resource.

(b)

Sharing

(c)

Define Completion Time.

- It is the time required for the process to complete a process.

3 Marks

(d)

Define briefly about 'CPU Scheduling'?

(e)

- If there are multiple processes near CPU is scheduled to execute each process one-by-one.

→ CPU takes the process that are ready to execute & allocates CPU to one of them.

→ This short term scheduler is also known as Dispatcher.

→ Here the Dispatcher executes most frequent process & makes ~~the~~ grain division of which process to execute next.

→ CPU Scheduling decision may take place when a process.

(3)

(i.) Switch from running to waiting state.

(ii.) Switch from running to ready state.

(iii.) Switch from waiting to ready state.

(iv.) Terminate.

→ (i), & (iv) Come under Non-Preemptive Scheduling.

→ (ii) & (iii) Come under Preemptive Scheduling.

→ In Non-Preemptive Scheduling, CPU has no choice, if it has execute the present process whatever there is in the queue.

→ Whereas in Preemptive Scheduling, CPU has

a choice of executing any process based on priority.

(b) Discuss about Scheduling Criteria in CPU Scheduling?

→ The CPU Scheduler the processes to execute to schedule the processes to execute and it follows a criteria which is known as the Scheduling Criteria.

→ : called as Deadlock.

Q5

→ The Scheduling Criteria is as follows:

6.

i. CPU Utilization

Keep the CPU as busy as possible.

ii. Throughput

Number of processes that complete their execution per time unit.

iii. Turn Around Time

Amount of time to execute a particular process.

iv. Waiting Time

Amount of time a process has been waiting in the Ready Queue.

v. Response Time

Amount of time it takes from a request was submitted until the first response is produced (not O/P).

Scheduling Algorithm Optimization Criteria

→ Maximum CPU Utilization

→ Maximum Throughput

→ Minimum Turn Around Time

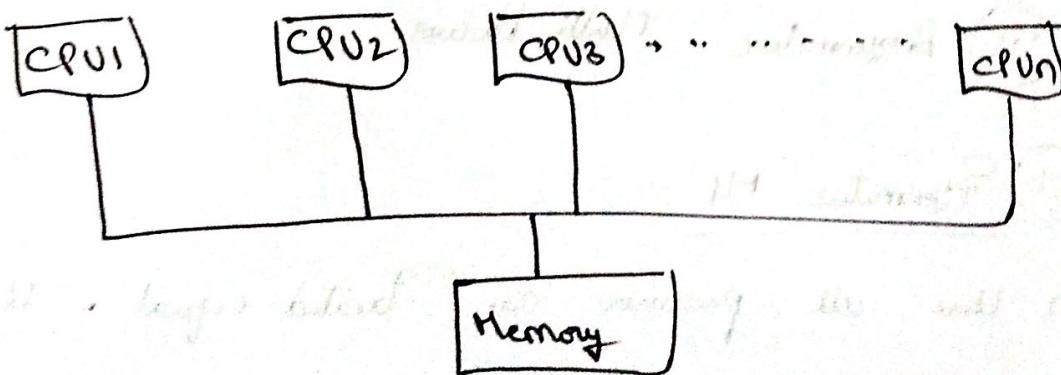
→ Minimum Waiting Time

→ Minimum Response Time.

(c)

Discuss about Multi-Processing System.

→ If there are more than one processor present in the system which can execute more than one process at the same time.



Multi-Processing System.

→ 2 or more processor within a Single System.

Parallelly.

→ Processor will be sharing memory, I/O devices and processor.

(6)

→ Let us consider 10 processes P_1, P_2, \dots, P_{10} .
If you want to schedule a particular process first and any process second - i.e. if you want to execute the process in a random order.

→ We need to decide the order of process execution i.e. what we need to decide.

→ This comes under Multi-Processing Scheduling.

→ This is of 2 types

I. Symmetric Multi-Processor

II. Asymmetric Multi-Processor

I. Symmetric MP =

→ Here, all processes are treated equal. Here as there are 10 processes. All processes are treated equal.

→ One OS controls all CPU's, each CPU has equal rights.

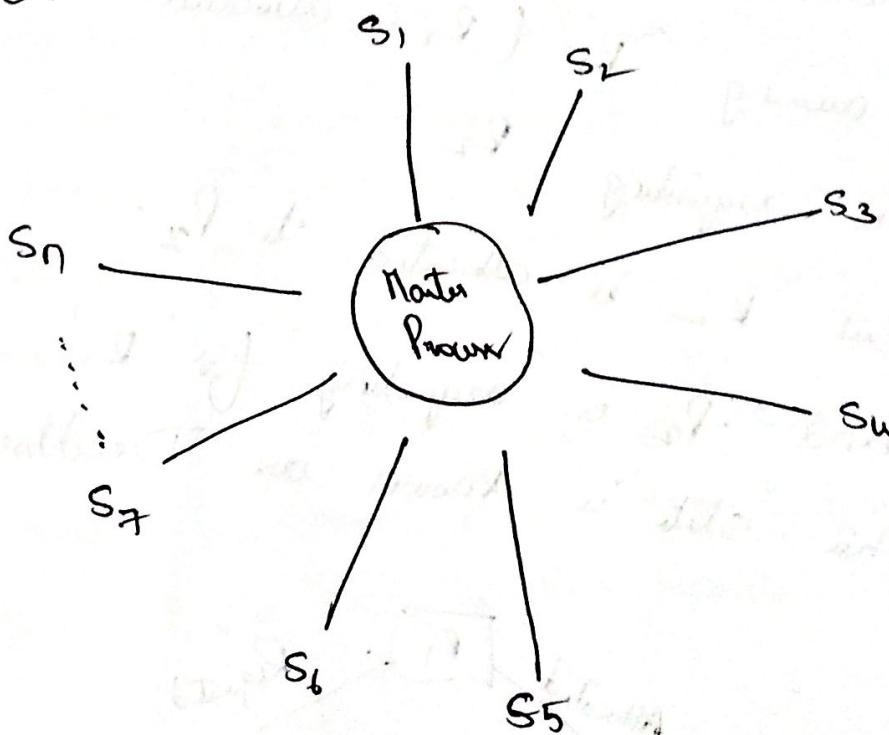
→ CPU's are in Peer-to-peer peer relationship.

II.

Asymmetric Multi-Processing Sys:

III.

- Here, Processors are treated as Master & Slave Processor
- i.e. We have Central Master Processor, this Master Processor will be Co-ordinating all the remaining Processors (Slaves)



S_i's → Slave Processor.

Advantages

Maximum Throughput, More Reliable, Fast Processing, Efficiency improved & More Economic System.

Note:

- Throughput: Number of Tasks executed per unit time.

(d)

Discuss about Deadlock.

(8.)

→ It is a situation when 2 processes sharing the same resource are effectively preventing each other from using the resource.

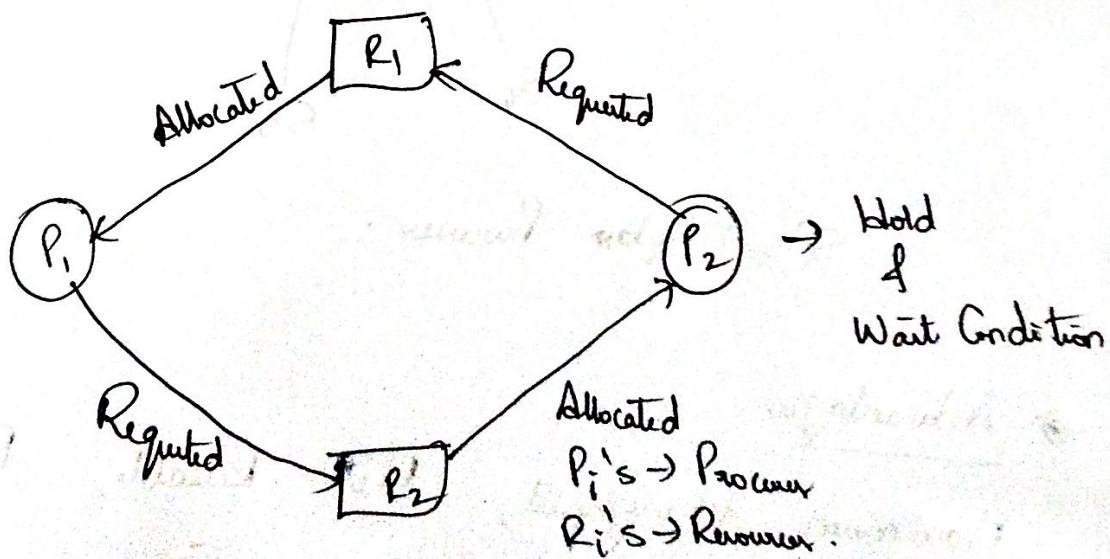
→ Let us consider two processes P_1, P_2 be 2 processes & R_1 & R_2 are two resources, R_1 is holding R_1 (R_1 is allocated to P_1) but P_1 is requesting R_2 .

P_1 is requesting R_2 .

→ But R_2 is allocated to P_2 .

→ And P_2 is requesting for R_1 .

→ This state is known as Deadlock.



→ P_1 is holding R_1 resource & requesting for R_2 .

→ P_2 is holding R_2 resource & requesting for R_1 .

→ Until P_2 releases R_2 , P_1 cannot acquire until P_1 releases R_1 , & P_2 cannot acquire R_1 .

→ This situation is called an Deadlock.

(9) (1)

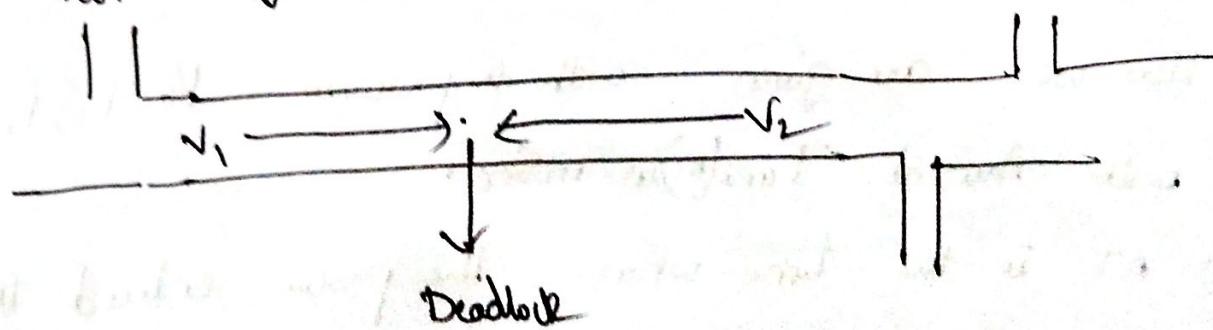
② Draw any General Examples of Deadlock.

→ Let us consider an example, Suppose if there 2 friends A & B, if A wants to Call B if B wants to Call A
→ A is Calling B & B is Calling A at same time.

→ So both A, B Call will get engaged.

→ This situation is called an Deadlock.

→ Let us take a narrow road, a Vehicle V1 is going of another Vehicle V2 is Coming on the opposite side of V1. There is no space to move, Now if V1 or V2 move to some side road means then there will be some space for the Vehicle to move but here V1 & V2 are not ready to give side to other Vehicles.



5 Mark

③ Explain in detail about any CPU Scheduling Technique.

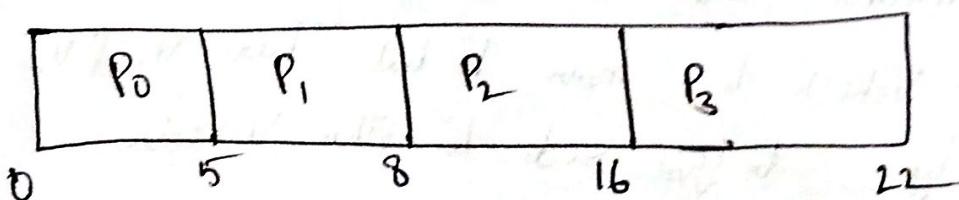
First Come First Serve (FCFS)

(iv)

→ CPU Scheduling Algorithm defines how CPU Scheduler handles different jobs & processes.

Process	Arrival Time(AT)	Burst Time(BT)	Completion Time(CT)	Total Waiting Time(WT)	Waiting Time(WT)
P ₀	0	5	5	5	0
P ₁	1	3	8	7	6
P ₂	2	8	16	14	12
P ₃	3	6	22	19	13

Gantt Chart



- Here we are given with 4 process P₀, P₁, P₂ & P₃ with Arrival Time(AT) in msec.
- AT is the time when the process entered the queue.
- BT is the time required for the process to complete itself.
- E.g. Here if CPU completes P₀ means it requires 5 msec. to process it, then its BT

- Here we need to find out the CT of WT. (11) (12)
- CT is the Time required for the CPU to complete a process.
- TAT is time for which the process is inside the Queue, i.e. the total time of the process inside the Queue.
- WT is the time for which the process waits for any resource.
- This is FCFS. Here the process whatever enters first will be served first, means the first process whatever enters the queue is executed first.
- Here for CT of each process, first P₀ arrives at 0msec & CPU executes the first process first according to FCFS.
- So P₀ executes at 5msec.
- Next P₁ enters in to the queue & the BT of P₁ is 3, so after 5msec when P₀ executes, P₁ will start executing by CPU, so $5+3=8$ msec, P₁ executes at 8msec.
- Next P₂ enters the queue, the BT of P₂ is 8, so after P₁ executes, P₂ starts executing by the CPU, so $8+8=16$ msec, So P₂ executes at 16msec.

→ Next similarly P_3 enters queue, P_3 's BT is 6, $16+22=22$, P_3 ends execution at 22mSec. (12)

→ So, for TAT, its difference b/w CT & AT ($TAT = CT - AT$)

$$TAT(P_0) = 8 - 1 \Rightarrow 5 - 0 = 5$$

$$TAT(P_1) = 8 - 1 = 7$$

$$TAT(P_2) = 16 - 2 = 14$$

$$TAT(P_3) = 22 - 3 = 19$$

→ For WT, its difference b/w TAT & BT i.e.

$$WT = TAT - BT$$

$$WT(P_0) = 5 - 5 = 0$$

$$WT(P_1) = 7 - 3 = 4$$

$$WT(P_2) = 14 - 8 = 6$$

$$WT(P_3) = 19 - 6 = 13$$

→ Next the total TAT is the sum of all TAT's of the processes i.e.

$$\text{Total TAT} = 5 + 7 + 14 + 19 = 45 \text{ mSec.}$$

→ The Average TAT is given by dividing the Total TAT with number of process i.e.

$$\text{Average (TAT)} = \frac{\text{Total TAT}}{n} = \frac{45}{4} = 11.25 \text{ mSec.}$$

$$\rightarrow \text{The Total WT} = 0 + 4 + 6 + 13 = 23 \text{ mSec.}$$

$$\rightarrow \text{Average (WT)} = \frac{23}{4} = 5.75 \text{ mSec.}$$

(b) Explain in detail about Deadlock detection.

(13)

→ Resource Allocation Graph (RAG)

→ When a Deadlock occurs, the progress of the system is suspended.

→ Before going to resolving the problem let us discuss about Deadlock Detection to know why this suspension has taken place.

→ Resource Allocation Graph, Resource Allocation means we are allocating the resource to each process.

→ So, how these resources are allocated to the processes we will arrange graph for this.

→ Each process initializes a resource as

- Request

- Use

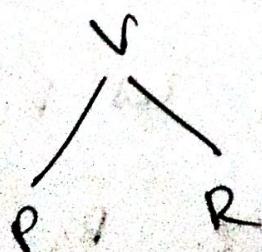
- Release

→ The RAG consists of

- A set of vertices V & set of edges E .

$$G = (V, E)$$

- V is partitioned into 2 types



- P is set of Processors
 $P = \{P_1, P_2, \dots, P_n\}$ initially available
- the set consisting of all the processor in the system.

- R is set of Resources.

$$R = \{R_1, R_2, \dots, R_m\}$$

- the set of Consisting of all the resource in the system.

- Request Edge is directed Edge

$P_i \rightarrow R_j$ (P_i requesting for R_j Resource)

- Assignment Edge is $R_j \rightarrow P_i$

(R_j is assigned to P_i).

- Now let us Consider an eg for RAM, a resource R_1 with a single instance. Similarly R_2 with single instance; R_3 with 2 instances & R_4 with 3 instances.

- Instances mean the resource can share to those many processor.

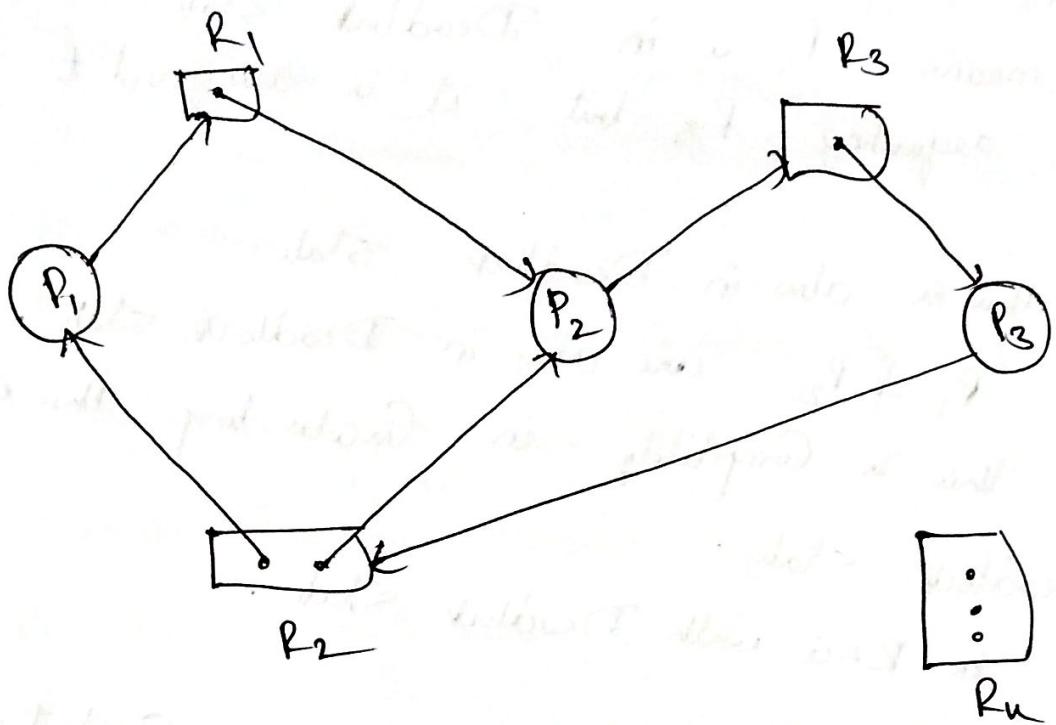
- A single instance can be shared to a single processor.

- Let us Consider we have 3 processor P_1, P_2 & P_3 .

- P_1 is requesting R_1 but R_1 is assigned to P_2 .

- Where P_2 is requesting R_3 , but R_3 is assigned to P_3 .

- One of instance in R_2 is assigned to P_1 & another instance in R_2 is assigned to P_2 . (15.)
- This is the Graphical Representation of RAG.



RAG

- Here P_1, R_1, P_2 & R_2 form a closed cycle.
- ~~P_2, R_3, P_3 & R_u form a Open Cycle.~~
- Now P_1, R_1, P_2 & R_2 form one Cycle & P_2, R_3, P_3 & R_u form another Cycle.
- So, here a Deadlock may occur, because here P_1 is in blocked state. P_1 is requesting R_1 , R_1 is assigned to P_2 . So, P_1 need to wait until R_1 is released.

- One instance of R_2 is given to P_2 & P_3 is requesting for R_2 . (16)
- So, P_3 need to wait until R_2 is released.
- It means P_3 is in Deadlock state.
- P_2 is requesting R_3 but it is assigned to P_3 .
- But this is also in Deadlock state.
- Now P_1 & P_2 are also in Deadlock state.
- Now this is Completely in Circular loop, this is in Deadlock state.
- This is RAG with Deadlock state.

c) Explain in detail about the Necessary Condition for Deadlock.

→ Deadlock may occur when Processors are in

- I. Mutual Exclusion
- II. Hold & Wait
- III. No Preemption
- IV. Circular Wait

→ When these 4 conditions may occur simultaneously for Deadlock to occur

→ So, these 4 conditions may lead to Deadlock.

(17.)

(I)

Mutual Exclusion: (No Sharing of Resource).

→ The Resources are not sharable.

→ If R_1 resource is allocated to P_1 , then it is not sharable.

→ If R_1 resource is allocated to it is sharable, any process can own it.

→ Mutual Exclusion means only one process can use a resource at a time.

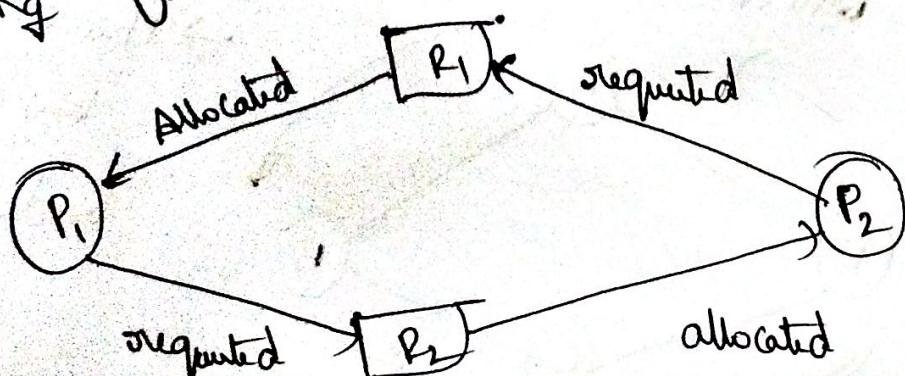
→ The process can enter into critical section of that can be executed.

(II)

Hold & Wait (P_i 's \rightarrow Process, R_i 's \rightarrow Resource)

→ P_1 holding R_1 & waiting for R_2 , P_2 holding R_2 & waiting for R_1 .

→ Hold & Wait Condition means a process can hold atleast one resource & will be waiting for another.



Hold
&
Wait
Condition

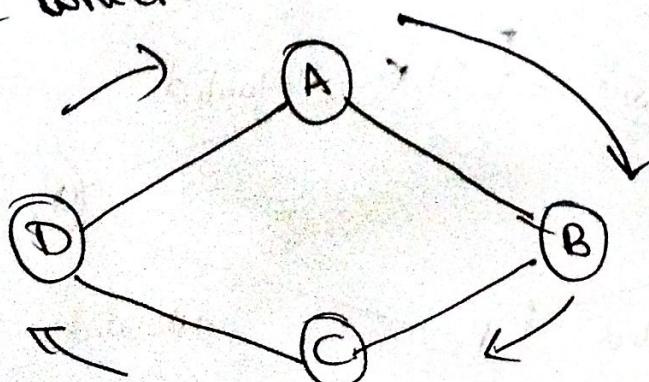
(v) No Preemption

18.

- Preemption means if one process completes its resource utilization it can release the resource.
- So that another process will access that resource.
- Here a process ~~Voluntarily~~ need to release the any resource & there is no other way for any resource to be released.

(vi) Circular Wait

- Let us consider processes A, B, C & D.
- So, 'A' requires some resources which is needed by 'B' & 'B' requires some resources which is needed by 'C'.
- And 'C' requires another resource which is needed by 'D'.
- And 'D' requires some resource which is needed by 'A'.
- Here everyone is waiting for some resource which is allocated for some other process.



→ This is Circular Wait, which leads to Deadlock

(d)

(1a)

Explain in detail about Deadlock Prevention.

- How to prevent the Deadlock?
- We need to find a situation before Deadlock occurs.

Necessary Conditions for Deadlock

- Mutual Exclusion (Resources are Non-Sharable)
- No Preemption (Power need to voluntarily release a Resource)
- Hold & Wait (Process holding one Resource and waiting for another Resource)
- Circular Wait (Process waiting for resources which are owned by another process in a circular order).

→ There are the conditions for Deadlock.

→ If all these conditions are true then Deadlock may or may occur.

→ Deadlock Prevention states that

- if possible try to prevent all these conditions or to try to prevent atleast one condition among all these

Condition

→ All Condition - FALSE

Deadlock Prevention

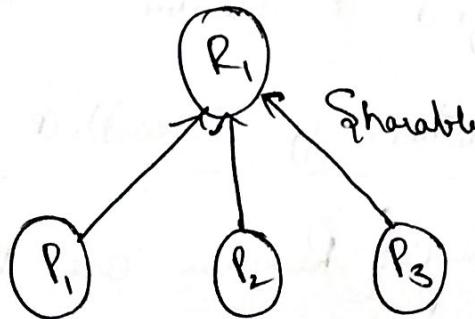
→ Atleast one Condition - FALSE

→ Make Mutual Exclusion false,

- Just Share the resources, if the resources are sharable, multiple process then we can avoid Mutual Exclusion

R_i 's → Resources

P_j 's → Process

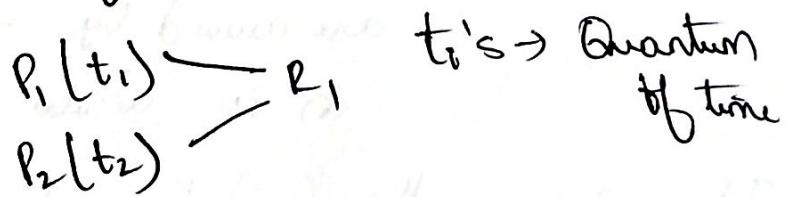


→ Making No Preemption false,

- No Preemption false means, making Preemption true.

- This can be done by using time Quantum method.

- Time Quantum method means allocating some time for each process for accessing any resource.



→ Making Hold & Wait false,

- How to make Hold & Wait false?

- By implementing No Hold & Wait

- Give all the resources to process before it starts.

- So it can take any number & can give those resources back.

- This condition is Hold & Wait.

(21.)

- Make a Circular Wait Fane,
- Need to make Circular Wait Fane.
 - By simply giving numbering to all the resource, we can prevent Circular Wait.

Eg: CPU-1, Printer-2, Scanner-3, ...

- So, that the process can request in increasing order.
- Eg: P_1 is asking 'CPU' & it needs '3' (Scanner), so P_2 which is asking '3', if cannot request for 1 before it is asking 3 - it can answer for resource greater than 3 & not less than 1.

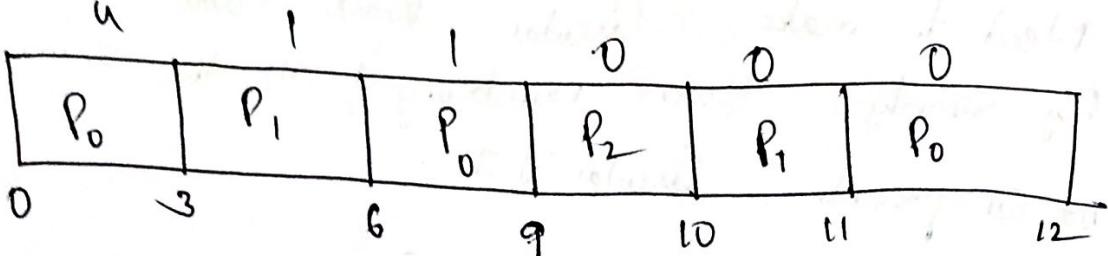
c) Discuss about Round Robin Scheduling Alg.

→ Here, we have a Go-Goto rule called as Quantum (q), ' q ' is the maximum time that CPU goes given a process at a single point of time. Before it will pame the process & move to another process.

→ Here in this case Quantum ($q=3$)

Process	AT	BT	CT	TAT (CT-AT)	WT TAT-BT
P_0	0	7	12	12	5
P_1	2	6	11	9	5
P_2	6	1	10	6	5

Gantt Chart



- First 'p0' enters the Queue at 0msec, after 3msec or $q_f = 3\text{msec}$, ' p_0 ' is pased f CPU checker whether any process enters into the Queue.
- $S_{q_0} = 2\text{msec}$, p_1 enters the Queue, S_0 here the required for p_0 to execute is $7 - 3 = 4\text{msec}$.
 p_0 will be in Queue after ' p_1 '.
- S_{q_0} at 3msec, CPU scheduler ' p_1 ' to execute or $q_f = 3$ at $3 + 3 = 6\text{msec}$, CPU checker.
- Now as ' p_0 ' is in the Queue after ' p_1 ', CPU checker whether any new process arrived and at ' p_1 ' arrives & p_2 will be in the Queue after p_0 ($p_0\ p_1\ p_0\ p_2$)
- Now ' p_1 ' is pased by the Queue CPU & p_0 in Queue starts executing. & ' p_1 ' will be in the Queue after p_2 ($p_0\ p_1\ p_0\ p_2\ p_1$)
- After 3msec, or $q_f = 3\text{msec}$, $6 + 3 = 9\text{msec}$, CPU pase ' p_0 ' & scheduler ' p_1 ' of remaining time needed by ' p_0 '. to execute in 1msec. ($p_0\ p_1\ p_0\ p_2\ p_1\ p_0$).

→ Now P₂ enters the Queue, the BT for 23

P₂ is 1msec, So at 10msec, P₂ will be created.

→ Now P₁ enters & executes at time 11msec, P₁ will be executed.

→ Now finally 'P₀' enters & executes at 12msec.

$$\text{Average (TAT)} = 27/3 = 9\text{ msec}$$

$$\text{Average (WT)} = 15/3 = 5\text{ msec.}$$

10 Marks

1.
Q. a)

Explain in detail about Banker's Algorithm.

→ When a process request for any resource it may have to wait.

→ And if process gets all the resources, it need to use those resources & need to return them in a finite amount of time.

Eg: P₁ gets 10 & P₂ gets 5 resources, they need to use those resources & need to return them in a finite amount of time.

→ Three Conditions need to be taken care when we are dealing with Deadlock Avoidance.

- First we need to check whether our System is in Safe or Unsafe state.
- Let us Consider 5 process P_0, P_1, P_2, P_3 & P_u .
- And Allocation for each process

A, B, C - 3 Resource.

Total Count of a resource can hold

$$A = 10, B = 5, C = 7$$

Maximum - Maximum each process can allocate

Current Work - The Available

(What we need to find out)

Remaining ned - Whatever is needed

(Maximum Allocation).

Process	Allocation	Maximum	Current Work (Available)			Remaining ned			
			A	B	C	A	B	C	A
P_0	0 1 0	7 5 3	3	3	2	7	4	3	
P_1	2 0 0	3 2 2	5	3	2	1	2	2	
P_2	3 0 2	9 0 2	7	4	3	6	0	0	
P_3	2 1 1	4 2 2	7	4	5	2	1	1	
P_u	0 0 2	5 3 3	7	5	5	5	3	1	
		7 2 5	3	3	2	10	5	7	

- Now P_2 enters the queue, the BT for P_2 is 1msec, so at 10msec, P_2 will be executed. . . 25.
- Now P_1 enters & executes at 11msec, P_1 will be ~~executed~~ executed
- Now finally P_0 enters & executes at 12msec

$P_0 \rightarrow$ Remaining need (A)

$$= Max(P_0, A) - Allocation(P_0, A)$$

$$= 7 - 0 = 7$$

Similarly B & C

$$R.N(B) = Max(P_0, B) - Allocation(P_0, B)$$

$$= 5 - 1 = 4$$

$$R.N(C) = Max(P_0, C) - Allocation(P_0, C)$$

$$= 3 - 0 = 3$$

Similarly Remaining Values

Current Work = Total Allocation Count of each resource
i.e. Total Times a resource is Allocated

$$\text{Current Work}(\uparrow) = 10 - 7 = 3$$

→ Now we'll need to find out which processes are in safe side in a sequence.

→ Now among all the processes P_0 to P_n ,

we need to find out which process is in Safe side in a sequence (first safe process to last safe process in sequence). (26)

→ Banker's Alg is having a formula to calculate this.

If Remaining Need (R) \leq Current Work, Then Update
 $R = R - A$

$$\text{Current Work} = \text{Current Work} + \text{Allocation}$$

→ For P_0 ,

$$\text{Remaining Need } (R) \leq \text{Current Work } (C-W)$$

$$R-N \quad \begin{matrix} A & B & C \\ 7 & 1 & 3 \end{matrix} \leq \begin{matrix} A & B & C \\ 3 & 3 & 2 \end{matrix}$$

$$7 \leq 3 \checkmark, \quad 1 \leq 3 \checkmark, \quad 3 \leq 2 \checkmark$$

$\therefore P_0$, P_0 is not in safe side

→ For P_1 ,

$$R-N \quad \begin{matrix} A & B & C \\ 1 & 2 & 2 \end{matrix} \leq \begin{matrix} A & B & C \\ 3 & 3 & 2 \end{matrix}$$

$$2 \leq 3 \checkmark, \quad 2 \leq 2 \checkmark$$

$$1 \leq 3 \checkmark,$$

$\therefore P_0, P_1$ is in safe side

Now Update

Current Work of P_1

$$C-W(P_1) = \begin{matrix} A & B & C \\ 3 & 3 & 2 \\ \oplus & 2 & 0 & 0 \\ \hline 5 & 3 & 2 \end{matrix}$$

CPI

→ For P_2 ,

$$\begin{array}{l} R \cdot N \leq C \cdot W \\ A \ B \ C \leq A \ B \ C \\ 6 \ 0 \ 0 \leq 5 \ 3 \ 2 \end{array}$$

$$6 \leq 5 \otimes, \quad 0 \leq 3 \otimes, \quad 0 \leq 2 \otimes$$

∴ S_0, P_2 is not in safe side.

→ For P_3 ,

$$\begin{array}{l} R \cdot N \leq C \cdot W \\ A \ B \ C \leq A \ B \ C \\ 2 \ 1 \ 1 \leq 5 \ 3 \ 2 \end{array}$$

$$2 \leq 5 \checkmark, \quad 1 \leq 3 \checkmark, \quad 1 \leq 2 \checkmark$$

S_0, P_3 is in safe side.

Update $C \cdot W$

$$C \cdot W = \frac{C \cdot W}{4} + \frac{C \cdot W}{5} + \frac{C \cdot W}{3} + \frac{C \cdot W}{2}$$

$$\begin{array}{r} \oplus \\ 2 \ 1 \ 1 \\ \hline 7 \ u \ 3 \end{array}$$

$\langle P_1, P_3 \rangle$

→ For P_4 ,

$$\begin{array}{l} R \cdot N \leq C \cdot W \\ A \ B \ C \leq A \ B \ C \\ 5 \ 3 \ 1 \leq 7 \ u \ 3 \end{array}$$

$$5 \leq 7 \checkmark, \quad 3 \leq u \checkmark, \quad 1 \leq 3 \checkmark$$

(20)
(21)

Now Update C-W,

$$C-W = C-W + \text{Allocation}$$

$$\begin{array}{r} A \quad B \quad C \\ 7 \quad u \quad 3 \\ (+) \quad 0 \quad 0 \quad 2 \\ \hline 7 \quad u \quad 5 \end{array}$$

$\leftarrow P_1, P_3, P_u$

→ Now again we need to check if P_0 & P_2 or
 P_1, P_3, P_u are exited and some
resources are free.

→ For P_0 ,

$$\begin{array}{l} R:N \leq C-W \\ \begin{array}{ccc} A & B & C \\ 7 & u & 3 \end{array} \leq \begin{array}{c} A \\ 7 \\ u \\ 5 \end{array} \\ 7 \leq 7, u \leq u, 3 \leq 5 \end{array}$$

$\leftarrow P_1, P_3, P_u, P_0$

→ For Update C-W, $C-W = C-W + \text{Alloc}$

$$\begin{array}{r} A \quad B \quad C \\ 7 \quad u \quad 5 \\ (+) \quad 0 \quad 1 \quad 0 \\ \hline 7 \quad 5 \quad 5 \end{array}$$

$\leftarrow P_1, P_3, P_u, P_0$

→ For P2,

$$R \cdot N \leq C \cdot W$$

$$A \quad B \quad C \leq A \quad B \quad C$$

$$6 \quad 0 \quad 0 \leq 7 \quad 5 \quad 5$$

$$6 \leq 7, \checkmark, \quad 0 \leq 5, \checkmark, \quad 0 \leq 5, \checkmark$$

Update C-W, C-W + Alloc

$$\begin{array}{r} 7 \quad 5 \quad 5 \\ 3 \quad 0 \quad 2 \\ \hline 10 \quad 5 \quad 7 \end{array}$$

$$\langle P_1, P_3, P_4, P_0, P_2 \rangle$$

→ Now all processes entered into safe side

$$\langle P_1, P_3, P_4, P_0, P_2 \rangle$$

→ Now when we execute the processes in this order then Deadlock does not occur.

Explain in detail about Deadlock.

b)

- Refer 3 Marker (2-d) Answer in page-8

Refer 3 Marker Answer (2-e) in page-9

Refer 5 Marker Answer (3-b) in page-13

Refer 5 Marker Answer (3-c) in page-16

Refer 5 Marker Answer (3-d) in page-19

(c) Explain in detail about any 2 CPU Scheduling Techniques? (30)

Refer 5 Marks Answer (3)-a in page 94

Refer 5 Marks, Answer (3)-e in page 21