

## Faculty Profile

Name: **Dr. P. Akhendra Kumar**  
Designation: Assistant Professor  
Teaching Areas: RF VLSI  
Research Interests: On-chip inductor and Capacitors modeling  
Education: Ph.D., from National Institute of Technology Warangal in 2018  
M.Tech from GMRIT (JNTU K) in 2011  
B.Tech from BVCITS in 2008.



### Professional Experience : (Total: 5 years)

1. 2020 – Till Date : Assistant Professor, IFHE, Hyderabad
2. 2017- 2020 : Associate Professor, SVECW Bhimavaram
3. 2012- 2014: Assistant Professor, SVECW Bhimavaram.

### Research / Selected Publications:

1. **P. Akhendra Kumar, A N Kiran, N. Bheema Rao**, *High quality factor fractal inductor with complementary split-ring array inclusion*, Circuit World, Jan 2020 , Pages 1–6, Doi: 10.1108/CW-06-2019-0052 (SCI).
2. **P. Akhendra Kumar, N. Bheema Rao**, *Design of on-chip Hilbert Fractal Inductor Using Improved Feed Forward Neural Network for Si RFIC's* , Turkish Journal of Electrical Engineering & Computer Sciences, Volume 26, May 2018, Pages 2437 - 2447, Doi:10.3906/elk-1705-362 (SCI).
3. **P. Akhendra Kumar, N. Bheema Rao**, *Series stacked fractal inductor for Radio frequency Applications*, IET Electronics Letters, Volume 53, Oct 2017, Pages 1387– 1388, 10.1049/el.2017.2623 .
4. **P. Akhendra Kumar, N. Bheema Rao**, *High Inductance Fractal Inductors for Wireless Applications*, Turkish Journal of Electrical Engineering & Computer Sciences, (SCI). Volume 25, May 2017, Pages 3868 - 3880, doi:10.3906/elk-1607-190 (SCI).
5. **P. Akhendra Kumar, N. Bheema Rao**, *Fractal spiral capacitor for Wireless Applications*, IET Electronics Letters, Volume 52, April 2017, Pages 481–483, doi:10.1049/el.2015.3420 (SCI).
6. **A. Narayana Kiran, P. Akhendra Kumar**, *Power Optimized and Low Noise Tunable BPF using CMOS Active Inductors for RF Applications*, International Journal of Computer Science and Information Security (IJCSIS), Volume 14, October 2016, Pages 53–55 (ESCI).
7. **P. Akhendra Kumar, N. Bheema Rao**, *A Novel Fractal Stacked Inductor using Modified Hilbert space filling curve for RFIC's*, Springer Lecture Notes in Electrical Engineering, Accepted (Scopus).
8. **P. Akhendra Kumar, N. Bheema Rao**, *Parallel stacked fractal inductor for using modified Hilbert space filling curve for RFIC'S*, Communication on Applied Electronics, Accepted (Scopus).