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# Faculty Profile

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| Name: | **Narayanam Ranganadh** |
| Designation: | Assistant Professor |
| Teaching Areas: | Digital Systems VLSI; Analog, Digital and Mixed CMOS VLSI; HDL programming; Nano-Technology; Electronic Devices and Circuits; Cellular Mobile Communications, Computer Organization & Architecture; speech processing. Digital Image Processing |
| Research Interests: | Software and Hardware Implementations in Digital Signal & Image Processing areas. |
| Education: | MS, Electrical Engineering, University of Texas at San Antonio, USA(Research Option)- 2004 |
| | B.Tech , Electronics and Communication Engineering, VR Siddhartha Engineering College, Nagarjuna University,2000. |
| | Master of Business Administration, NIBM, 2016. |

## Professional Experience (14 years of Teaching & Research)

1. 2014 May - Till date: Faculty of Science &Technology, IFHE, Hyderabad.
2. Research Training: Auditory Neural Signal Processing, 2009-2011, University of Ottawa, Canada.
3. Signal & Image Processing Assistant III: Neural Signal and Image Processing, Helen Wills Neuro Science Institute, Henry Wheeler Jr. Brain Imaging Centre, 2004-2005, University of California Berkeley, USA.

## Research / Selected Publications

1. Ranganadh N, “Development Of Novel Algorithm for Haar Transform Using Paired Transform: Implementation On TMS DSP Processors”, International Journal of Engineering Science and Research Technology, June 2020, 9(4).
2. Ranganadh N, “Implementation of DAUBECHIES Discrete Wavelet Filter Banks Using Xilinx FPGAS”, IOSR journal of VLSI and Signal Processing, Volume 6(4), September 2019.
3. Ranganadh N, SSSP Rao, “Implementation of a Highly Efficient Novel Frequency Domain SNR Hardware Using XILINX FPGAs”, IJESRT, Dec 2017, VOL 6, ISSUE 12.
4. Ranganadh N, Kishore Kumar, “A novel experimental evaluation for the ‘development of a novel standard notion’, image quality assessment (iqa) measures, computational time for image contrast enhancement”, IJESRT, vol 5, Issue 8, August 2016.

## Books:

1. Ranganadh Narayanam, “Implementation and Performance Evaluation of the Fast Discrete
2. Fourier Transform by Using Radix-2 and Paired Transform Algorithms”, 2004. (Worldcat.org).
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