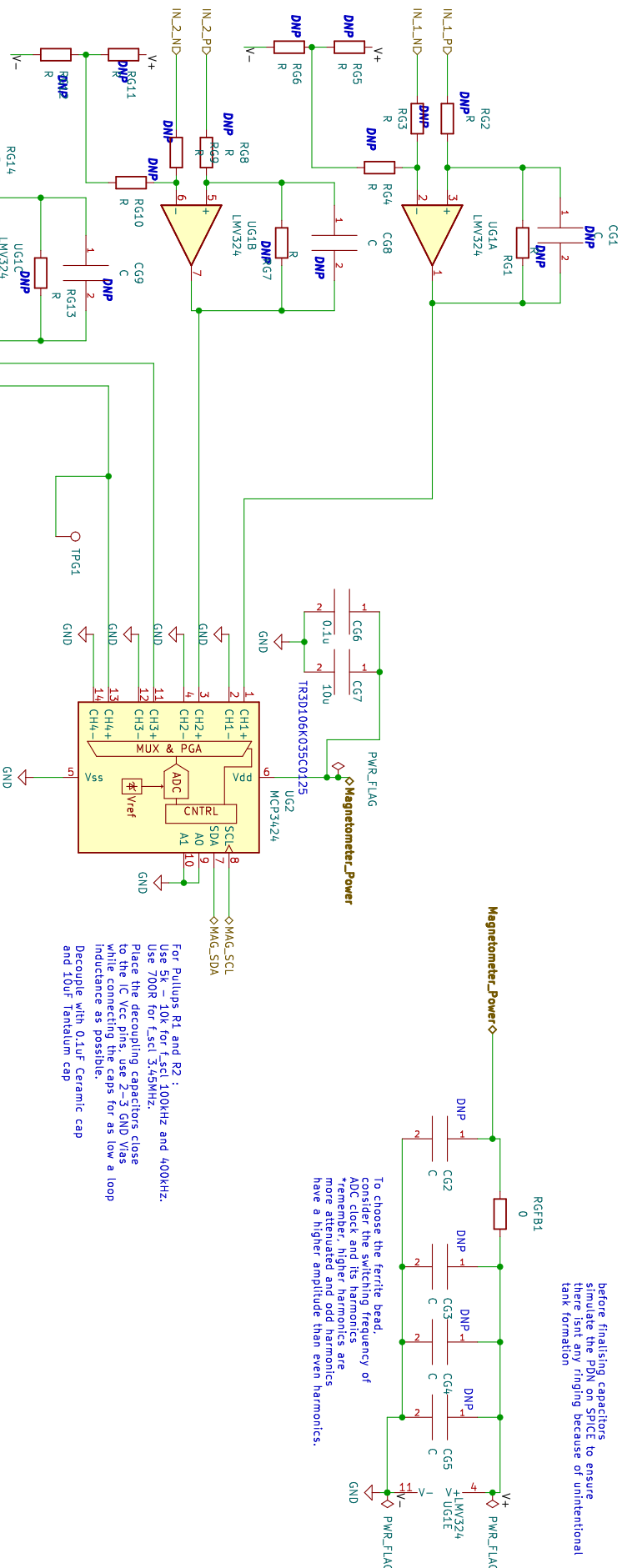


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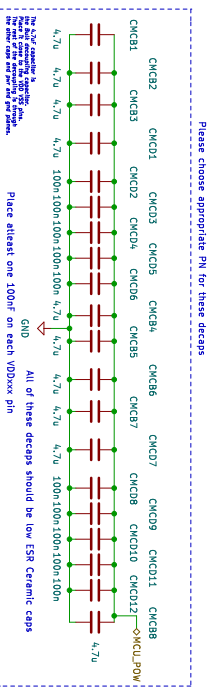
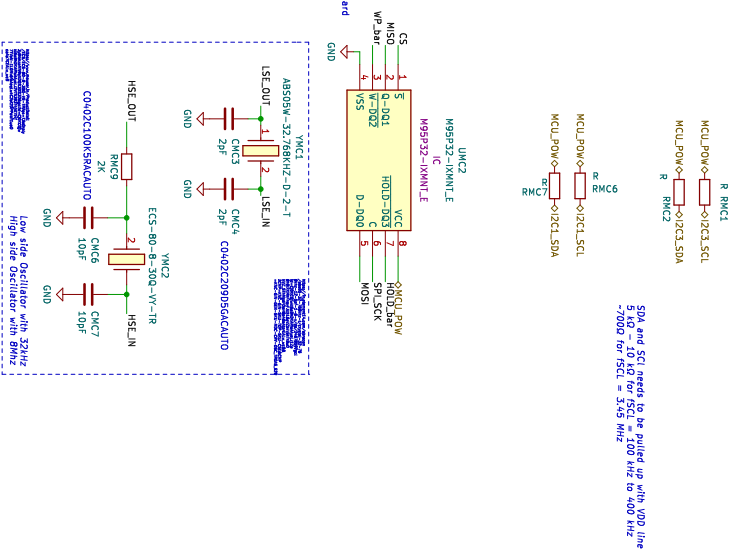
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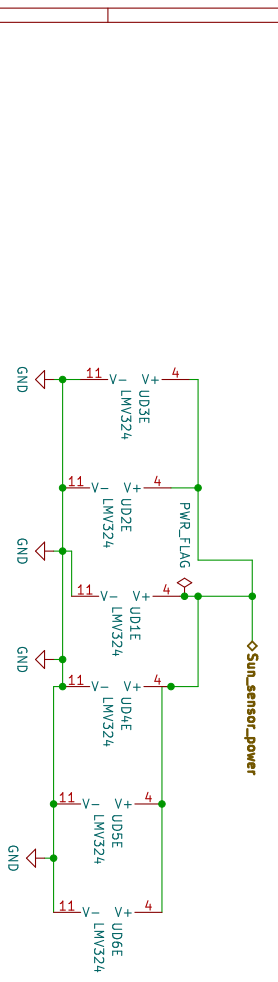
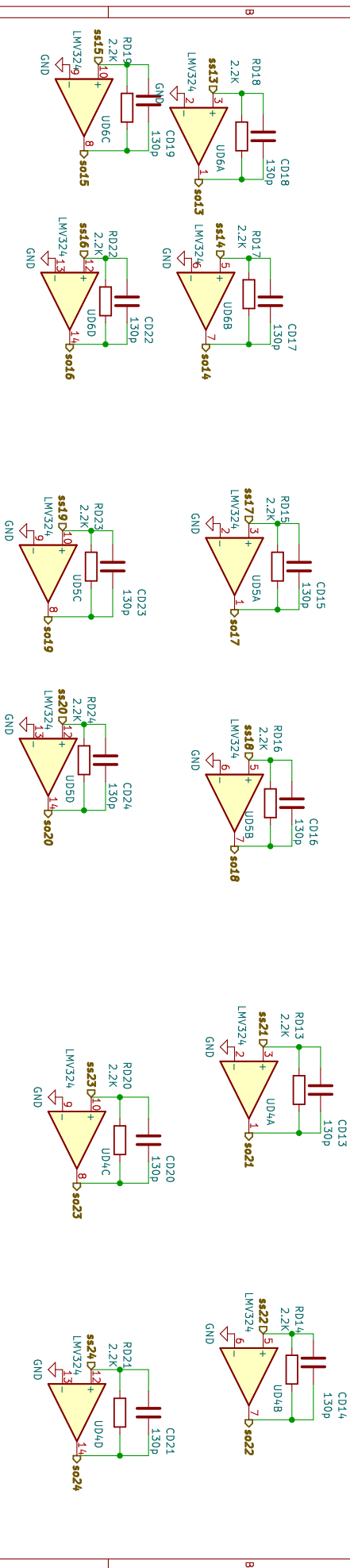
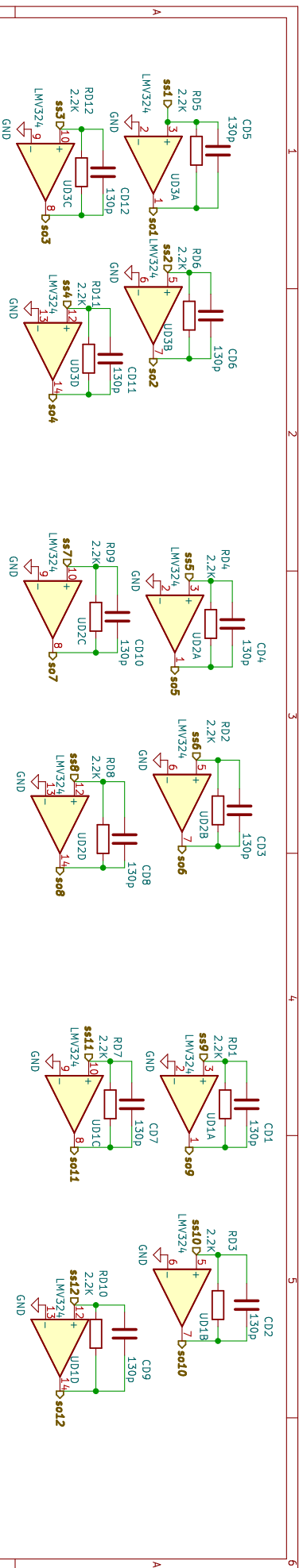
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Size: A3	Date:
Kicad E.D.A. kicad (6.0.9)	Rev:
	Id: 1/16



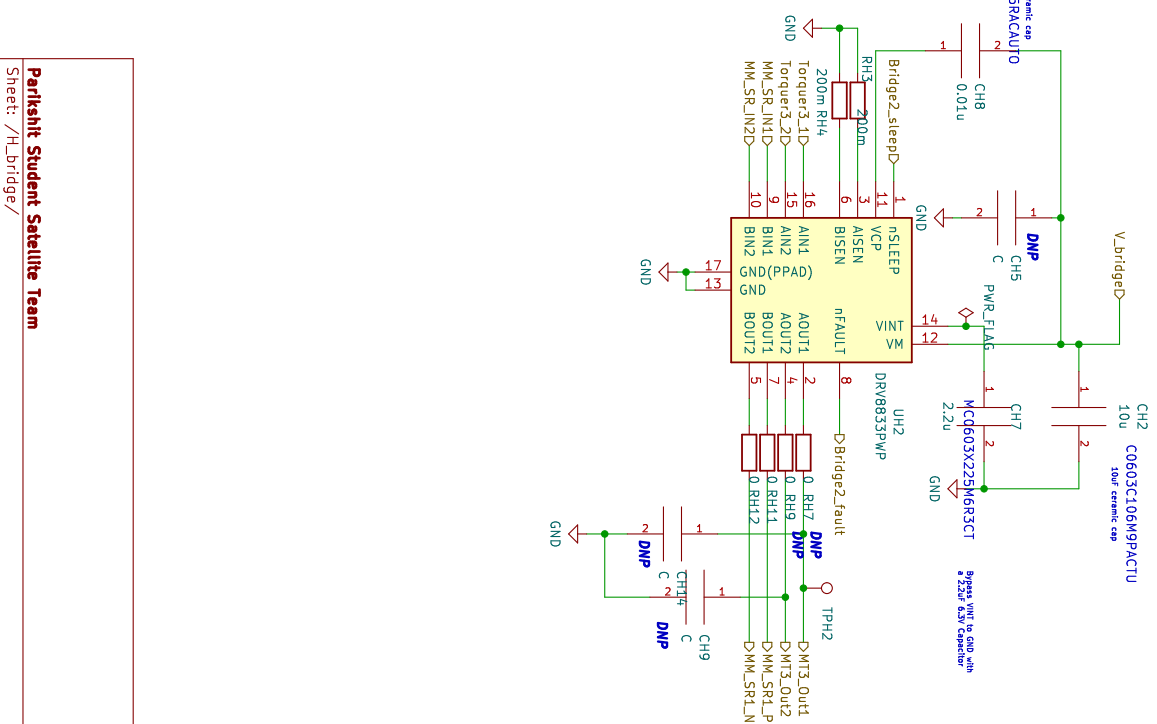
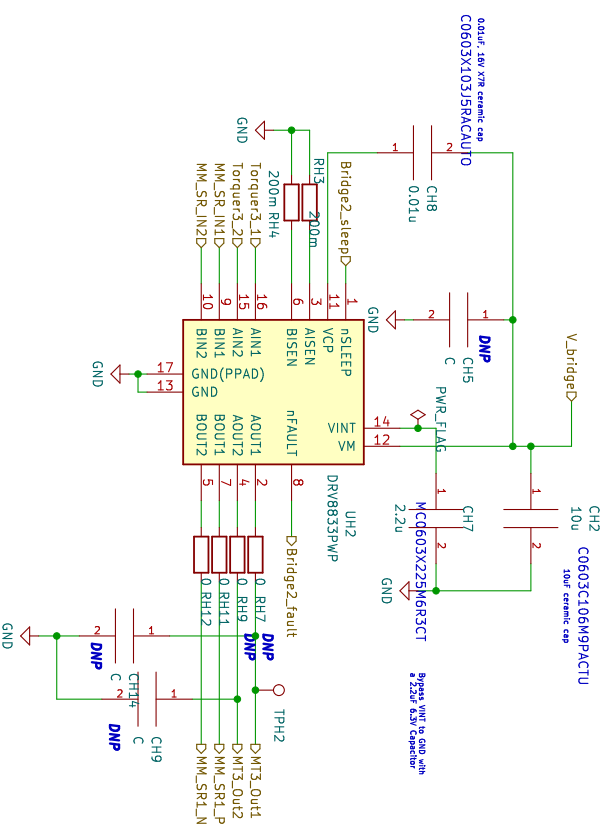
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Size: A4	Date:
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Rev:	
Id: 2/16	



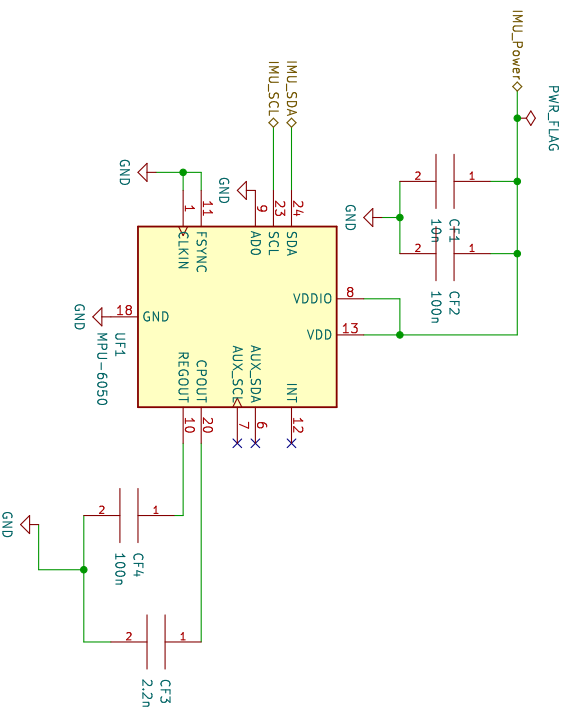




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Size: A4	Date:	Rev:	
Kicad E.D.A. kicad (6.0.9)		Id: 5/16	



Id: 7/16



Perikshit Student Satellite Team

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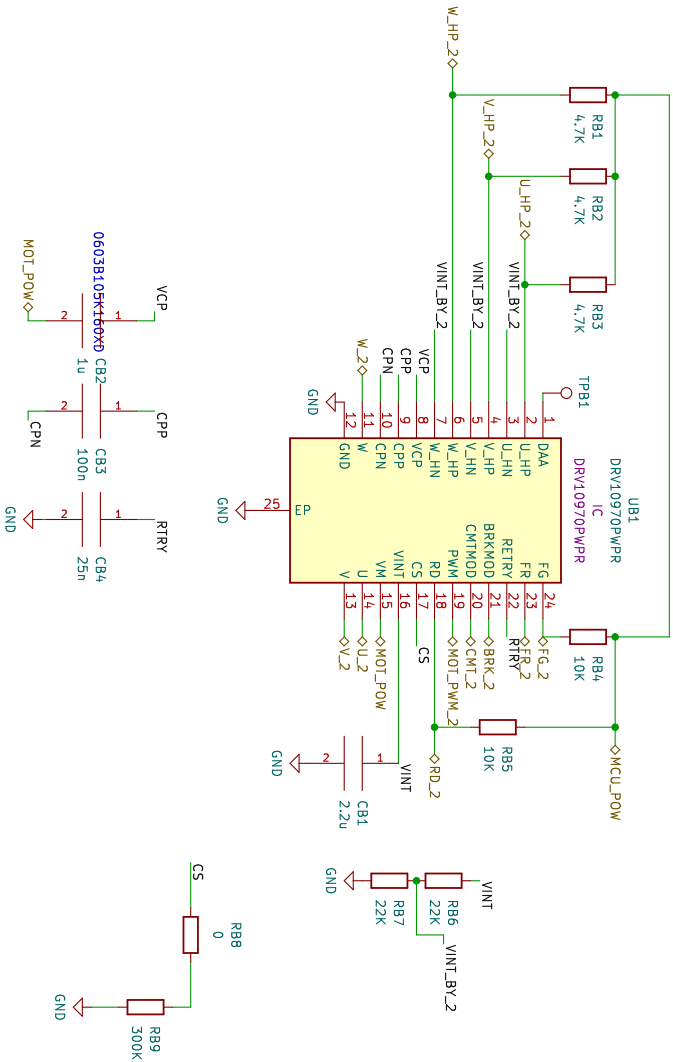
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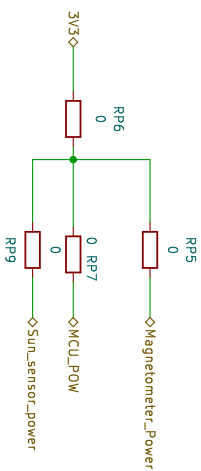
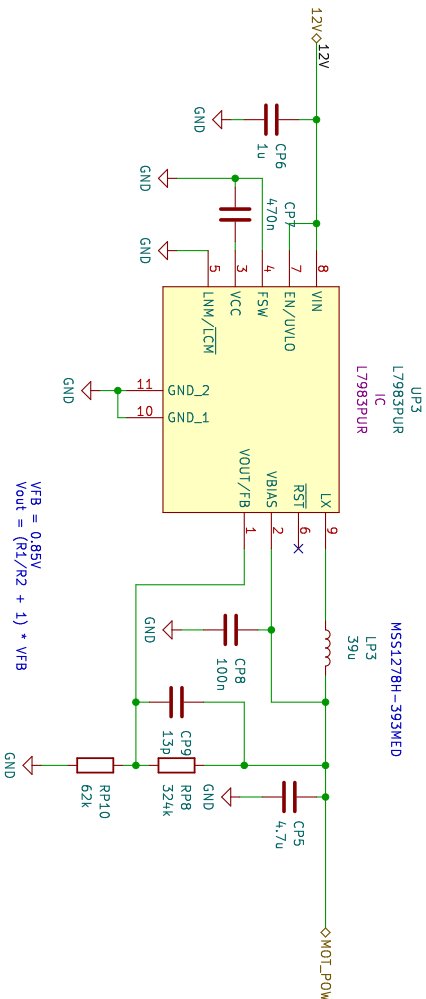
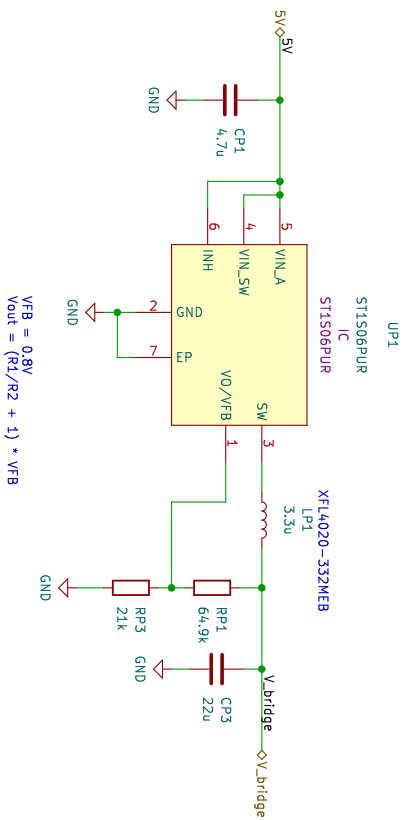
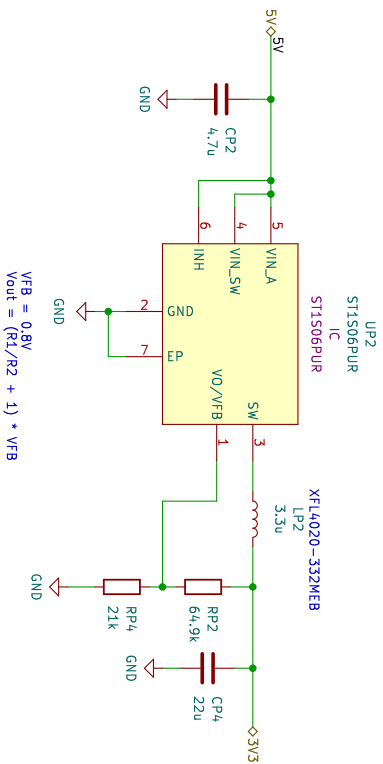
Rev:

Id: 8/16

A low-ESR ceramic capacitor must be placed in between the VM and VCP pins. TI recommends a value of 1-µF rated for 16 V. Place this component as close as possible to the pins.



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Title: AOCs BOARD	
Size: A4	Date:
KiCad E.D.A. kicad (6.0.9)	Rev: Id: 11/16



Perkshitt Student Satellite Team

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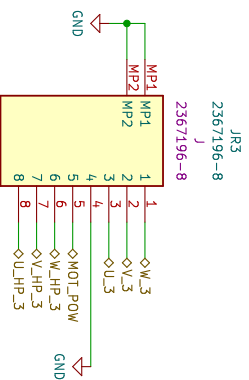
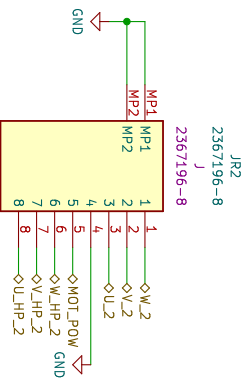
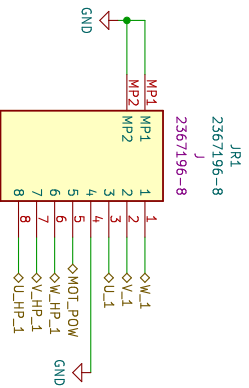
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Rev: Id: 11/16





Perikshit Student Satellite Team

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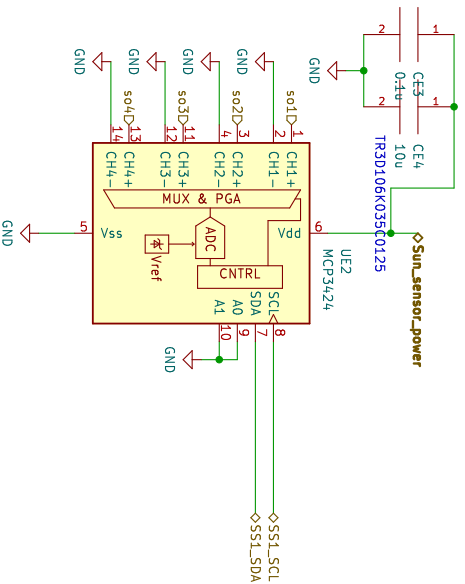
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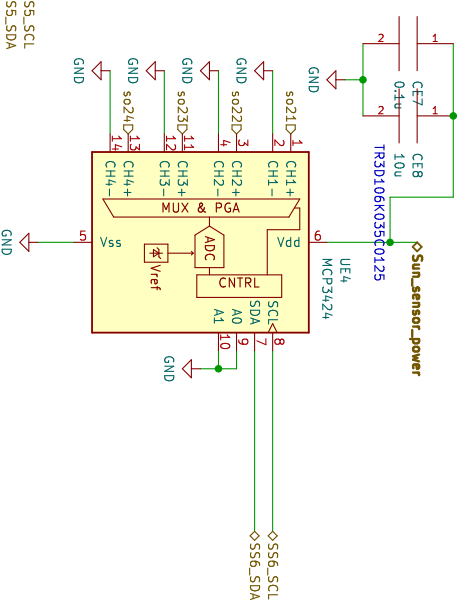
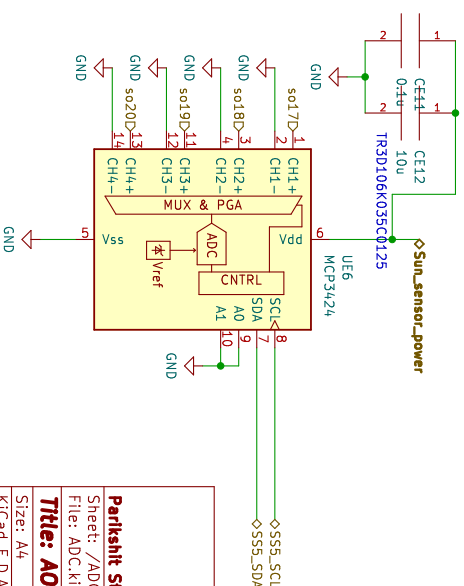
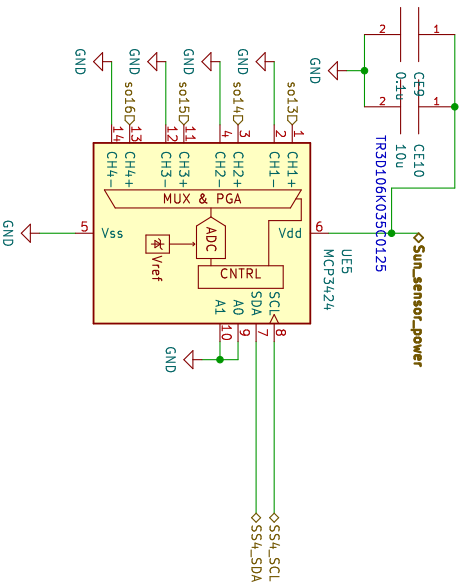
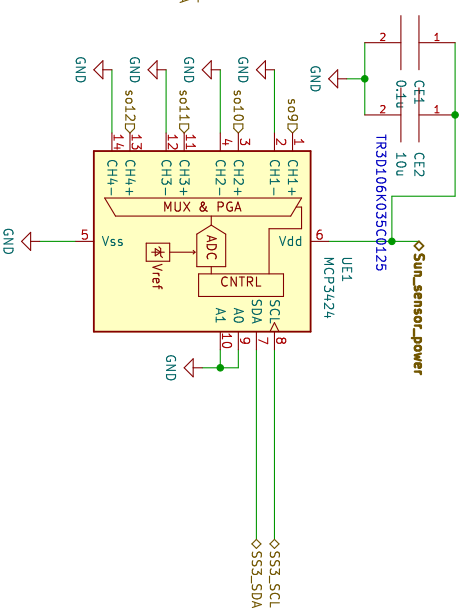
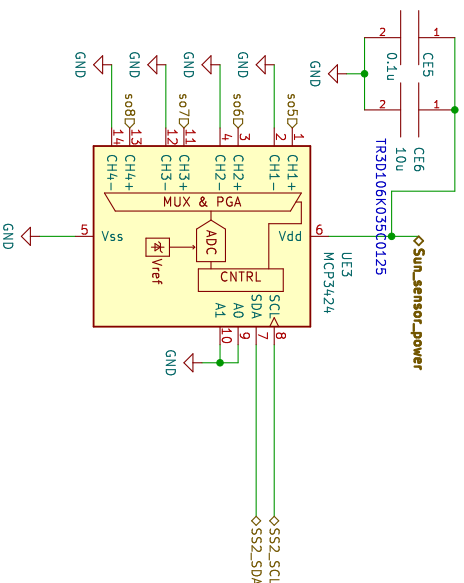
KiCad E.D.A. kicad (6.0.9)

Rev:

Id: 13/16



Decouple with 0.1uF Ceramic cap
and 100F Tantalum cap



Parikshit Student Satellite Team

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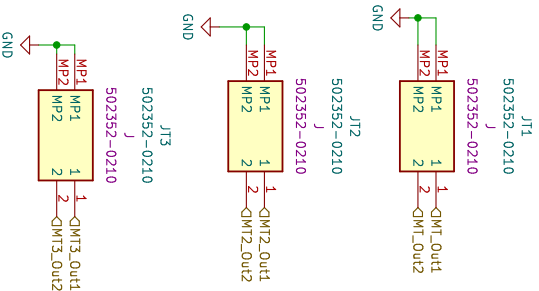
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KiCad E.D.A. kicad (6.0.9)

Rev

Id: 13/16



Perikshit Student Satellite Team
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Size: A4

Date:

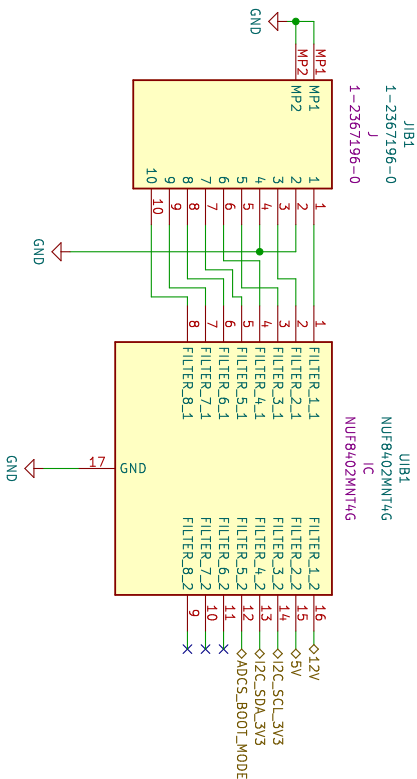
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Rev:

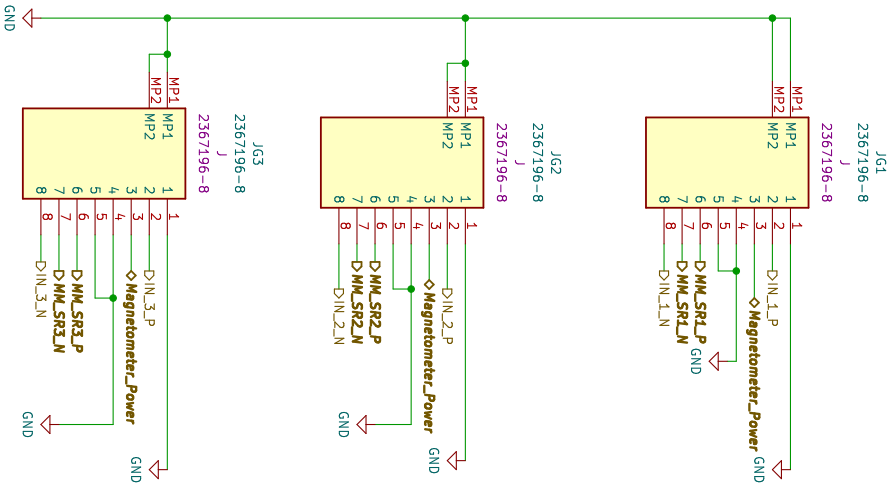
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Sheet Comments :

Route ESD Diode close to connector



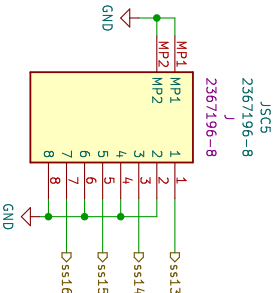
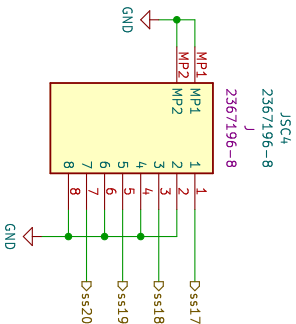
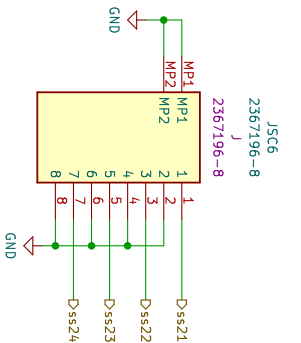
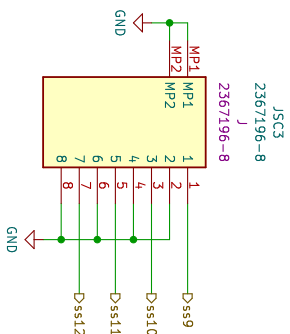
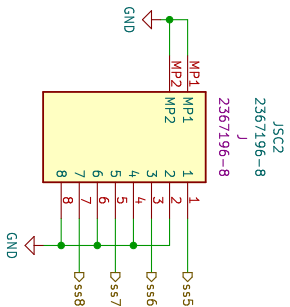
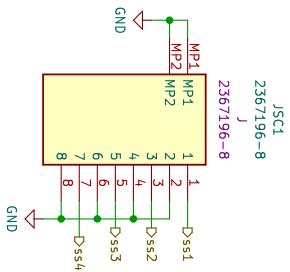
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Perikshit Student Satellite Team
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Title: AOCs BOARD

Size: A4	Date:	Rev:
KiCad E.D.A. kicad (6.0.9)		Id: 15/16



Perikshit Student Satellite Team

Sheet: /SS_Connector/
File: SS_Connector.kicad_sch

Title: AOCs BOARD

Size: A4

Date:

KiCad E.D.A. kicad (6.0.9)

Rev:

Id: 16/16