

# Computer Systems Organisation (CS2.201)

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## 14 June, Monday (Lecture 10) – Conditional Branching

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### Jump Instructions

Jump instructions can be either direct (to a label) or indirect (to a value stored in a register).

There are various jump instructions, both conditional and non-conditional.

Instruction	Synonym	Jump condition	Description
<code>jmp Label</code>		1	Direct jump
<code>jmp *Operand</code>		1	Indirect jump
<code>jz Label</code>	<code>jz</code>	ZF	Equal / zero
<code>jnz Label</code>	<code>jnz</code>	$\sim$ ZF	Not equal / not zero
<code>js Label</code>		SF	Negative
<code>jns Label</code>		$\sim$ SF	Nonnegative
<code>jg Label</code>	<code>jnl</code>	$\sim$ (SF $\wedge$ OF) & $\sim$ ZF	Greater (signed >)
<code>jge Label</code>	<code>jnl</code>	$\sim$ (SF $\wedge$ OF)	Greater or equal (signed >=)
<code>jle Label</code>	<code>jnge</code>	SF $\wedge$ OF	Less (signed <)
<code>jle Label</code>	<code>jng</code>	(SF $\wedge$ OF)   ZF	Less or equal (signed <=)
<code>ja Label</code>	<code>jnbe</code>	$\sim$ CF & $\sim$ ZF	Above (unsigned >)
<code>jae Label</code>	<code>jnb</code>	$\sim$ CF	Above or equal (unsigned >=)
<code>jb Label</code>	<code>jnae</code>	CF	Below (unsigned <)
<code>jbe Label</code>	<code>jna</code>	CF   ZF	Below or equal (unsigned <=)

Figure 1: Jump Instructions

Before linking, the location is passed to the jump instruction using program-relative addressing: the difference between the PC's contents and the jump destination is given. For example, the .o file and its disassembled version could be

```
8: 7e 0d          jle 17
a: 89 d0          mov %edx, %eax
c: d1 f8          sar %eax
e: 29 c2          sub %eax, %edx
10: 8d 14 52        lea (%edx,%edx,2), %edx
13: 85 d2          test %edx, %edx
```

```

15: 7f f3          jg a
17: 89 d0          mov %edx, %eax

```

where, in the first line, 0d indicates that the destination is 0x0a + 0x0d, or 17.

After linking, the absolute addresses are given.

```

804839c: 7e 0d          jle 80483ab
804839e: 89 d0          mov %edx, %eax
80483a0: d1 f8          sar %eax
80483a2: 29 c2          sub %eax, %edx
80483a4: 8d 14 52       lea (%edx,%edx,2), %edx
80483a7: 85 d2          test %edx, %edx
80483a9: 7f f3          jg 804839e
80483ab: 89 d0          mov %edx, %eax

```

## Branches

Consider the C code

```

int absdiff (int x, int y)
{
    if (x < y)
        return y - x;
    else
        return x - y;
}

```

The assembly code is

```

-- x at %ebp+8, y at +12
    movl 8(%ebp), %edx
    movl 12(%ebp), %eax
    cmp  %eax, %edx
    jge  .L2
    subl %edx, %eax
    jmp  .L3
.L2
    subl %eax, %edx
    movl %edx, %eax
.L3
    ret

```

Therefore the general format to convert if t {then} else {else} is, therefore,

```

    if (!t)
        goto false;
    then;
    goto done;
false:

```

```

    else;
done:
...

```

However, if the original code was

```

int absdiff(int x, int y)
{
    return (x < y ? y-x : x-y)
}

```

it is converted to

```

movl 8(%ebp), %edx
movl 12(%ebp), %eax
movl %edx, %ebx
subl %ecx, %ebx
movl %ecx, %ebx
subl %edx, %eax
cmpl $edx, %ecx
cmovl %ebx, %eax

```

where the last statement is a “conditional move”, which is executed if  $x < y$ .

There are analogous conditional move instructions for all operations.

Instruction	Synonym	Move condition	Description
<u>cmovz</u> <u>S, R</u>	<u>cmovz</u>	ZF	Equal / zero
<u>cmovne</u> <u>S, R</u>	<u>cmovnz</u>	~ZF	Not equal / not zero
cmovs S, R		SF	Negative
cmovns S, R		~SF	Nonnegative
cmovg S, R	cmovnle	~(SF ^ OF) & ~ZF	Greater (signed >)
cmovge S, R	cmovnl	~(SF ^ OF)	Greater or equal (signed >=)
cmovl S, R	cmovnge	SF ^ OF	Less (signed <)
cmovle S, R	cmovng	(SF ^ OF)   ZF	Less or equal (signed <=)
cmova S, R	cmovnbe	~CF & ~ZF	Above (unsigned >)
cmovae S, R	cmovnb	~CF	Above or equal (Unsigned >=)
cmovb S, R	cmovnae	CF	Below (unsigned <)
cmovbe S, R	cmovna	CF   ZF	below or equal (unsigned <=)

Figure 2: Conditional Move Instructions