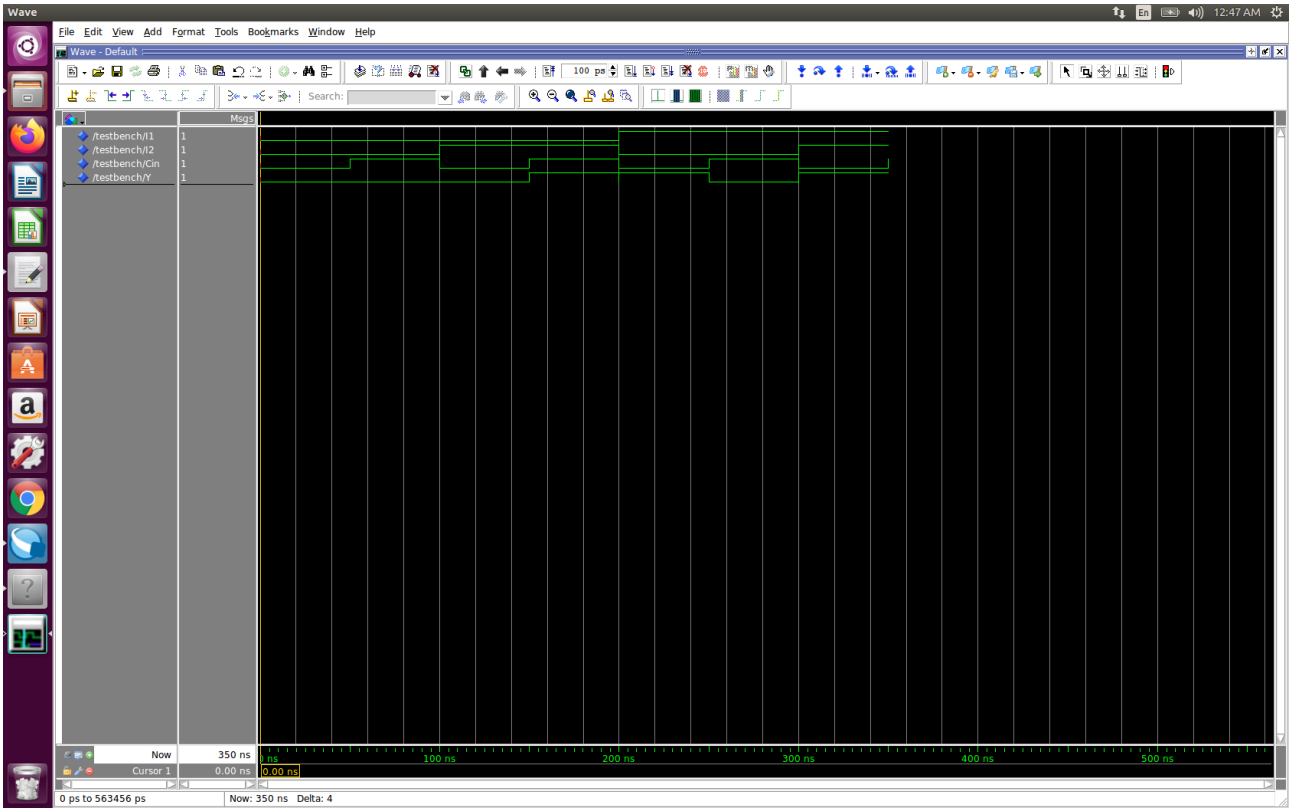
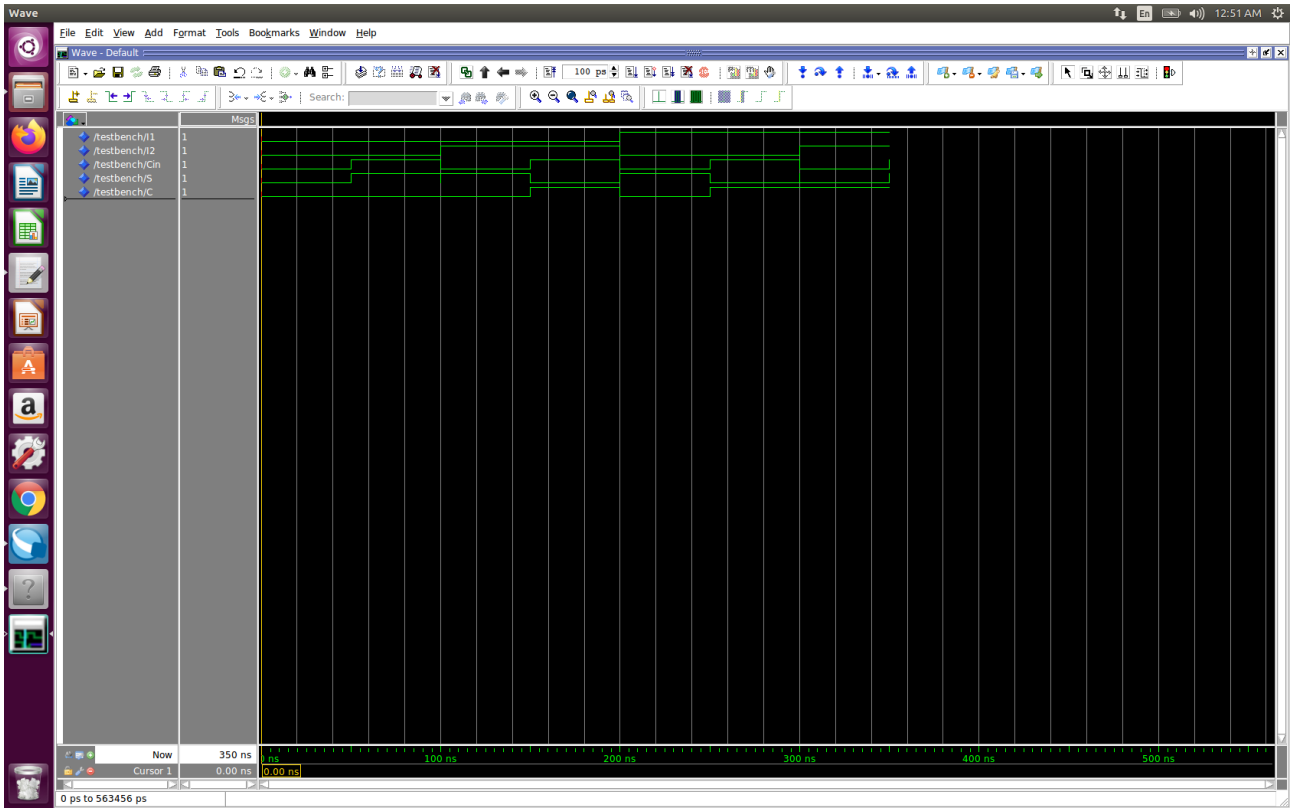
Simulations for HW1:
1. Mux simulation

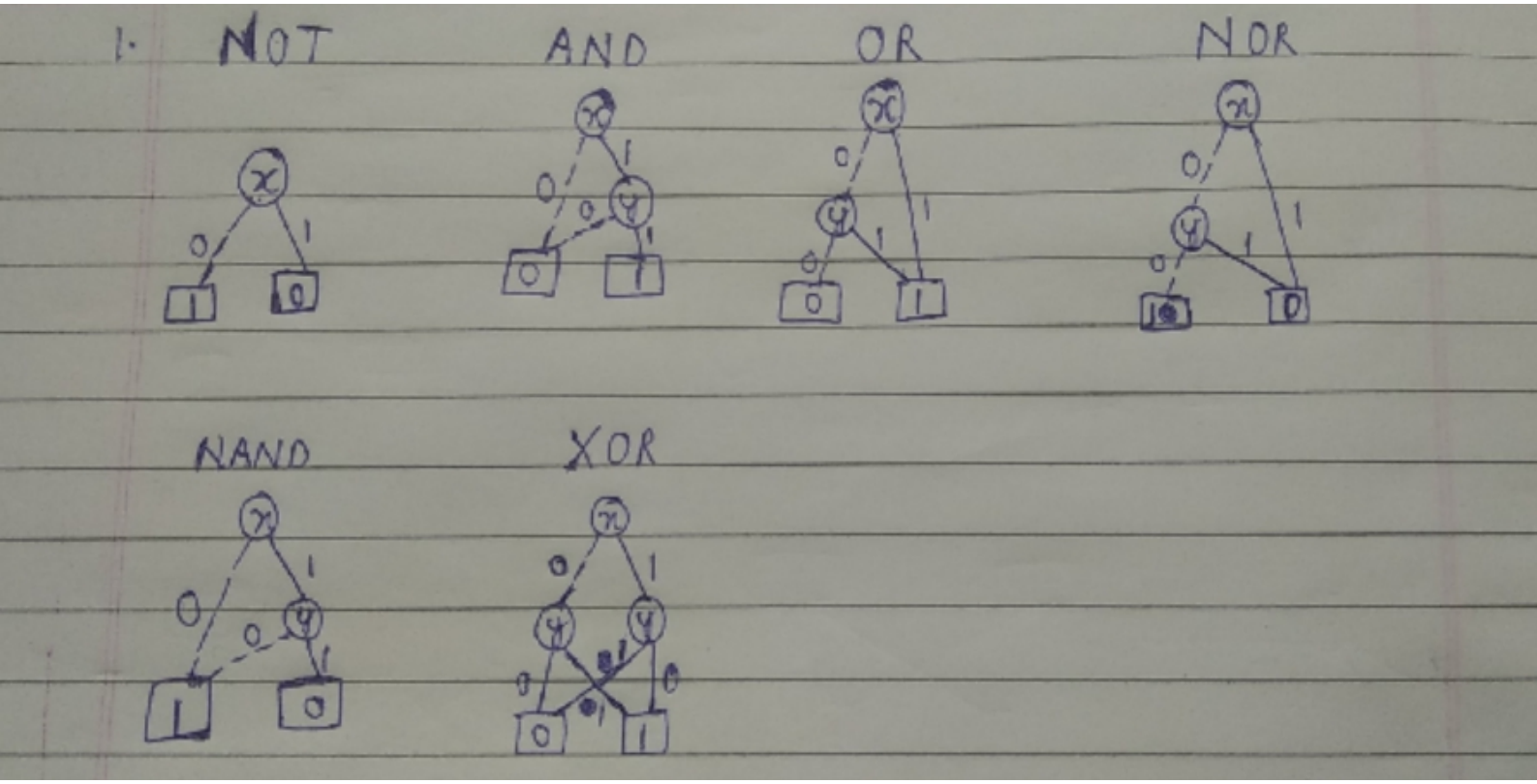


2. Fulladder simulation

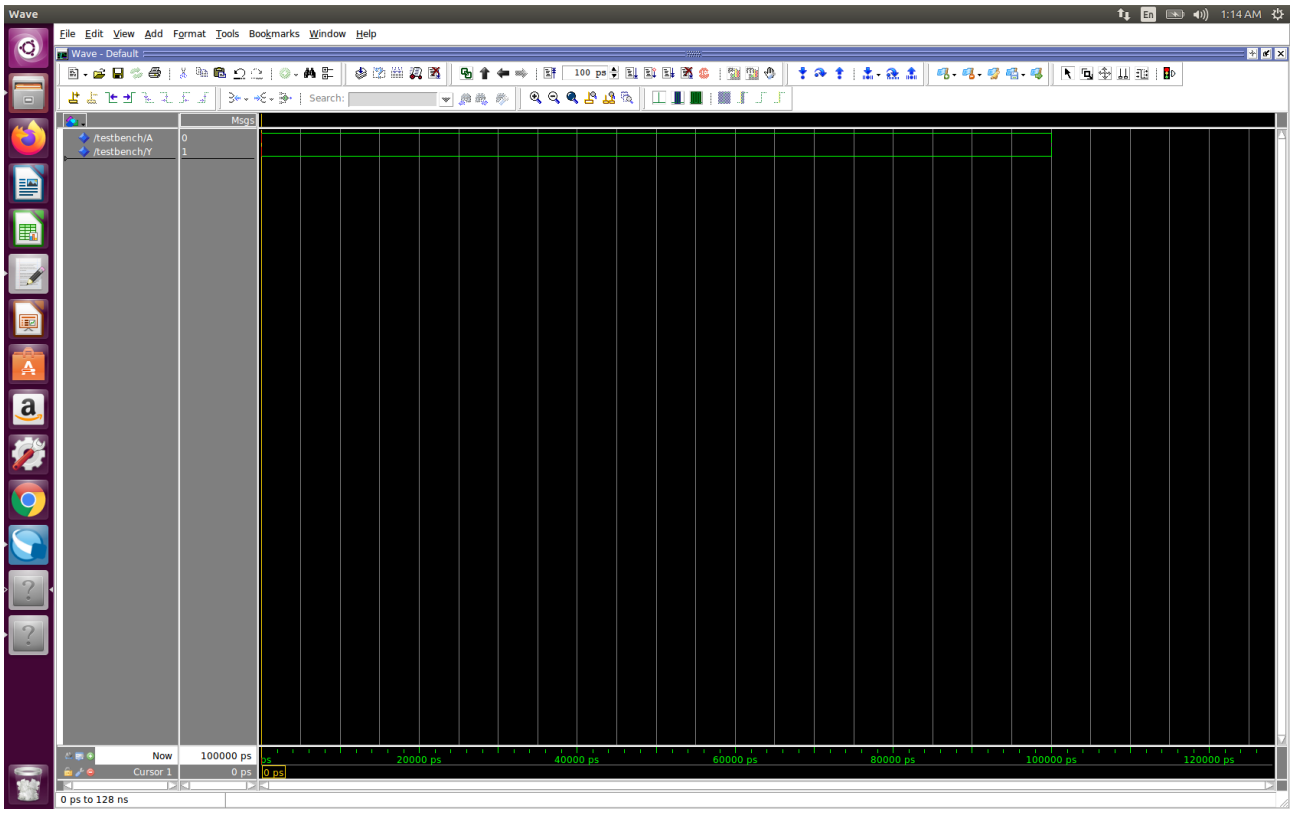


Lab1 BDDs and simulations:

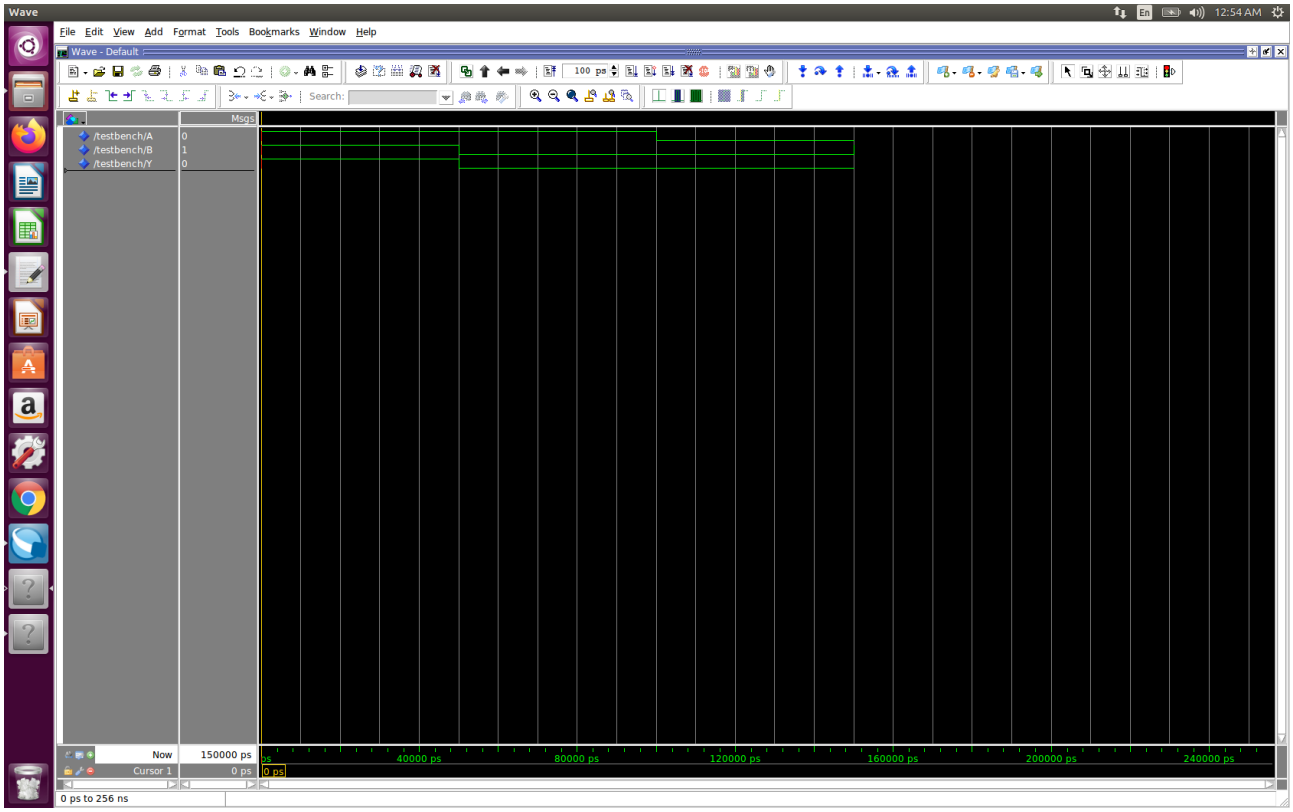
1.



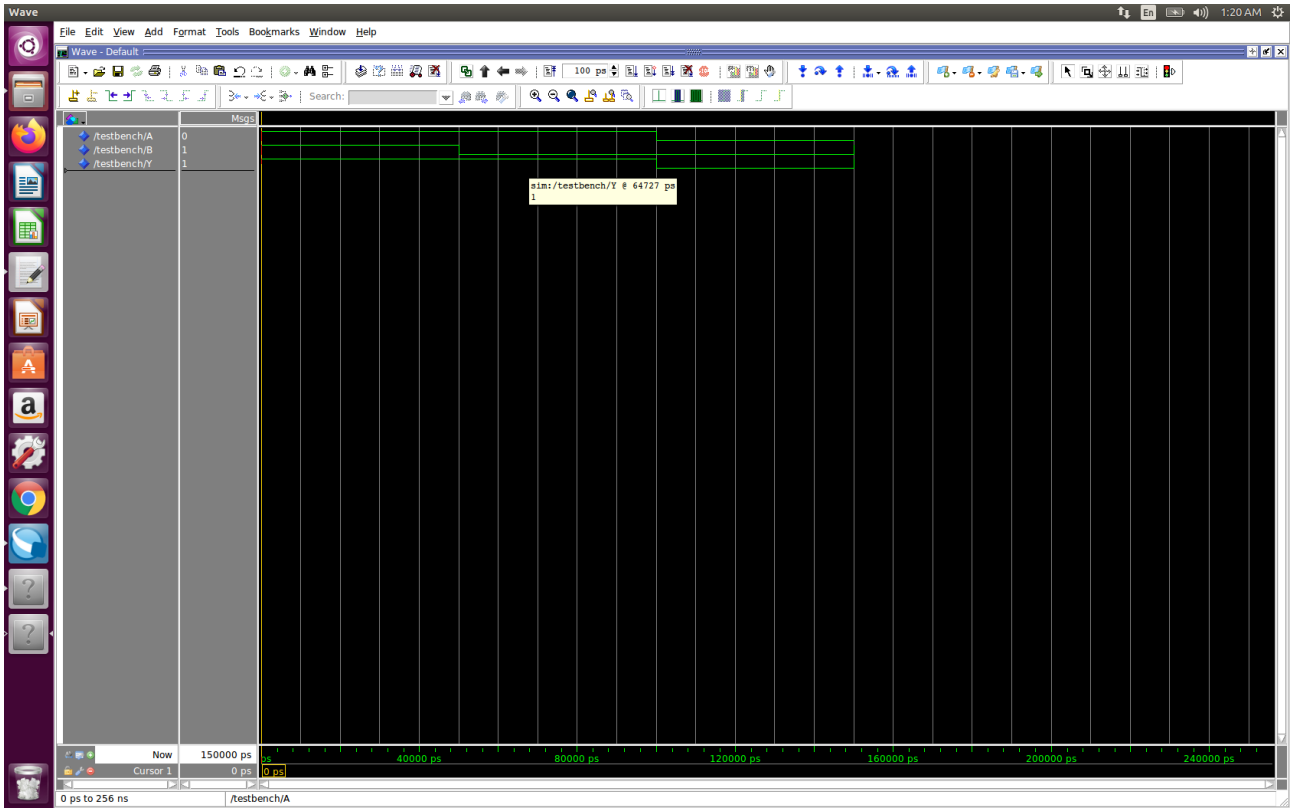
NOT simulation



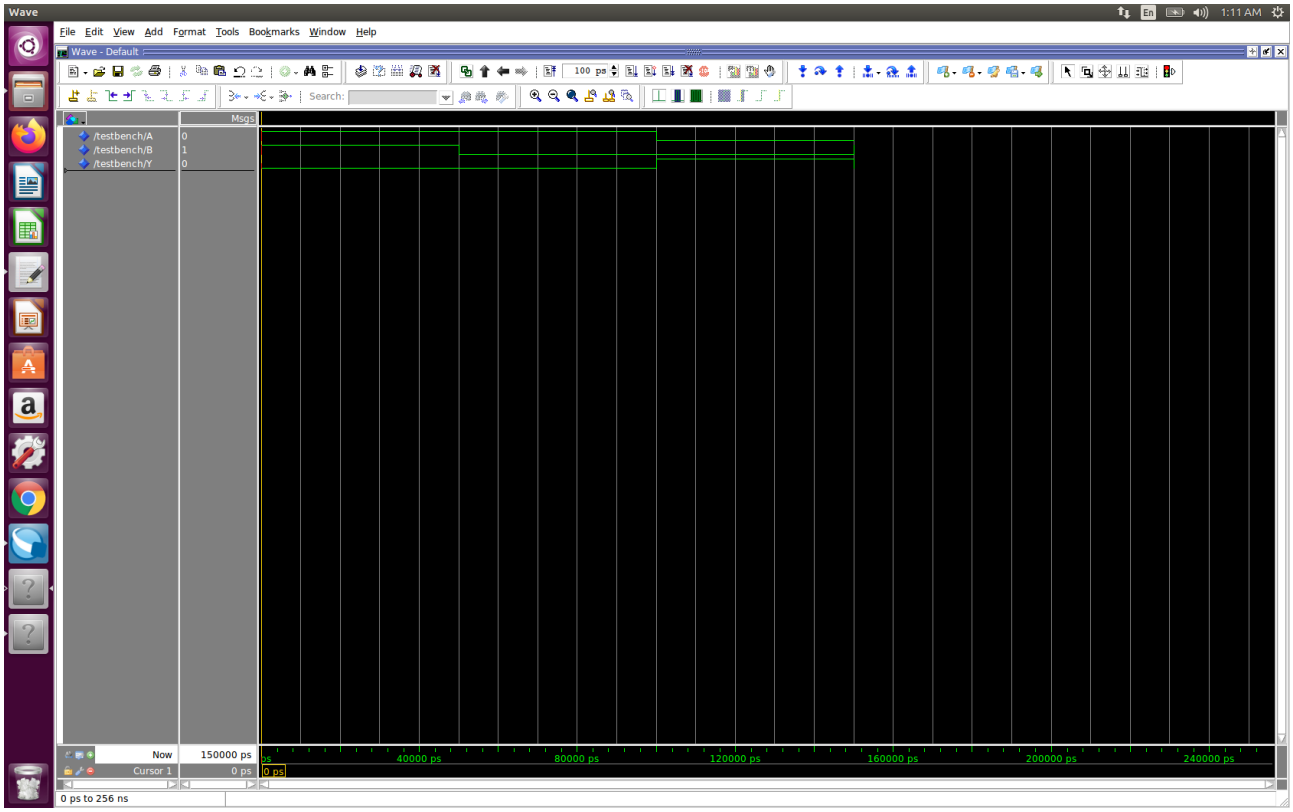
AND simulation



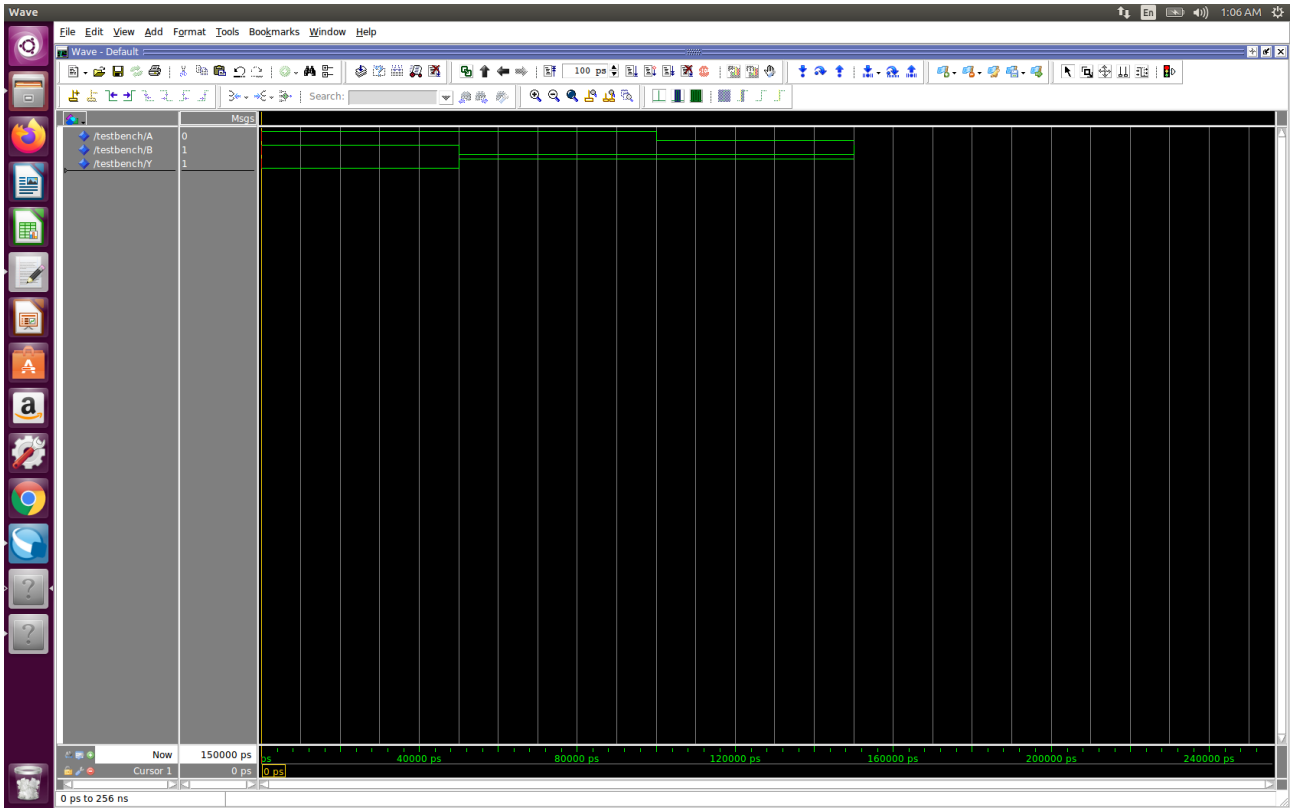
OR simulation



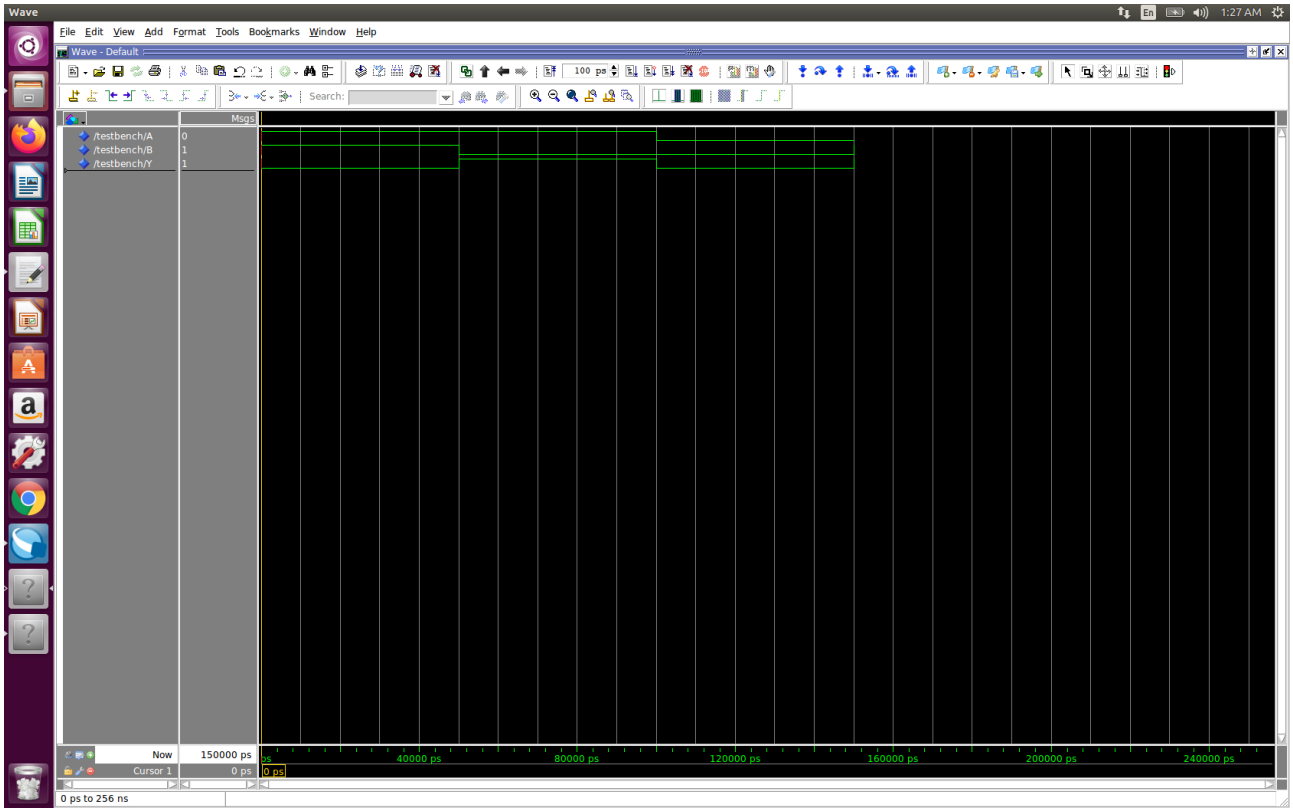
NOR simulation



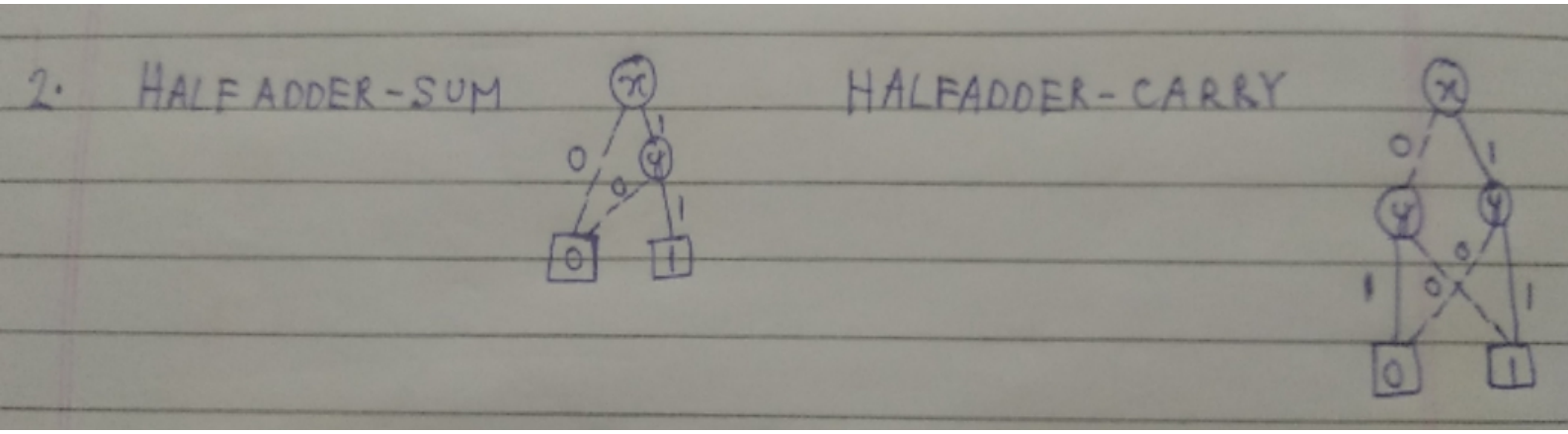
NAND simulation

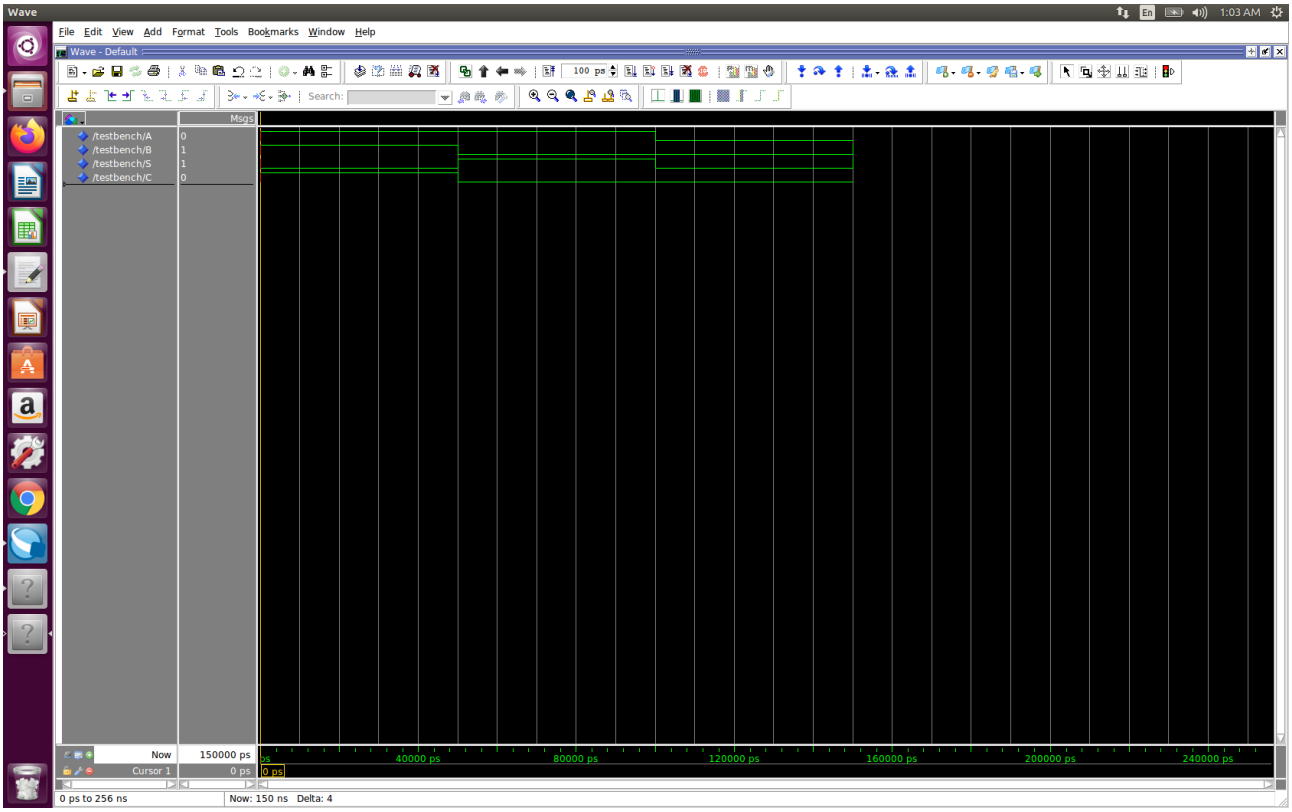


XOR

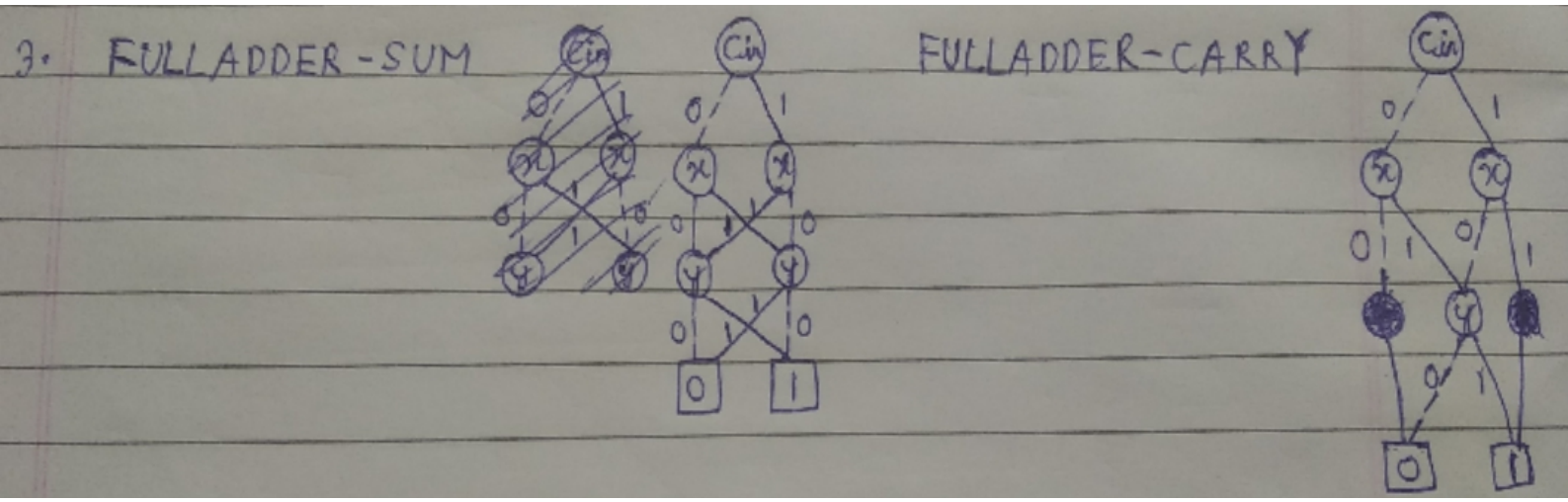

Wave
File Edit View Add Format Tools Bookmarks Window Help
Wave - Default
Search:
Msgs
/testbench/A
/testbench/B
/testbench/Y
0
1
1
Now
150000 ps
Cursor 1
0 ps
40000 ps
80000 ps
120000 ps
160000 ps
200000 ps
240000 ps
0 ps to 256 ns

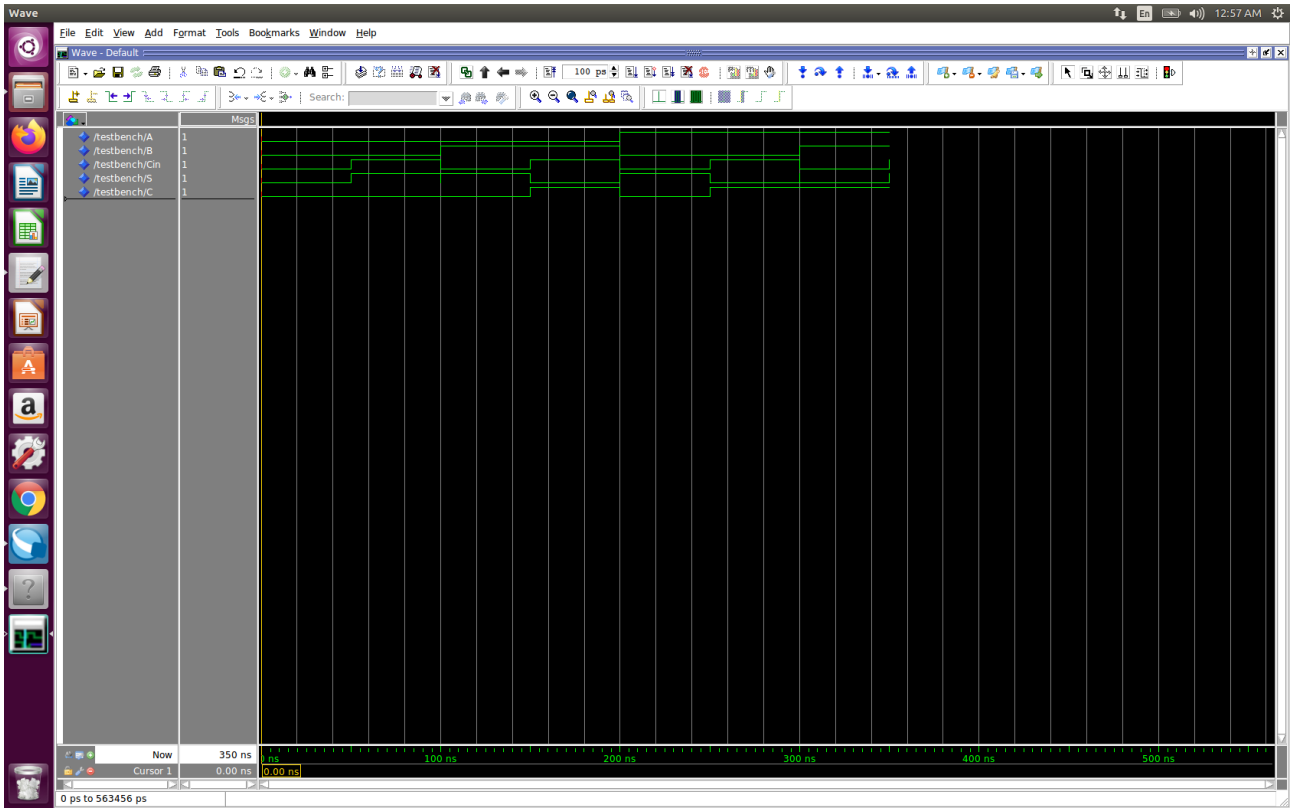
2.


2. HALFADDER-SUM
HALFADDER-CARRY

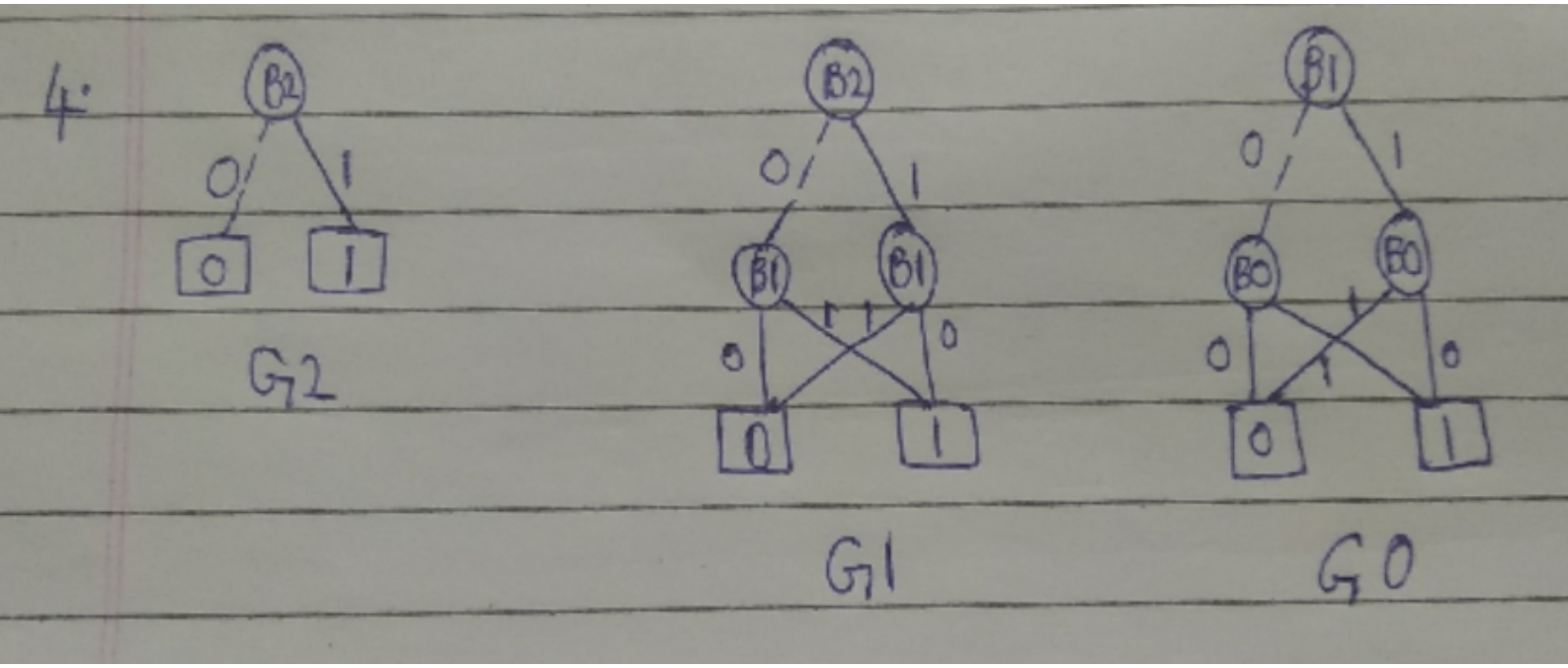

Wave
File Edit View Add Format Tools Bookmarks Window Help
Wave - Default
Search:
Msgs
/testbench/A
/testbench/B
/testbench/S
/testbench/C
0
1
1
0
Now
150000 ps
Cursor 1
0 ps
40000 ps
80000 ps
120000 ps
160000 ps
200000 ps
240000 ps
0 ps to 256 ns
Now: 150 ns Delta: 4

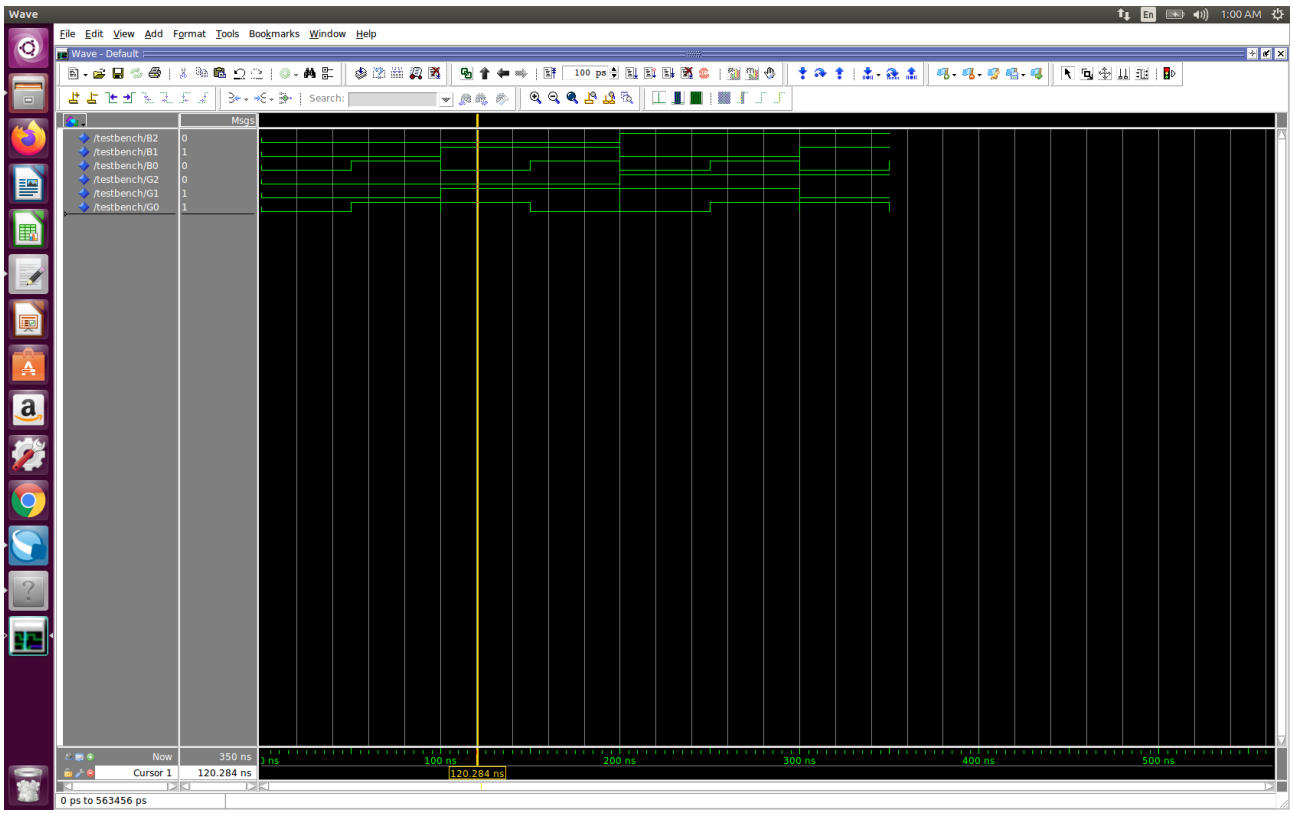
3.


3. FULLADDER-SUM
Cin
x
y
0
1
FULLADDER-CARRY
Cin
x
y
0
1


Wave
File Edit View Add Format Tools Bookmarks Window Help
Wave - Default
Msgs
/testbench/A
/testbench/B
/testbench/Cin
/testbench/S
/testbench/C
Now
350 ns
Cursor 1
0.00 ns
100 ns
200 ns
300 ns
400 ns
500 ns
0 ps to 563456 ps

4.


4.
B2
0
1
0
1
G2
B2
0
1
B1
B1
0
1
G1
B1
0
1
B0
B0
0
1
G0


Wave
File Edit View Add Format Tools Bookmarks Window Help
Wave - Default
Search:
Msgs
/testbench/B2
/testbench/B1
/testbench/B0
/testbench/G2
/testbench/G1
/testbench/G0
0
1
0
0
1
1
Now
350 ns
Cursor 1
120.284 ns
100 ns
200 ns
300 ns
400 ns
500 ns
120.284 ns
0 ps to 563456 ps