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### **COMPILER DESIGN LAB**

**EXP 10 - INTERMEDIATE CODE GENERATOR** 

AIM

To implement a c program for intermediate code generation

# REQUIREMENTS

- 1. Knowledge of the concepts of compiler design and intermediate and generation
- 2. Online GIDB compiler to over and execute

THEORY

In the analysis - synthesis model of relignon a fa bre troof est, relignon a trarelates a seurce program into an independent intermediate code, then the back and of the compiler uses this intermediate code to generate the torget rode which now be understood ca machere,

.code .

- # enlarced portability of the code
- \* ratargeting is facilitated
- \* easier to only source code modification to improve the performance of source code by entiming the intermediate

## ALGORITHM

- 1. Start
- 2- get address rode sequence
- 3. Determine avoient location of 3 using address [for 1st operand]
- 4. If everent location not absorby exist generate more (B, O).
- 5. Update address of A [ for 2nd operand]
- 6. If avoient value of B and () is rull, exist.
  - 7- If they generate exercition () A, 3 ADPR.
  - 8. Store the more instruction in menory.
  - 9- Stop.

### **SCREENSHOTS:**

### **INPUT FILE:**

```
nain.c input.txt : output.txt :

1 + a b t1
2 * c d t2
3 - t1 t2 t
4 = t ? x
```

#### **OUTPUT FILE:**



