

Lecture - 14

- * Recap:
- Blocking assignment examples
 - Non blocking assignment examples

* ~~Summary~~ Summary of Blocking/Non-Blocking

• begin

```
a = #5 b; // a will get value of b after 5
c = #5 a; // c will get the new value of a
end          after 10
```

• begin

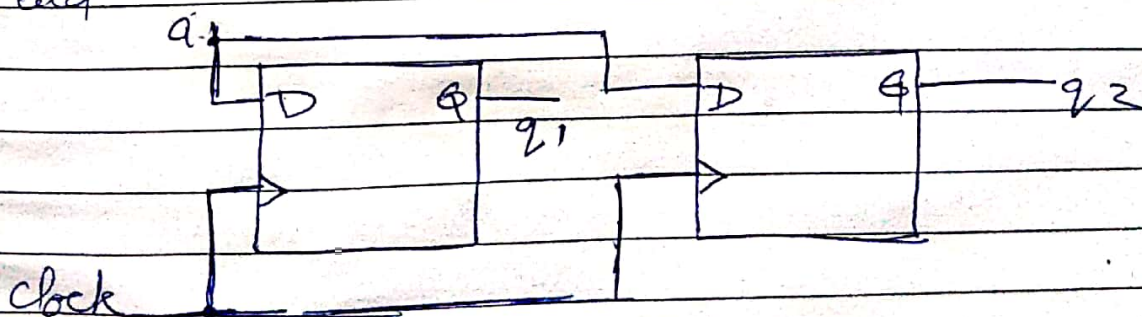
```
a <= #5 b; // 'a' is scheduled to get the
c <= #5 a; // value of 'b' 5 units times
end          // into the future &
              'c' is also scheduled to get
              the value of 'a' 5 units times
              into the future
```

* Example
always @ (posedge clock)

begin

```
q1 = a;
q2 = q1;
```

end



Homework

* Infer the circuits generated by following verilog codes

① always @(posedge clock)
begin

~~q1 = a;~~ q2 = q1;
~~q2 = q1;~~ q1 = a;

end

② always @(posedge clock)
begin

q1 <= a;

q2 <= q1;

end

③ always @(posedge clock)

q2 <= q1;

always @(posedge clock)

q1 <= a;

④ always @(posedge clock)

q1 = a;

always @(posedge clock)

q2 = q1;