97 Statement * Syntan: if boolean expression then sequential statements elset bookean engression then sequential statements sequential statement Ix Enangles: & if sum <= 100 then sum := sum + 18; end of;

* If michel-in then

deposited <= total 10;

els if diwe_in then

deposited <= total 15;

else

deposited <= totalerror;

end if * if drl=1' then if ctal2 = 0' then else

mun_out <= "0001"; if etal2 = 0' then

mun out <= 1000"; end it;

end it;

epeat all at * Repeat all gates very if else statements * Case Statement (Pg. 100) * Syntax: case expression is
when choices => sequential statements
when choices => sequential statements when others => sequential statements

* Example: 4:1 Multiplexer library iece; use jeer stollogic 1164. all; entity MUX41 is port (a, b, c, d: in bustologic; stal enable: in stalogic vector (0 to); z: out stal-logic); end MUX-4-1; architecture MUXBEH of MUX4-1 is constart MUX Delay: time := 10 us; process (a, b, c, d, s) variable temp: stallogic; when "00" => temp:= a; when "01" => temp:= b; when "10" -> temp := C; when "11" => temp:=d: when there > temp:= X end case; 2 <= temp after MUX Delay; end process; end MUXBEH;

* Null Statement, (Pg. 102) * Systax: null; * Ite a seq startement * Does not cause any action to take place * Can be used in if / case statement to emplicitely sperify that no action needs 2 & performed. * Syntan: loop label iterations scheme

for loop

seg tetement; and loop looplabel; * Three types of "iteration schemes": 3) neither of above x Example: factorial:= 1; slocal variable, eniste only inside for loop. for num in 2 to N loop factorial := factorial x num; end loop; * If loopidenifier has some name as a variable outside for loop their inside will use the for loop identifier

2) While while j<20 frem := sum # 2; j'4:= j+3; end loop; 3) No Condition No Condition & loop can only break the uning exit, next or retron * Eg: j:= j+21; frem := from ×10; exist when sum >100; end loop; \$EXIT Statement (Pg. 104) * Systan: enit loop-label when condition; j:= j+21; from := sum x 10; if sun >100 then enit L3;

* Eg: loop wait on A=B; = wait until A=B; exit when A=B; NEXT Statement * Syntan: nent loop label when condition; * nent statement results in skipping the remaining statements in the current iteration of the specified for j in 10 down to 5 of sum < total sum then sum := sum + 2; cleif sum = total sum then end it;

k:=k+1; ----this statement gets skipped it

end loop;

nent is executed. * Generate Fibonacci Series Design a calculator ming VHDL * Design a 12 hour digital clock.