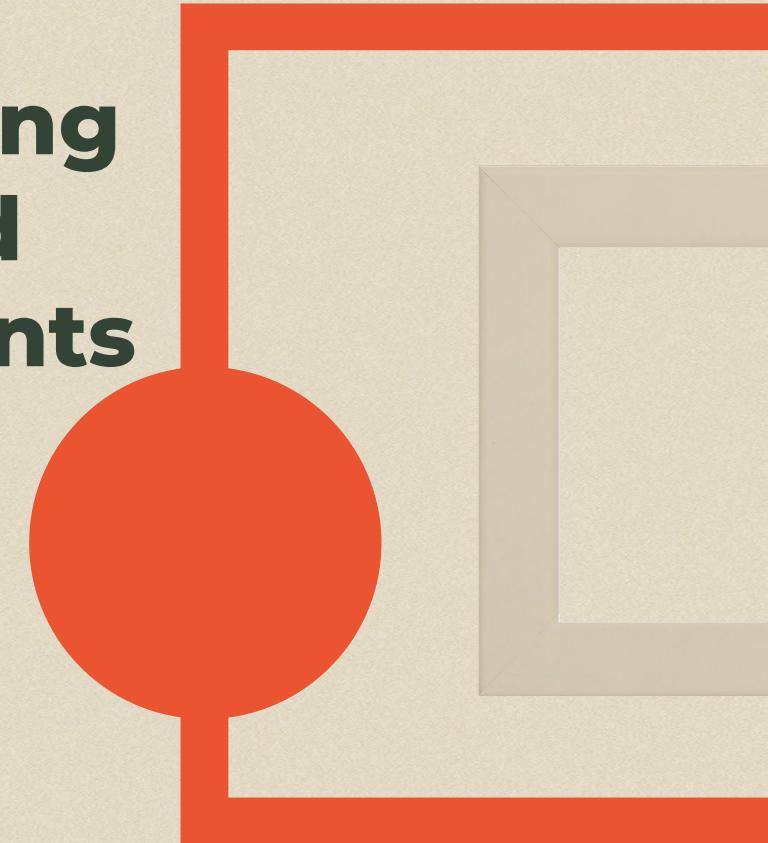


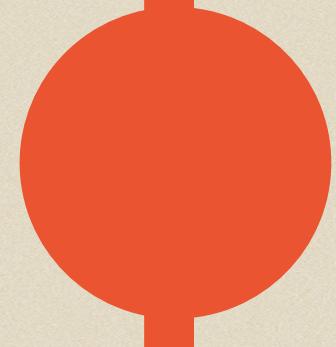
Functional Programming for Securing Cloud and Embedded Environments

Abhiroop Sarkar
Chalmers University



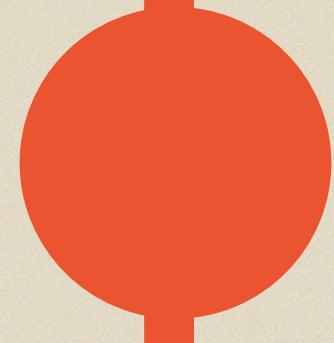
My Research

“Securing Digital Systems
through Programming
Language Techniques”



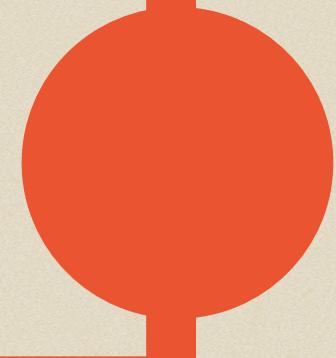
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Programming
Language Techniques”

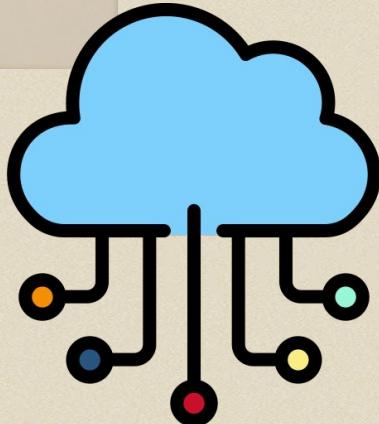


My Research

Digital Systems



My Research



Digital Systems

Cloud Computing

- Multi-Tenant
- Large *Trust Boundary*

My Research



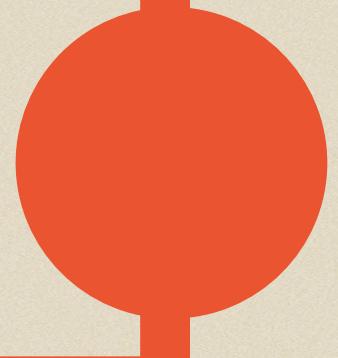
Digital Systems

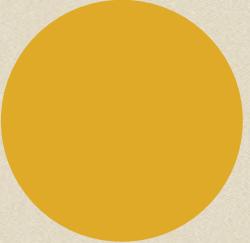
Embedded Systems

- Resource Constrained
- Reactive systems
- Time-bound systems

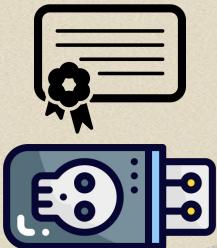
My Research

“Securing Digital Systems





Stuxnet **computer worm**





Microsoft Security Bulletin MS10-046 - Critical

Vulnerability in Windows Shell Could Allow Remote Code Execution (2286198)

Microsoft Security Bulletin MS10-061 - Critical

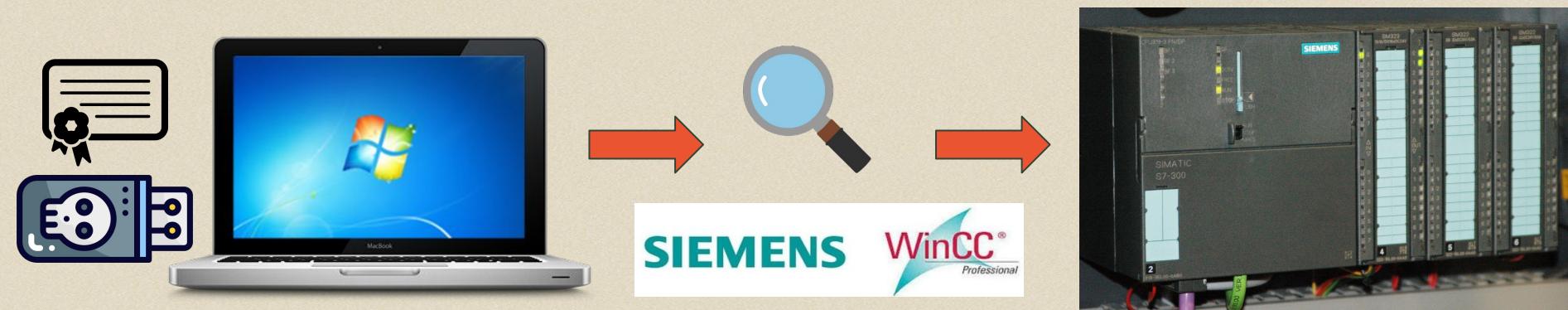
Vulnerability in Print Spooler Service Could Allow Remote Code Execution (2347290)



OS Remote Code Execution



OS Remote Code Execution Detect vulnerable PLC Driver otherwise hide



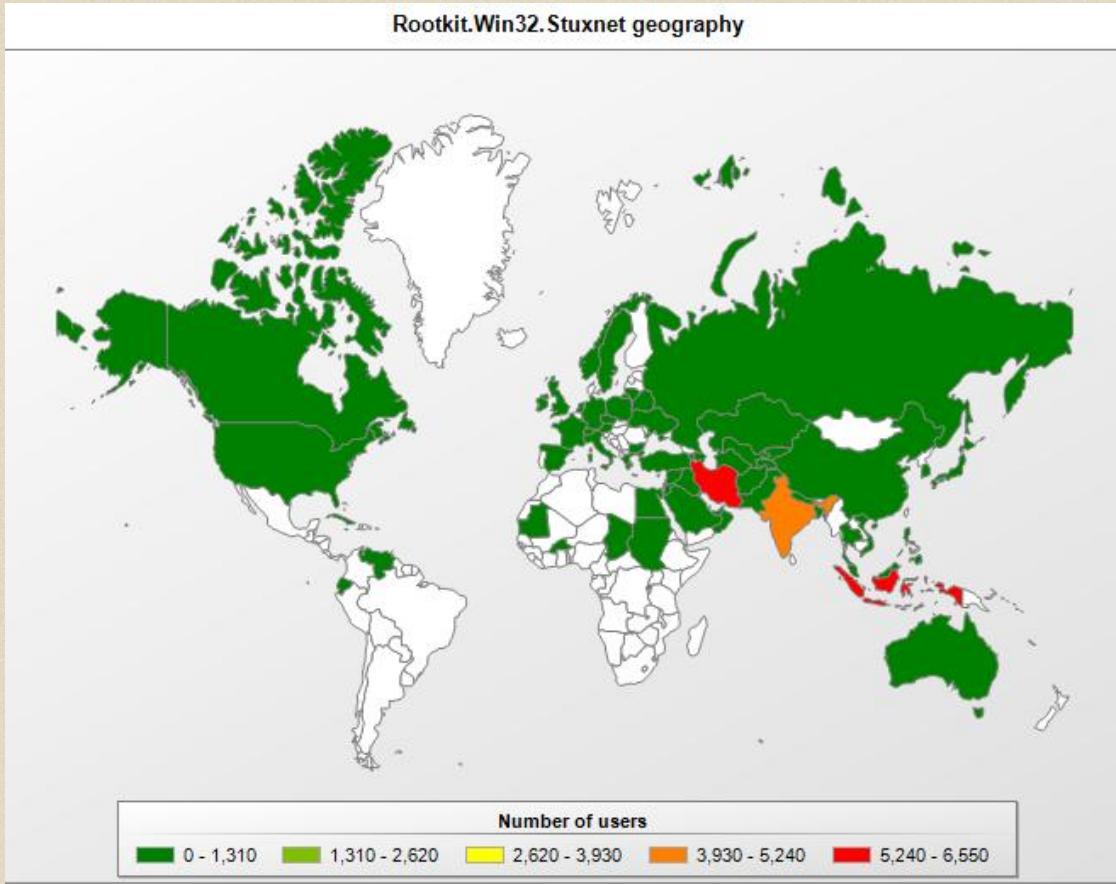
**OS Remote Code
Execution**

**Detect vulnerable PLC
Driver otherwise hide**

**Attack Siemens
PLC 807 - 1210Hz**

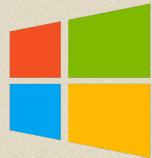
Stuxnet Impact

Rootkit.Win32.Stuxnet geography



ATTACKER MODELS

Attacker Model 1



TRUST

in the OS and other low-level software

ATTACKER MODELS

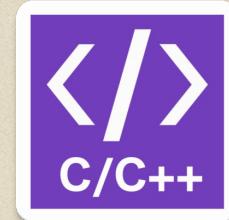
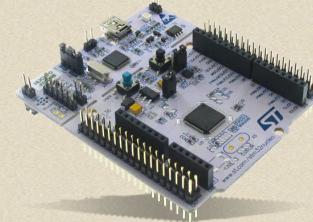
Attacker Model 1



TRUST

in the OS and other low-level software

Attacker Model 2

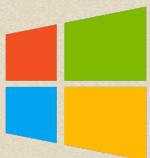


MEMORY UNSAFETY

to accommodate resource constraints

ATTACKER MODELS

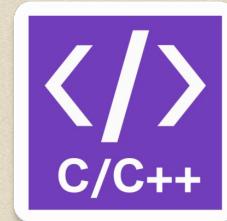
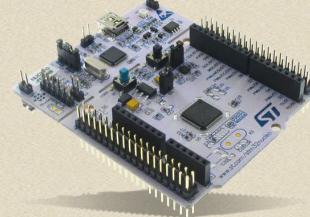
Attacker Model 1



TRUST

in the OS and other low-level software

Attacker Model 2



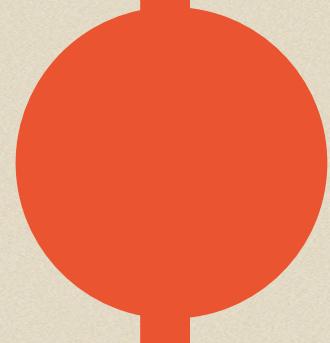
MEMORY UNSAFETY

to accommodate resource constraints

My Research

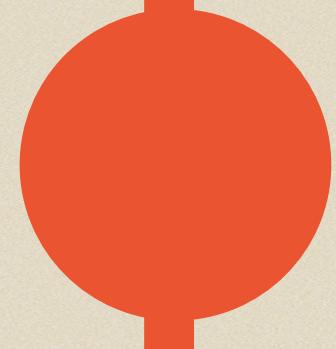
“Securing Digital Systems

”



My Research

“Securing Digital Systems
through Programming
Language Techniques”



ATTACKER MODELS

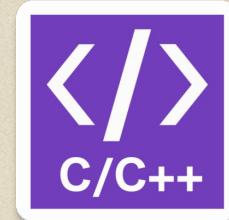
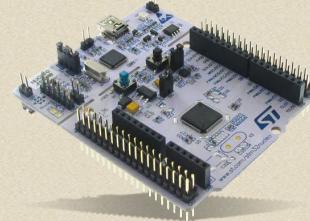
Attacker Model 1



TRUST

in the OS and other low-level software

Attacker Model 2



MEMORY UNSAFETY

to accommodate resource constraints

Contributions

Attacker Model 1



HasTEE⁺

for reducing ***trust*** on
low-level software

Attacker Model 2



SynchronVM

for ***memory-safe, soft real-time***
embedded systems

Part I

HasTEE⁺



HasTEE: Programming Trusted Execution Environments with Haskell

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Abstract

Trusted Execution Environments (TEEs) are hardware enforced memory isolation units, emerging as a pivotal security solution for security-critical applications. TEEs, like Intel SGX and ARM TrustZone, allow the isolation of confidential code and data within an untrusted host environment, such as the cloud and IoT. Despite strong security guarantees, TEE adoption has been hindered by an awkward programming model. This model requires manual application partitioning and the use of error-prone, memory-unsafe, and potentially information-leaking low-level C/C++ libraries.

We address the above with *HasTEE*, a domain-specific language (DSL) embedded in Haskell for programming TEE applications. HasTEE includes a port of the GHC runtime for the Intel-SGX TEE. HasTEE uses Haskell's type system to automatically partition an application and to enforce *Information Flow Control* on confidential data. The DSL, being embedded in Haskell, allows for the usage of higher-order functions, monads, and a restricted set of I/O operations to write any standard Haskell application. Contrary to previous work, HasTEE is lightweight, simple, and is provided as a *simple security library*; thus avoiding any GHC modifications. We show the applicability of HasTEE by implementing case studies on federated learning, an encrypted password wallet, and a differentially-private data clean room.

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Keywords: Trusted Execution Environment, Haskell, Intel SGX, Enclave

ACM Reference Format:

Abhiroop Sarkar, Robert Krook, Alejandro Russo, and Koen Claessen. 2023. HasTEE: Programming Trusted Execution Environments with Haskell. In *Proceedings of the 16th ACM SIGPLAN International Haskell Symposium (Haskell '23), September 8–9, 2023, Seattle, WA, USA*. ACM, New York, NY, USA, 19 pages. <https://doi.org/10.1145/360926.3609731>

1 Introduction

Trusted Execution Environments (TEEs) are an emerging design of hardware-enforced memory isolation units that aid in the construction of security-sensitive applications [Mulligan et al. 2021; Schneider et al. 2022]. TEEs have been used to enforce a strong notion of *trust* in areas such as confidential (cloud-)computing [Baumann et al. 2015; Zegzda et al. 2017], IoT [Lesjak et al. 2015] and Blockchain [Bao et al. 2020]. Intel and ARM each have their own TEE implementations known as Intel SGX [Intel 2015] and ARM TrustZone [ARM 2004], respectively. Principally, TEEs provide a *disjoint* region of code and data memory that allows for the physical isolation of a program's execution and state from the underlying operating system, hypervisor, and I/O peripherals. For

HasTEE⁺: Confidential Cloud Computing and Analytics with Haskell

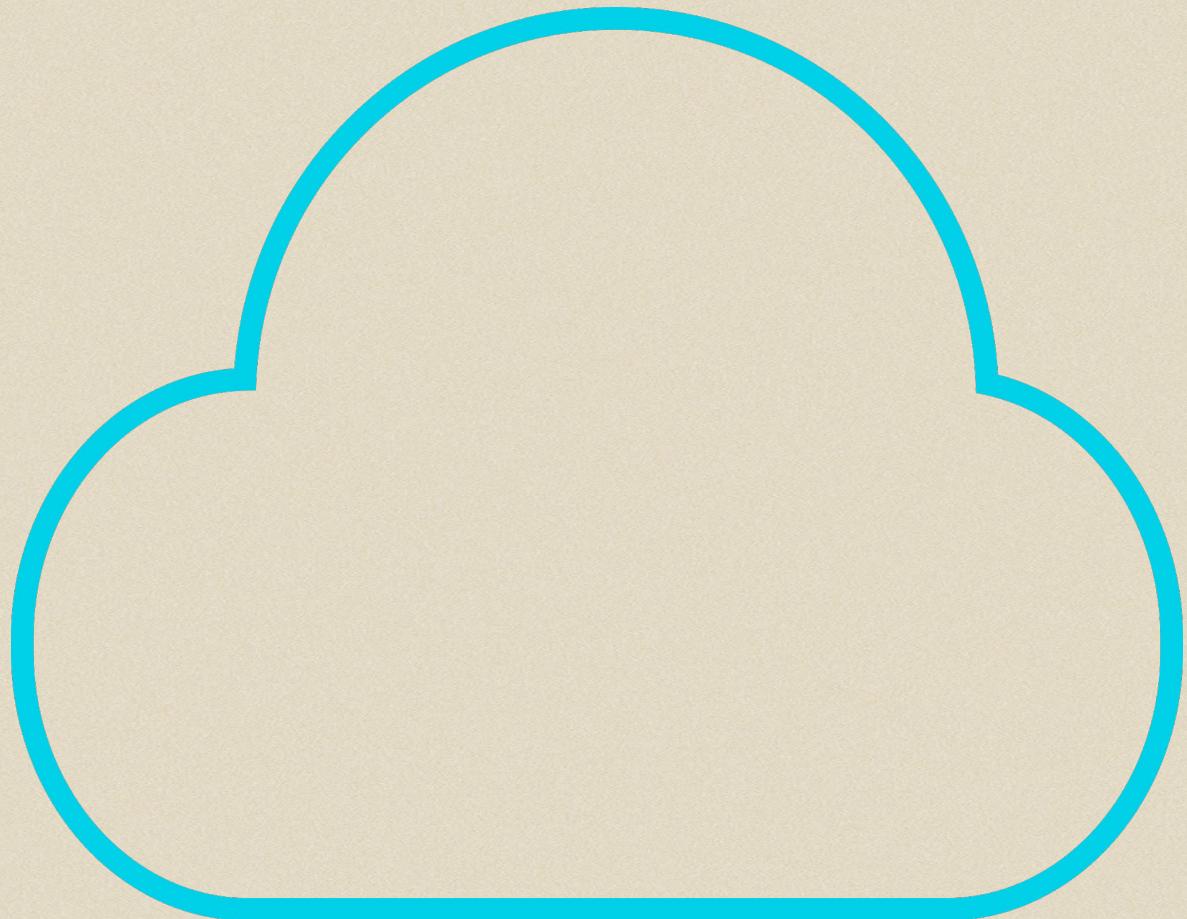
Abhiroop Sarkar^[0000-0002-8991-9472] and Alejandro Russo^[0000-0002-4338-6316]

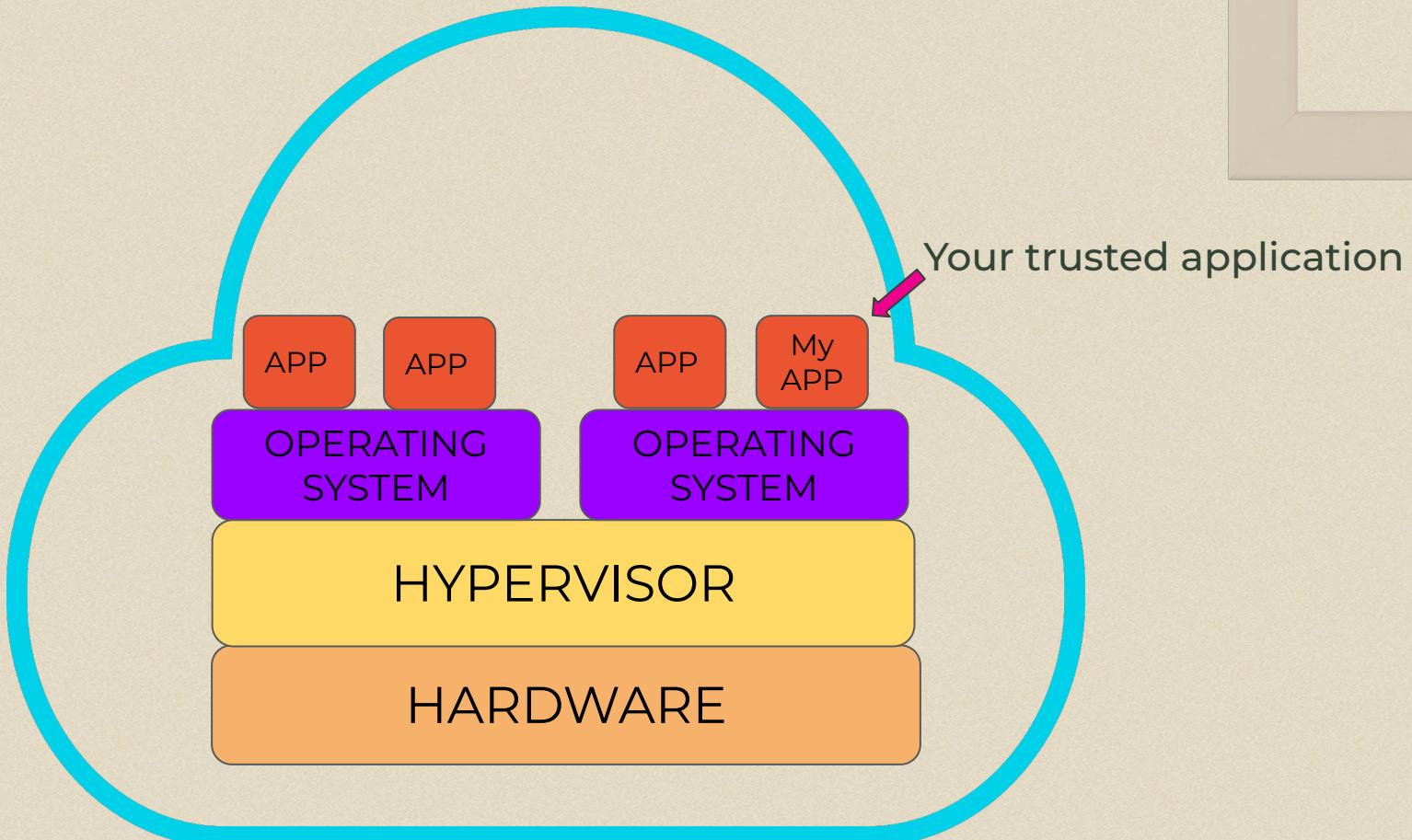
Chalmers University, Gothenburg, Sweden
{sarkara,russo}@chalmers.se

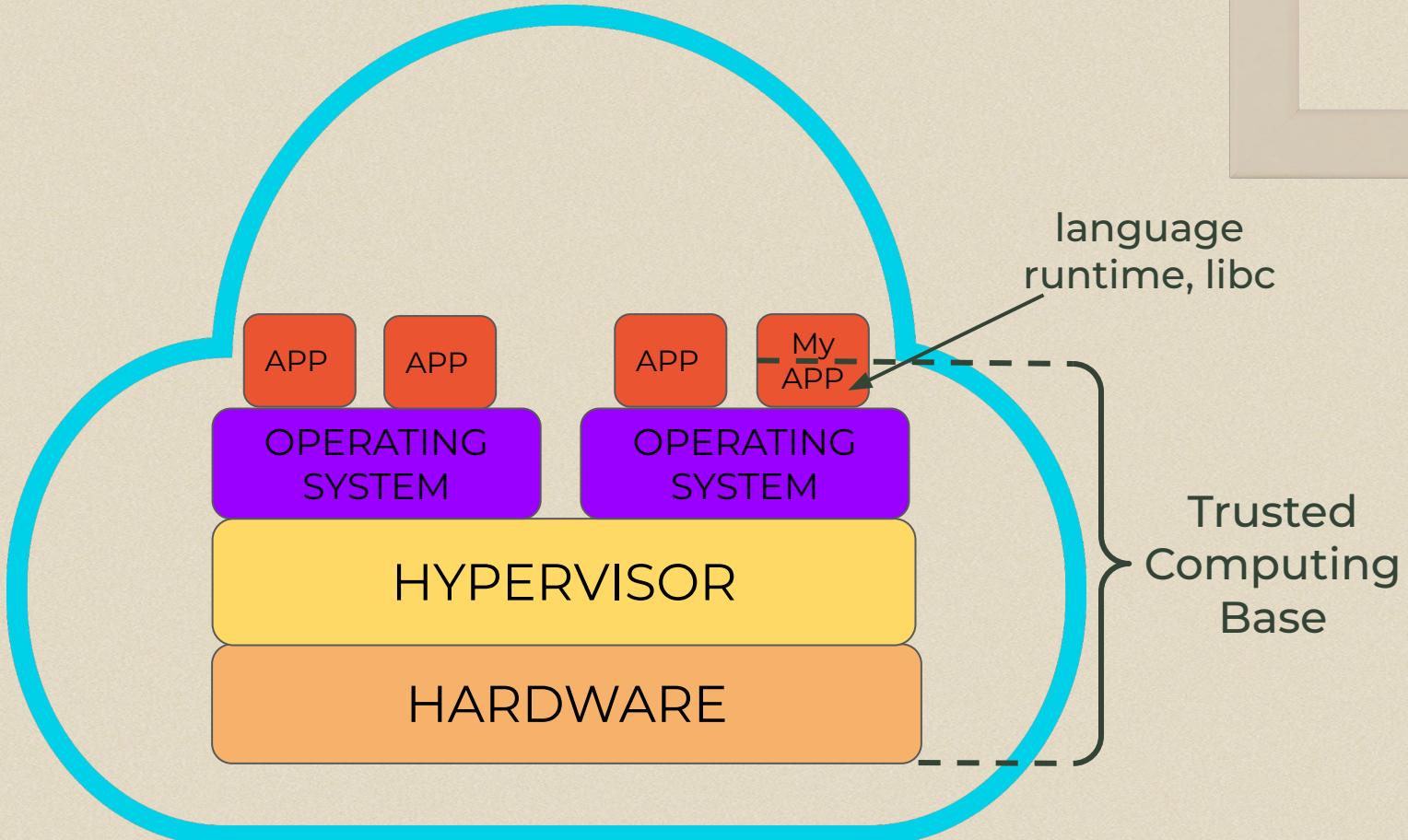
Abstract. Confidential computing is a security paradigm that enables the protection of confidential code and data in a co-tenanted cloud deployment using specialized hardware isolation units called Trusted Execution Environments (TEEs). By integrating TEEs with a Remote Attestation protocol, confidential computing allows a third party to establish the integrity of an *enclave* hosted within an untrusted cloud. However, TEE solutions, such as Intel SGX and ARM TrustZone, offer low-level C/C++-based toolchains that are susceptible to inherent memory safety vulnerabilities and lack language constructs to monitor explicit and implicit information-flow leaks. Moreover, the toolchains involve complex multi-project hierarchies and the deployment of hand-written attestation protocols for verifying *enclave* integrity.

We address the above with HasTEE⁺, a domain-specific language (DSL) embedded in Haskell that enables programming TEEs in a high-level language with strong type-safety. HasTEE⁺ assists in multi-tier cloud application development by (1) introducing a *tierless* programming model for expressing distributed client-server interactions as a single program, (2) integrating a general remote-attestation architecture that removes the necessity to write application-specific cross-cutting attestation code, and (3) employing a dynamic information flow control mechanism to prevent explicit as well as implicit data leaks. We demonstrate the practicality of HasTEE⁺ through a case study on confidential data analytics, presenting a data-sharing pattern applicable to mutually distrustful participants and providing overall performance metrics.

Keywords: Confidential Computing · Trusted Computing · Trusted Execution Environments · Information Flow Control · Attestation · Haskell.







Hypervisor/OS Vulnerabilities

{* VIRTUALIZATION *}

Hyper-V bug that could crash 'big portions of Azure cloud infrastructure': Code published

Now patched
dereference 1

Tim Anderson · Wed.

“Most serious” Linux privilege-escalation bug ever is under active exploit (updated)

Lurking in **VULNERABILITIES**

DAN GOODIN ·

Decade-Old VENOM Bug Exposes Virtualized Environments to Attacks

Security firm Cro **NEWS**

Critical Xen hypervisor flaw endangers virtualized environments

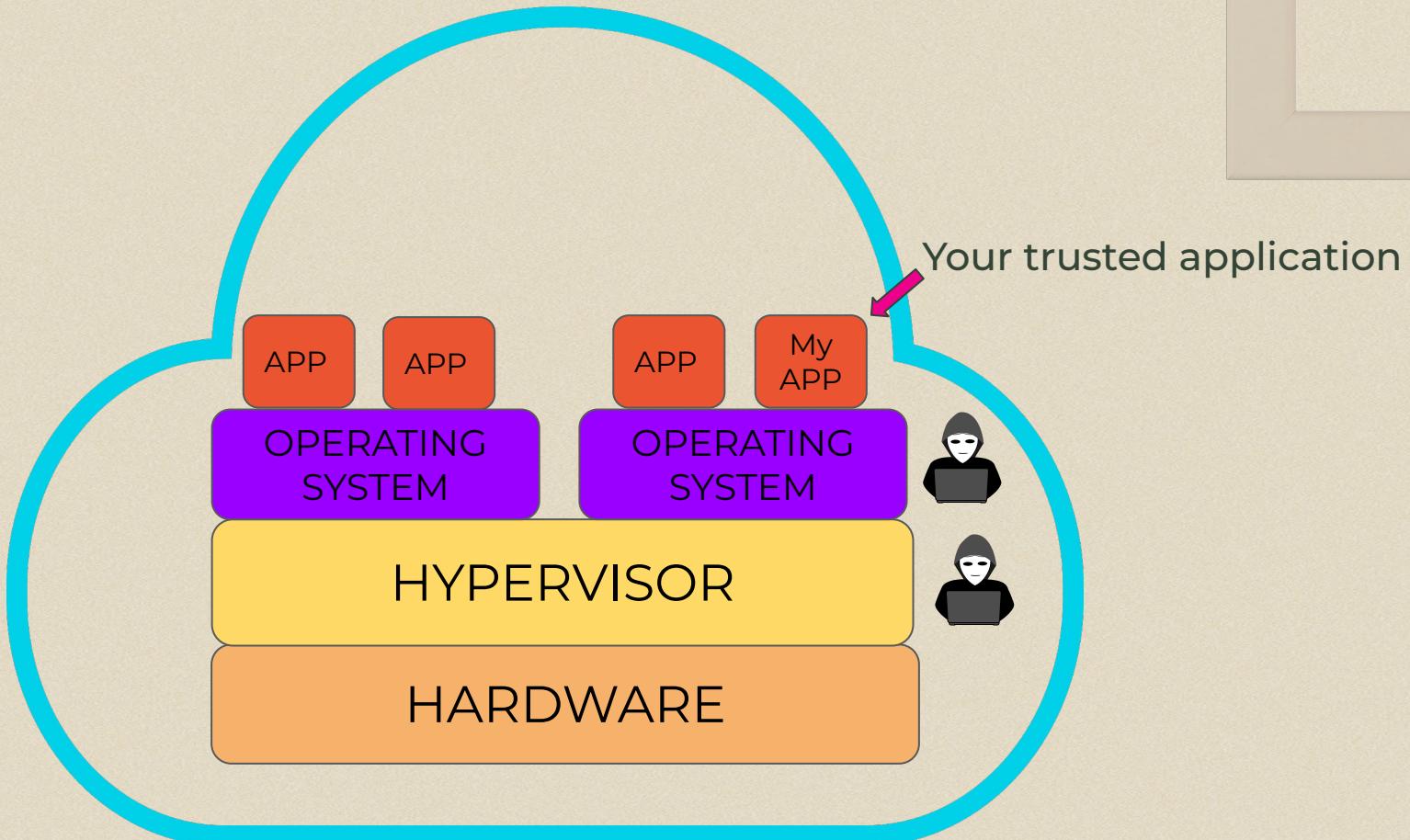
The vulnera **NETWORK SECURITY**

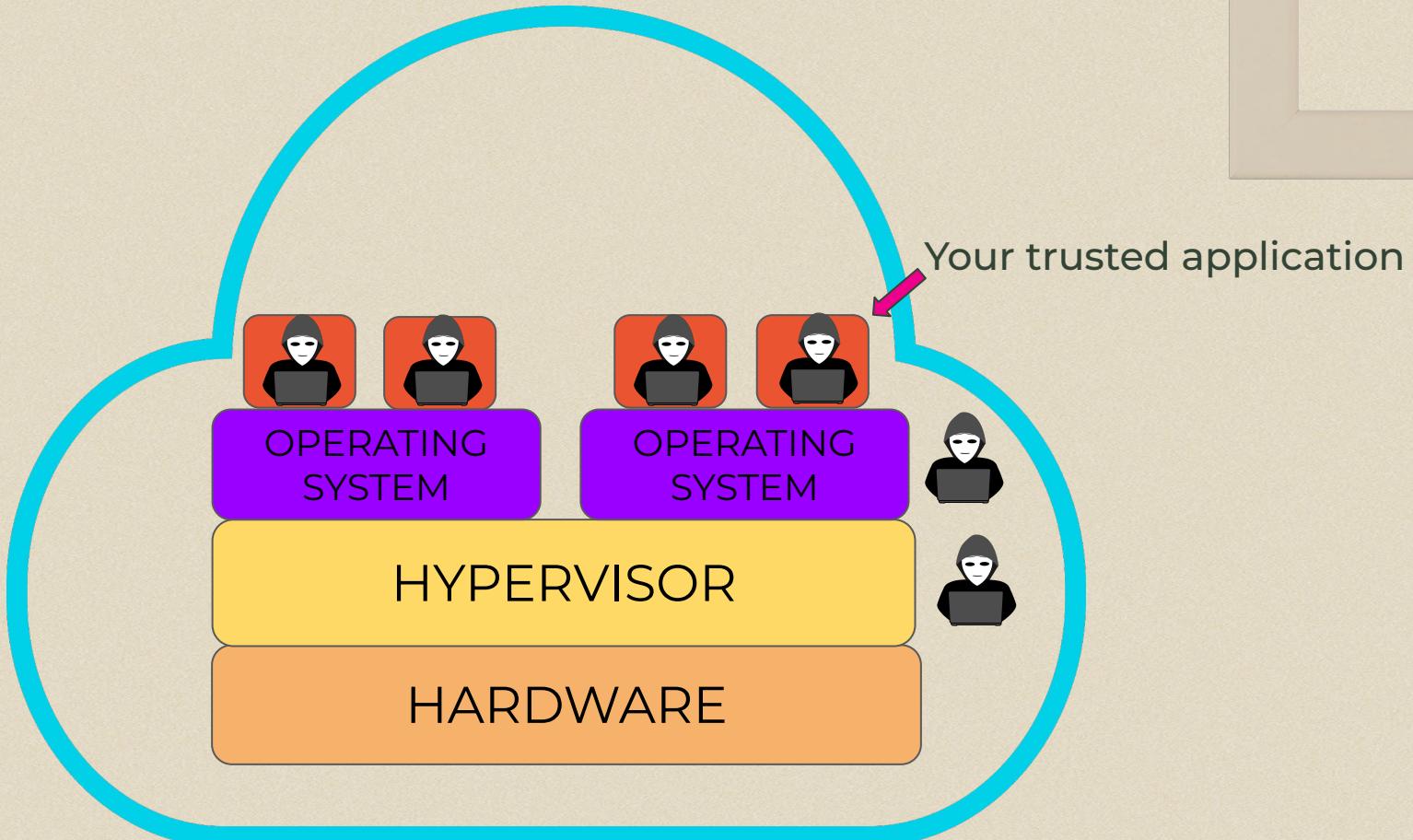
Microsoft Ships Urgent Fixes for Critical Flaws in Windows Kerberos, Hyper-V

Patch 1

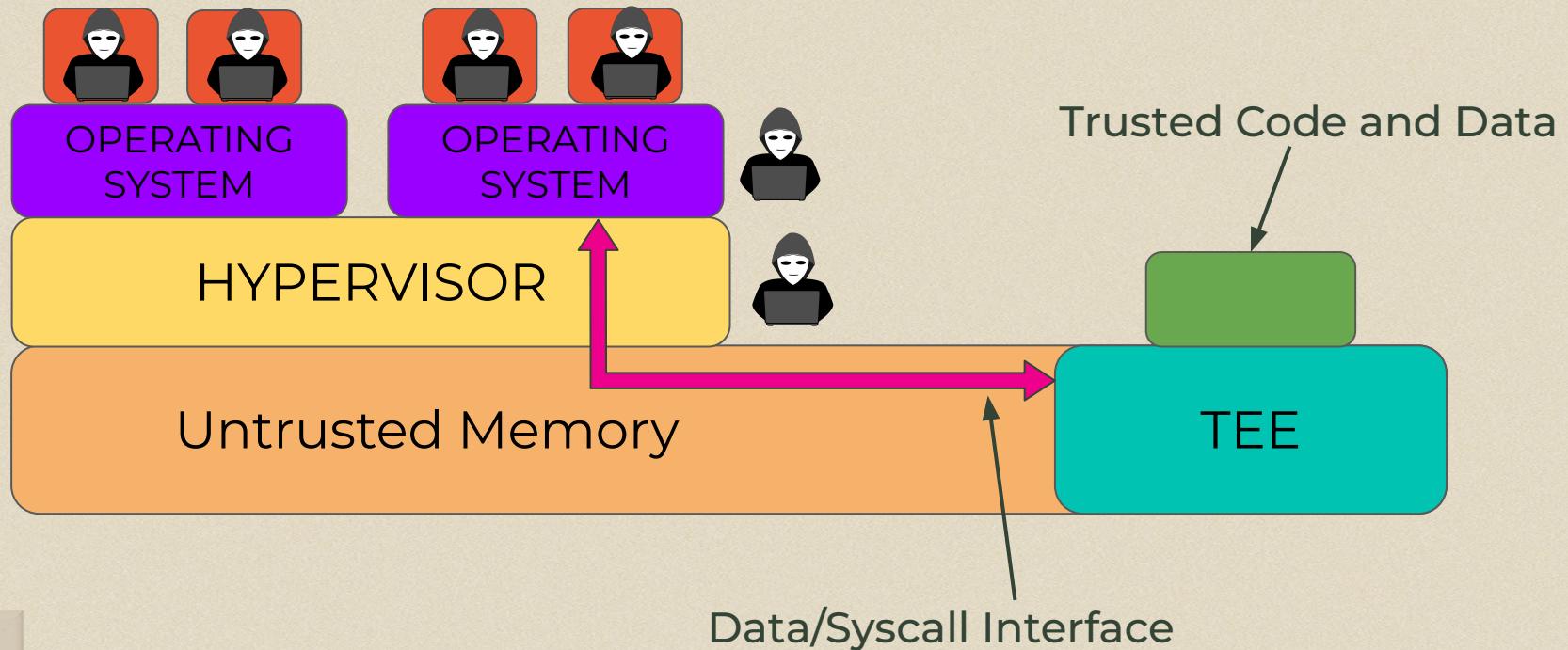
Home > News > Cloud

Hypervisor security flaw could expose AWS, Azure

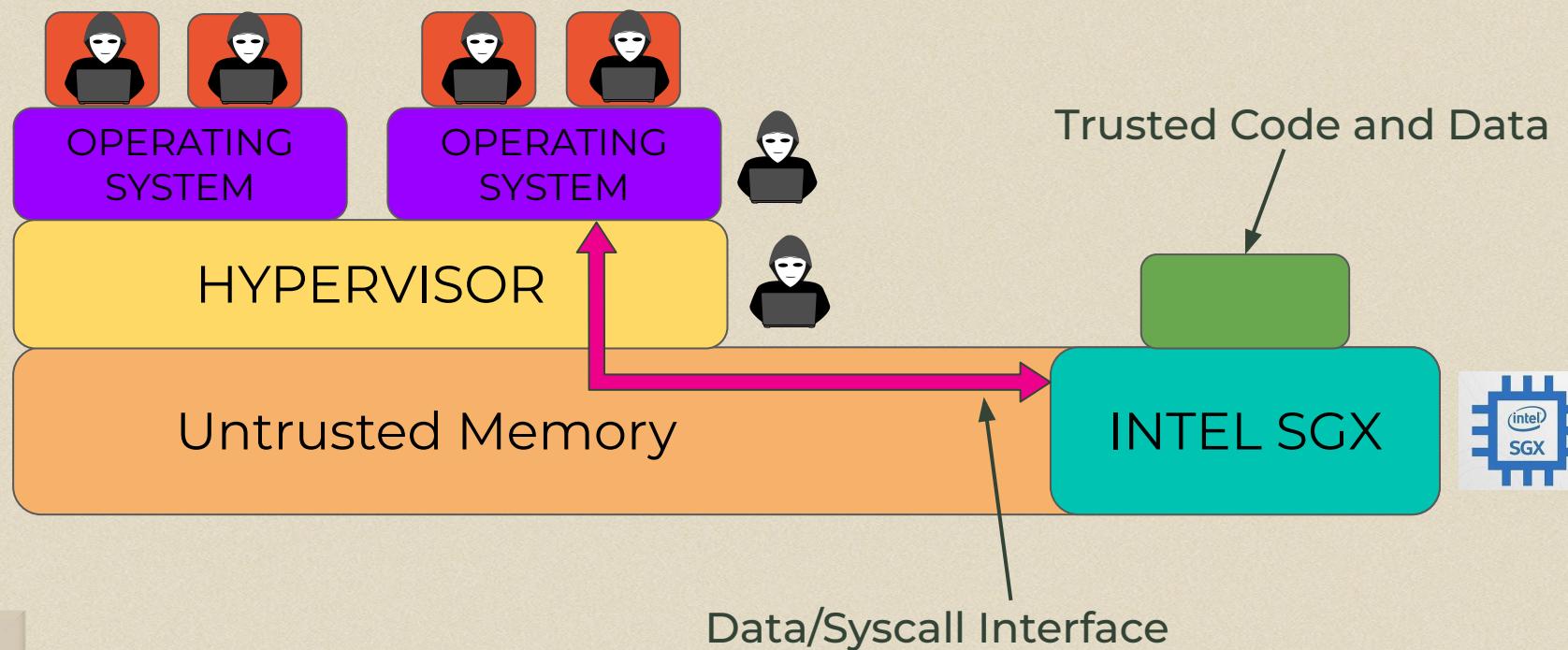




Trusted Execution Environments (TEE)

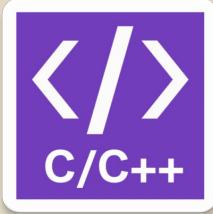


Trusted Execution Environments (TEE)

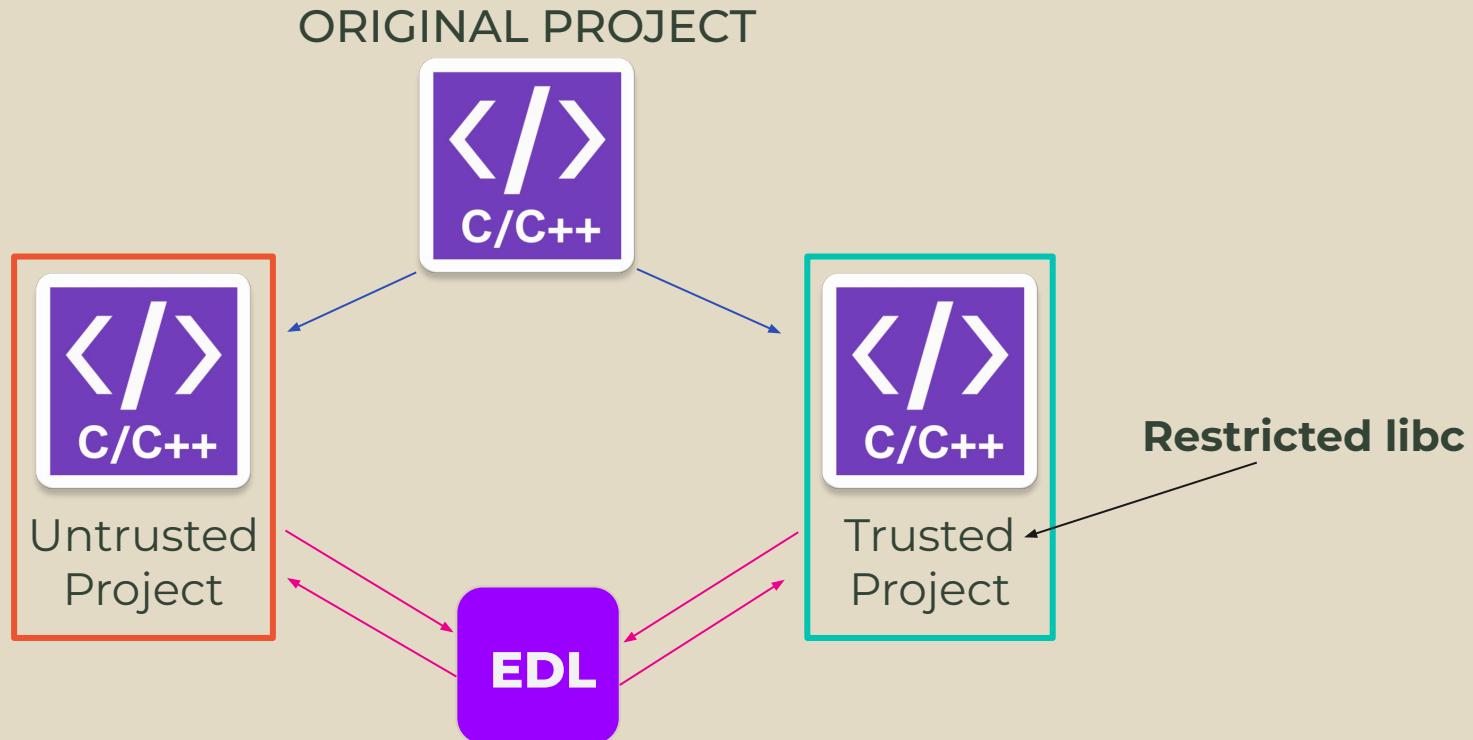


Programming TEEs

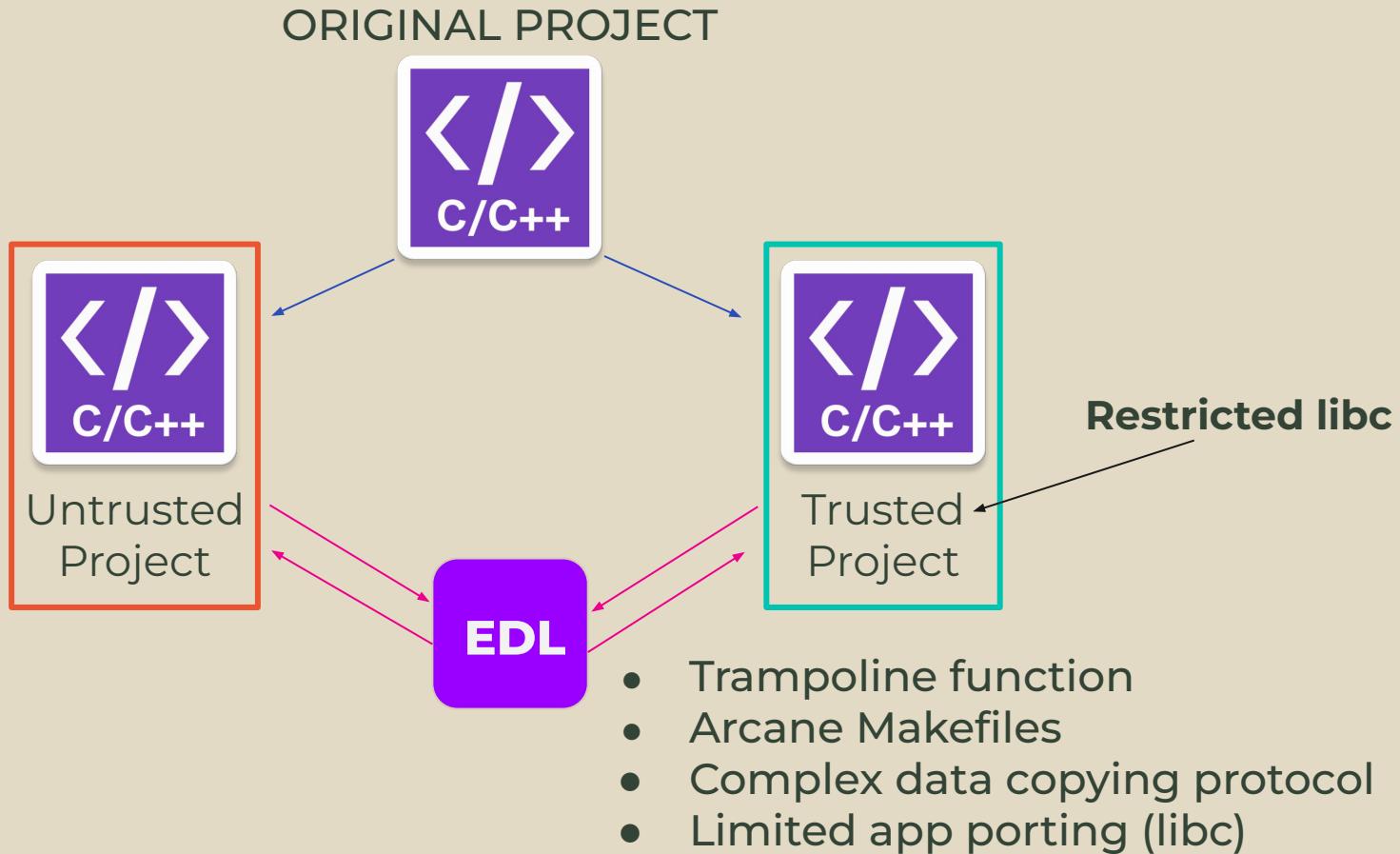
ORIGINAL PROJECT



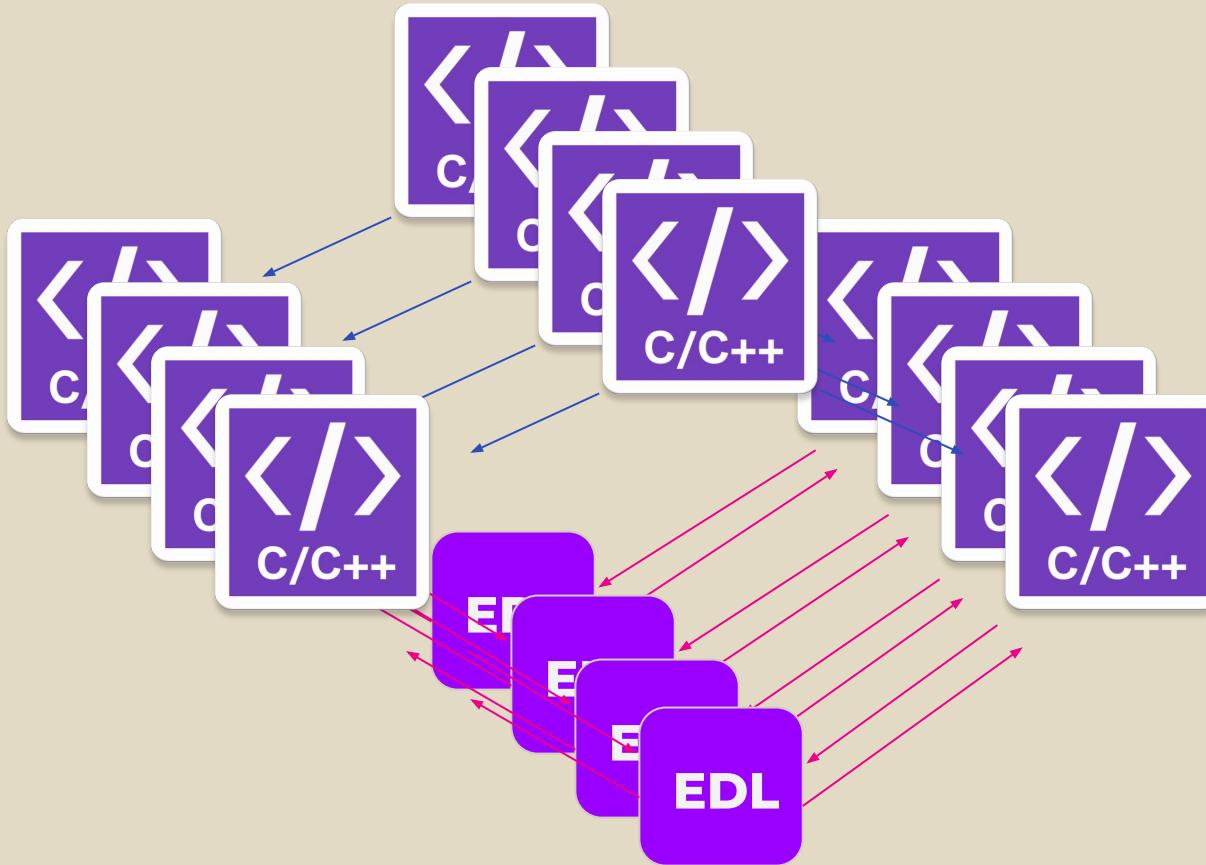
Programming TEEs



Programming TEEs

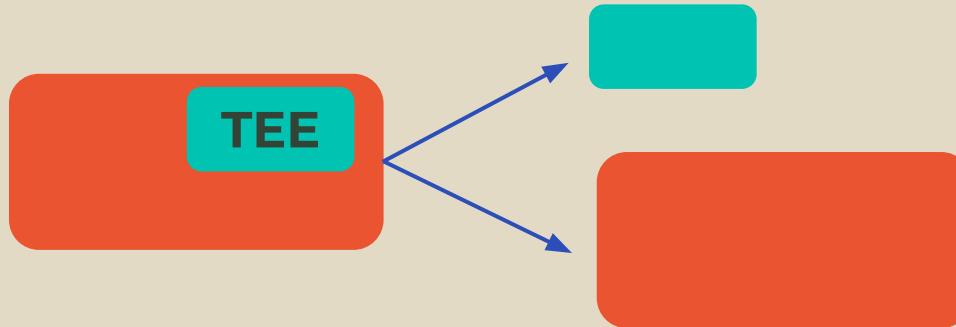


Distributed TEE Applications



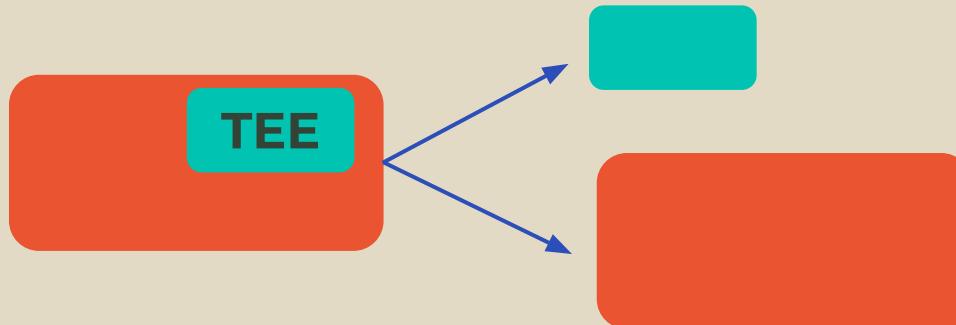


AUTOMATIC PROGRAM PARTITIONING





AUTOMATIC PROGRAM PARTITIONING



INFORMATION FLOW CONTROL



Secure Program Partitioning

STEVE ZDANCEWIC, LANTIAN ZHENG, NATHANIEL NYSTROM, and
 ANDREW C. MYERS
 Cornell University

Language Support for Secure Software Development with Enclaves

CSF '21

Aditya Oak
TU Darmstadt

Amir M. Ahmadian
KTH Royal Institute of Technology

Musard Balliu
KTH Royal Institute of Technology

Guido Salvaneschi
University of St.Gallen

PtrSplit: Supporting General Pointers in Automatic Program Partitioning

CCS '17

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 gtan@

Trent Jaeger
 The Pennsylvania State University
 University Park, PA

First, **seamless integration** of enclave programming into software applications remains challenging. For example, Intel provides a C/C++ interface to the SGX enclave but no direct support is available for managed languages. As managed languages like Java and Scala are extensively used for developing distributed applications, developers need to either interface their programs with the C++ code executing in the enclave (e.g., using the Java Native Interface [12]) or compile their programs to native code (e.g., using Java Native [13]).

ATC '17

Glamdring: Automatic Application Partitioning for Intel SGX

Joshua Lind <i>Imperial College London</i>	Christian Priebe <i>Imperial College London</i>	Divya Muthukumaran <i>Imperial College London</i>	Dan O'Keeffe <i>Imperial College London</i>
Pierre-Louis Aublin <i>Imperial College London</i>	Florian Kelbert <i>Imperial College London</i>	Tobias Reiher <i>TU Dresden</i>	David Goltzsche <i>TU Braunschweig</i>
David Eyers <i>University of Otago</i>	Rüdiger Kapitza <i>TU Braunschweig</i>	Christof Fetzer <i>TU Dresden</i>	Peter Pietzuch <i>Imperial College London</i>

Secure Program Partitioning

STEVE ZDANCEWIC, LANTIAN ZHENG, NATHANIEL NYSTROM, and
ANDREW C. MYERS

Cornell University

Language Support for Secure Software Development

- Significantly **changes** base language/compiler/runtime

Aditya Oak
TU Darmstadt

Amir M. Ahmadian
KTH Royal Institute of Technology

Musard Balliu
KTH Royal Institute of Technology

Guido Salvaneschi
University of St.Gallen

- “**Most interesting dynamic properties of programs are undecidable**” (Rice’s theorem)
- Either lack **information flow control** or **runtime integration with TEEs** or **attestation support**

First, seamless integration of enclave programming into software applications remains challenging. For example, Intel provides a C/C++ interface to the SGX enclave but no direct support is available for managed languages. As managed languages like Java and Scala are extensively used for distributed applications, developers need to either integrate their programs with the C++ code executing in the enclave (e.g., using the Java Native Interface [12]) or compile their programs to native code (e.g., using Java Native [13]) thus missing many advantages of managed environments.

Glamdring: Automatic Application Partitioning for Intel SGX

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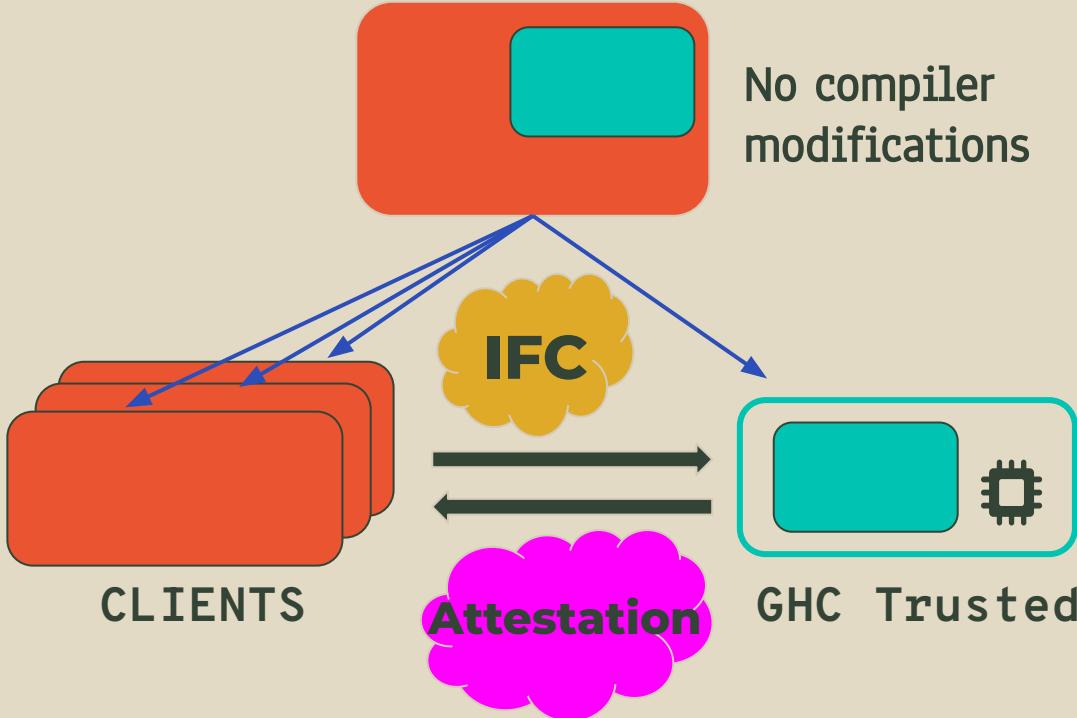
Rüdiger Kapitza
TU Braunschweig

Christof Fetzer
TU Dresden

Peter Pietzuch
Imperial College London



HasTEE⁺



MONAD

return :: a → m a

(>>=) :: m a → (a → m b) → m b

MONAD

return :: a → m a

tainting

(>>=) :: m a → (a → m b) → m b

MONAD

return :: a → m a

computation
builder

(>>=) :: m a → (a → m b) → m b

**TRAINT
TRACKING**

MONAD

return :: a → m a

(>>=) :: m a → (a → m b) → m b

**TAIN
TRACKING**

**ALTERNATE
SEMANTICS**

Illustration : Password Checker

```
pwdChkr :: Enclave String -> String -> Enclave Bool  
pwdChkr pwd guess = fmap (== guess) pwd
```

Enclave monad

```
pwdChkr :: Enclave String -> String -> Enclave Bool  
pwdChkr pwd guess = fmap (== guess) pwd
```

```
passwordChecker :: App Done  
passwordChecker = do  
    passwd <- inEnclaveConstant "secret"  
    efunc <- inEnclave $ pwdChkr passwd
```

App monad

```
pwdChkr :: Enclave String -> String -> Enclave Bool  
pwdChkr pwd guess = fmap (== guess) pwd
```

```
passwordChecker :: App Done  
passwordChecker = do  
    passwd <- inEnclaveConstant "secret"  
    efunc <- inEnclave $ pwdChkr passwd
```

Load code

Load data

```
pwdChkr :: Enclave String -> String -> Enclave Bool  
pwdChkr pwd guess = fmap (== guess) pwd
```

```
passwordChecker :: App Done  
passwordChecker = do  
    passwd <- inEnclaveConstant "secret"  
    efunc <- inEnclave $ pwdChkr passwd  
    runClient $ do -- Client code
```

```
        liftIO $ putStrLn "Enter your password"  
        userInput <- liftIO getLine  
        res       <- gateway (efunc <@> userInput)  
        liftIO $ putStrLn ("Login returned " ++ show res)
```

```
main = runApp passwordChecker
```

```
pwdChkr :: Enclave String -> String -> Enclave Bool  
pwdChkr pwd guess = fmap (== guess) pwd
```

Remote
function
invocation

```
passwordChecker :: App Done  
passwordChecker = do  
    passwd <- inEnclaveConstant "secret"  
    efunc <- inEnclave $ pwdChkr passwd  
    runClient $ do -- Client code
```

Remote function
application

```
liftIO $ putStrLn "Enter your password"  
userInput <- liftIO getLine  
res       <- gateway (efunc <@> userInput)  
liftIO $ putStrLn ("Login returned " ++ show res)
```

```
main = runApp passwordChecker
```

```
pwdChkr :: Enclave String -> String -> Enclave Bool
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passwordChecker :: App Done
passwordChecker = do
    passwd <- inEnclaveConstant "secret"
    efunc <- inEnclave $ pwdChkr passwd
    runClient $ do -- Client code
        liftIO $ putStrLn "Enter your password"
        userInput <- liftIO getLine
        res       <- gateway (efunc <@> userInput)
        liftIO $ putStrLn ("Login returned " ++ show res)

main = runApp passwordChecker
```

```
pwdChkr :: Enclave String -> String -> Enclave Bool  
pwdChkr pwd guess = fmap (== guess) pwd
```

```
passwordChecker :: App Done  
passwordChecker = do
```

```
    passwd <- inEnclaveConst "secret"
```

```
    efunc <- inEnclave $ pwdChkr passwd
```

```
    runClient $ do -> Client code
```

```
        liftIO $ putStrLn "Enter your password"
```

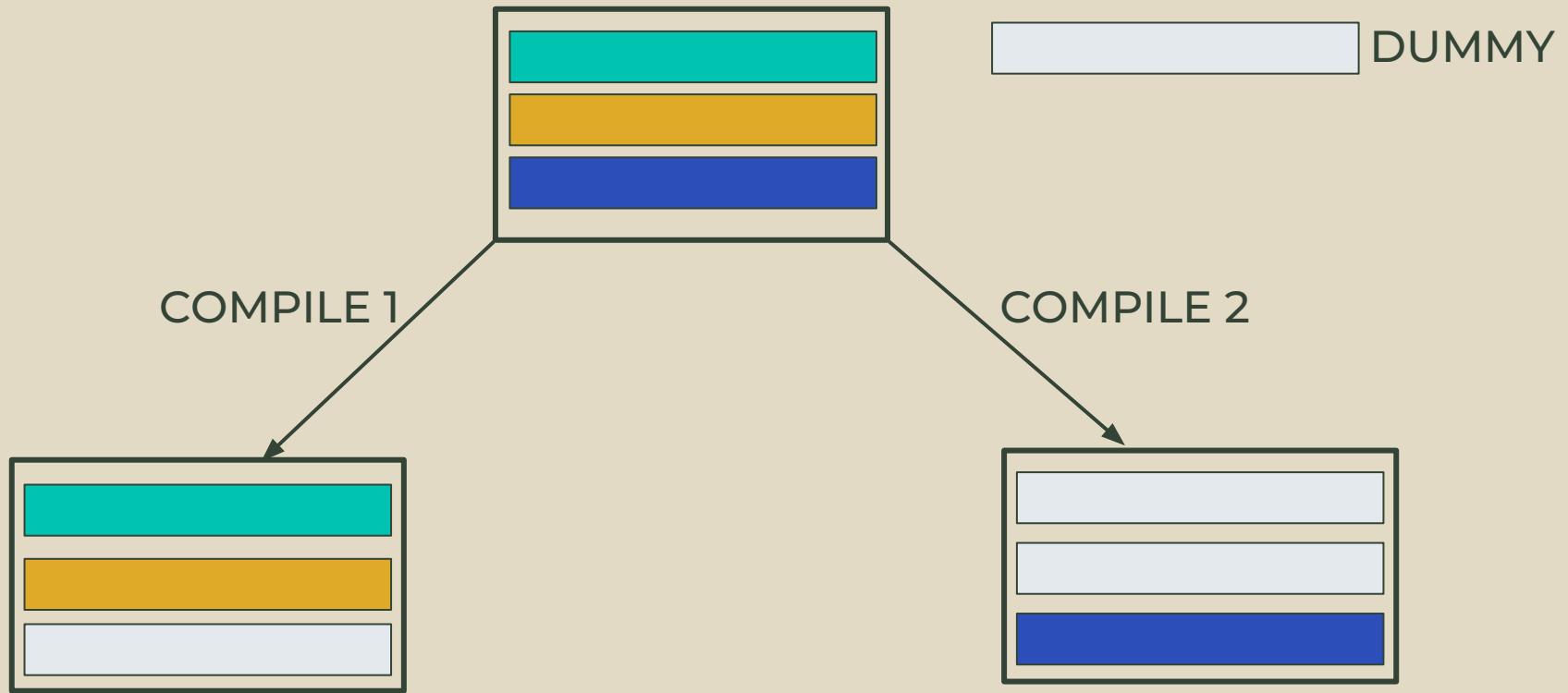
```
        userInput <- liftIO getLine
```

```
        res <- gateway (efunc <@> userInput)
```

```
        liftIO $ putStrLn ("Login returned " ++ show res)
```

```
main = runApp passwordChecker
```

COMPILED TWICE



Compilation 1

Compilation 2

```
-- Enclave
pwdChkr :: Enclave String -> String -> Enclave Bool
pwdChkr pwd guess = fmap (== guess) pwd

passwordChecker :: App Done
passwordChecker = do
  passwd <- inEnclaveConstant "secret"
  efunc <- inEnclave $ pwdChkr passwd
  return DONE ← DUMMY
-- wait for calls from Client
main = runApp passwordChecker
```

GHC Trusted 

INTEL SGX

Compilation 1

```
-- Enclave
pwdChkr :: Enclave String -> String -> Enclave Bool
pwdChkr pwd guess = fmap (== guess) pwd

passwordChecker :: App Done
passwordChecker = do
    passwd <- inEnclaveConstant "secret"
    efunc <- inEnclave $ pwdChkr passwd
    return DONE

-- wait for calls from Client
main = runApp passwordChecker
```



INTEL SGX

Compilation 2

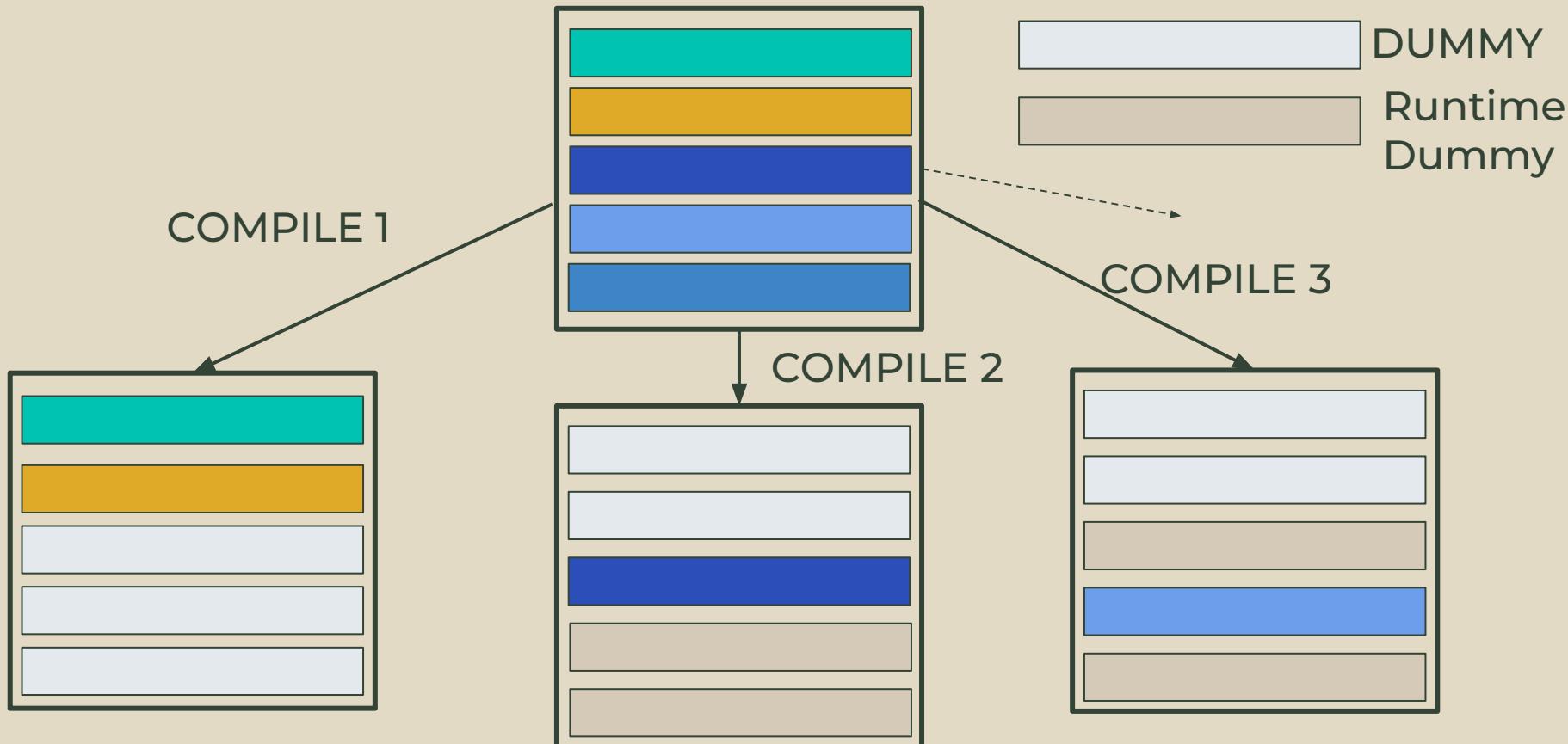
```
-- Client
pwdChkr = -- gets optimised away
passwordChecker :: App Done
passwordChecker = do
    passwd <- return Dummy
    efunc <- inEnclave $ -- ignores pwdChkr body
    runClient $ do -- Client code
        liftIO $ putStrLn "Enter your password"
        userInput <- liftIO getLine
        res      <- gateway (efunc <@> userInput)
        liftIO $ putStrLn ("Login returned " ++ show res)

-- drives the application
main = runApp passwordChecker
```

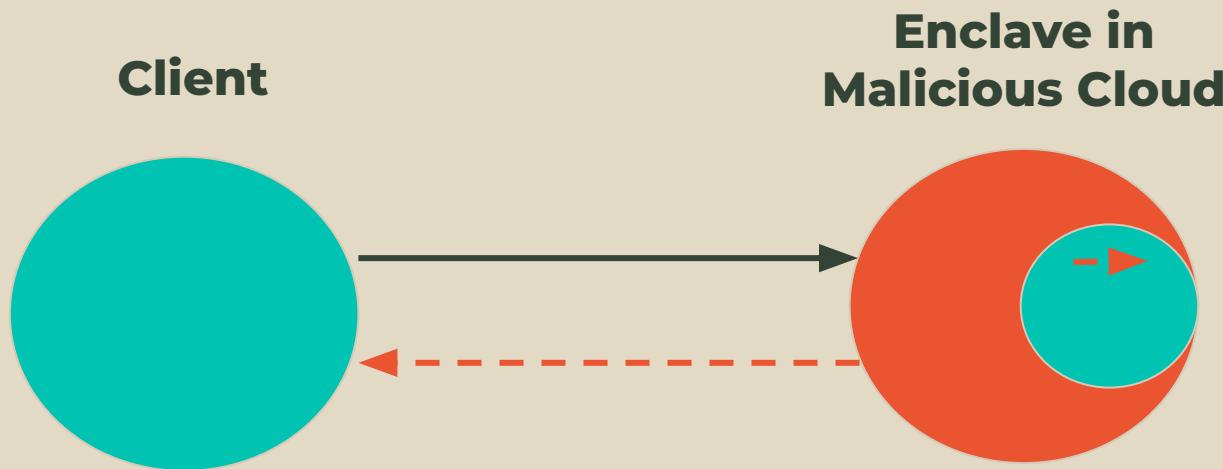
A diagram consisting of two black arrows pointing from the removed code in Compilation 1 to the corresponding lines in Compilation 2. One arrow points from the commented-out line `pwdChkr = -- gets optimised away` to the line `pwdChkr = -- gets optimised away` in Compilation 2. Another arrow points from the line `return DONE` in Compilation 1 to the line `passwd <- return Dummy` in Compilation 2.

DUMMY

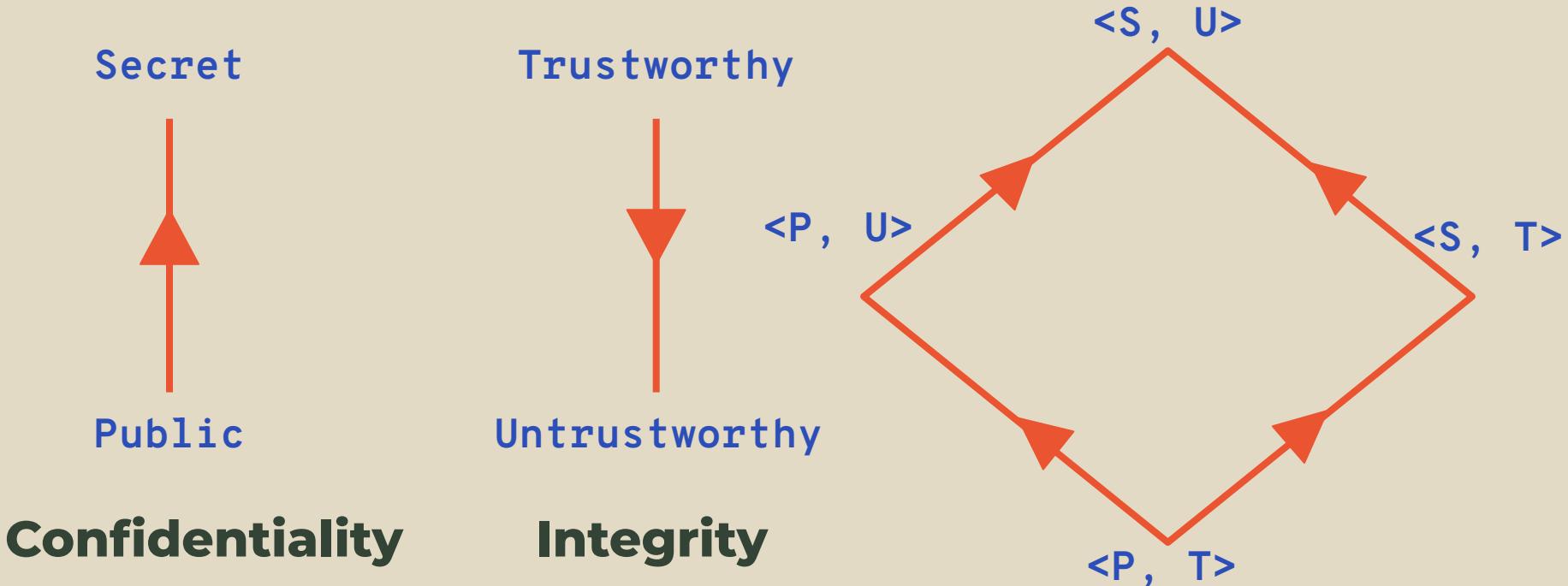
Generalisation for multiple clients



Information Flow

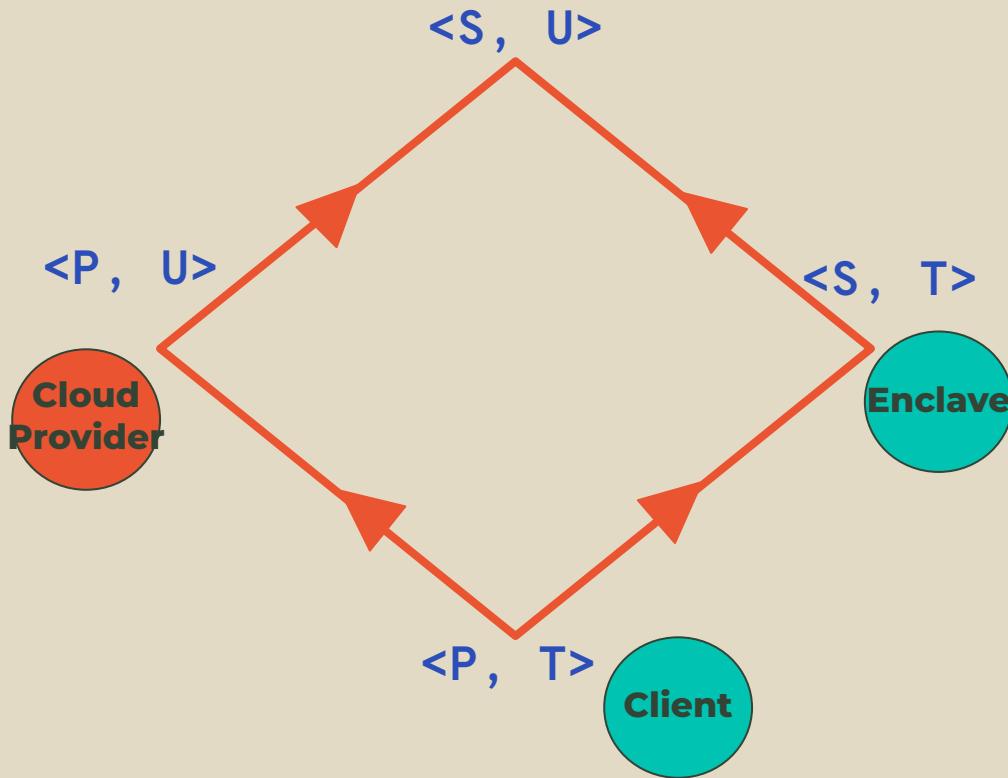


Information Flow Control

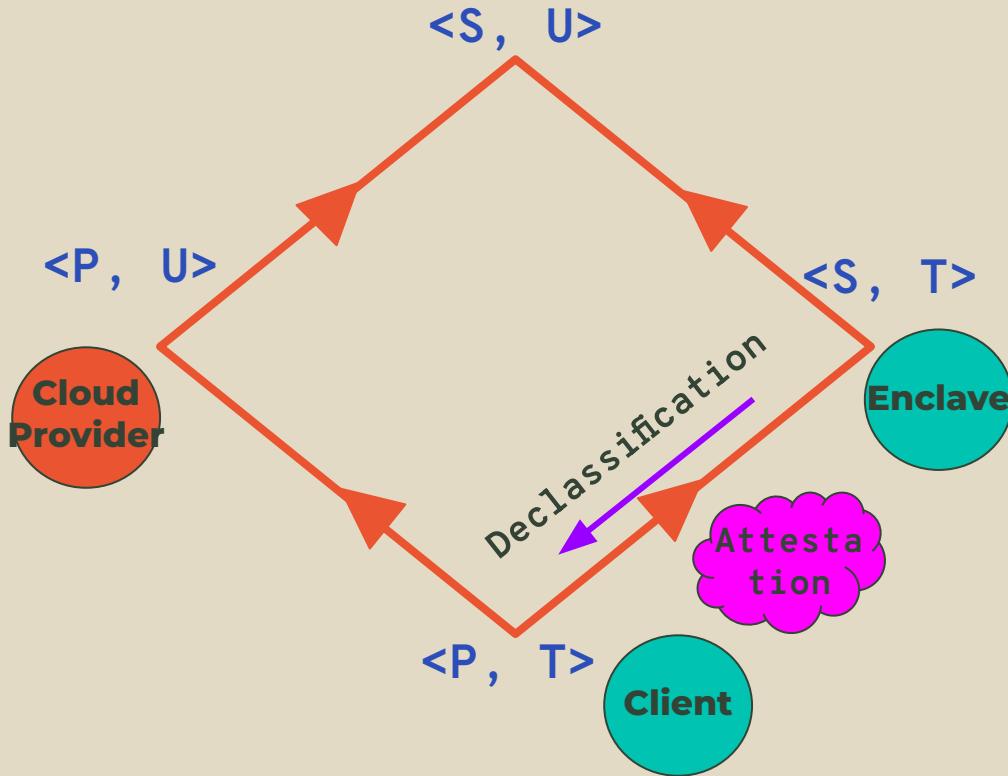


1. Denning, Dorothy E. "A lattice model of secure information flow." *Communications of the ACM* 19.5 (1976).
2. Biba, K.J. Integrity considerations for secure computer systems. Technical Report. April 1977.

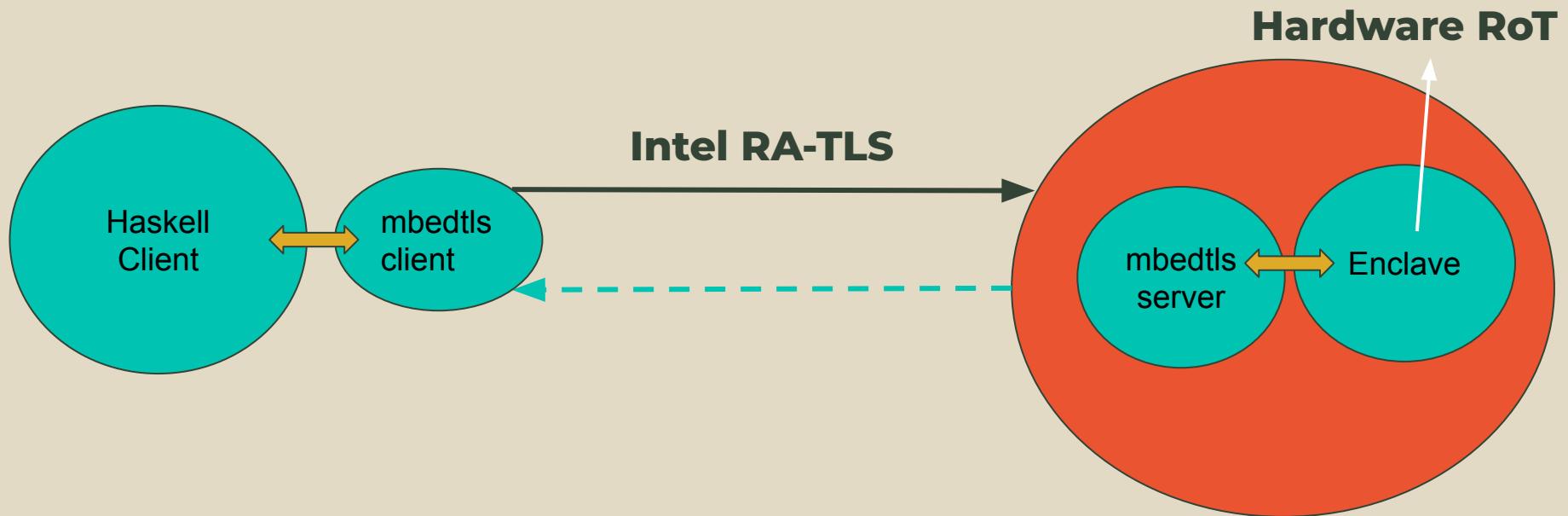
Information Flow Control



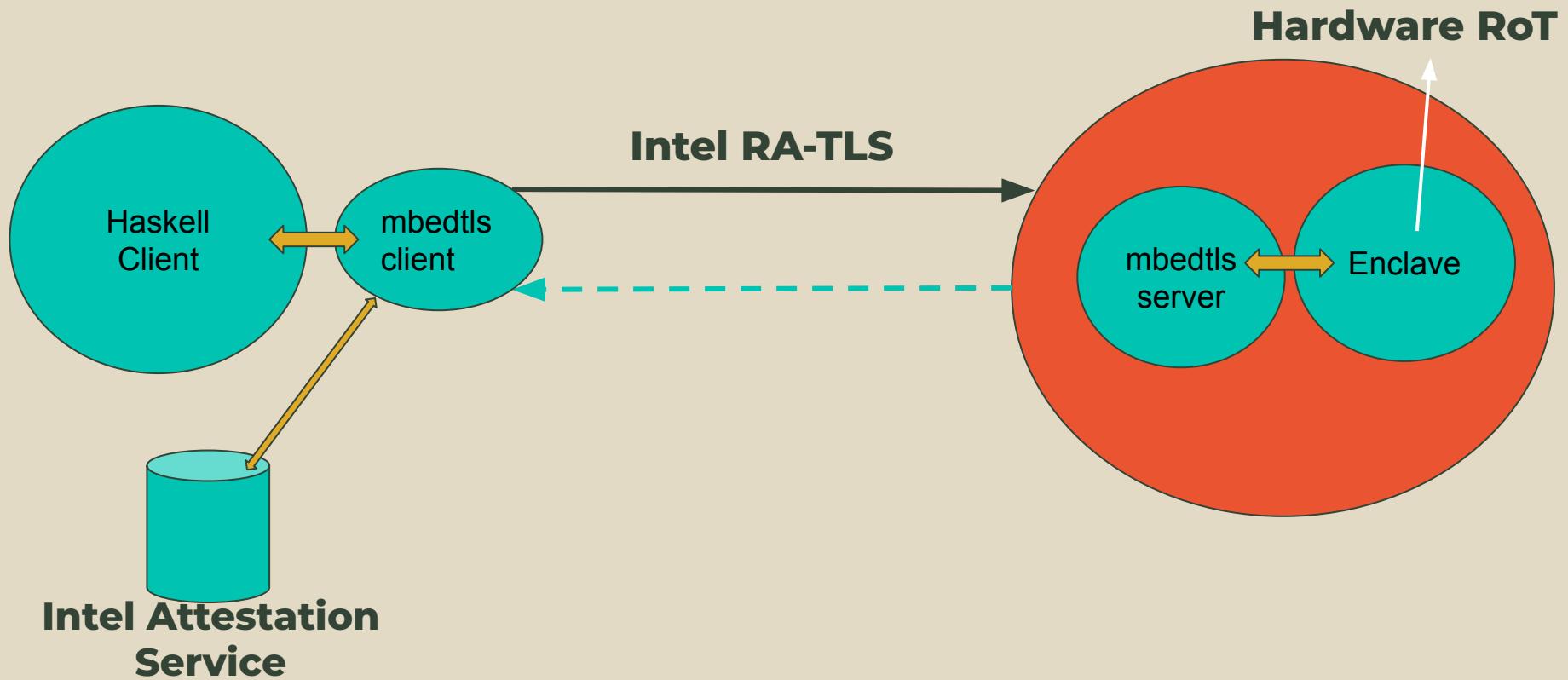
Information Flow Control



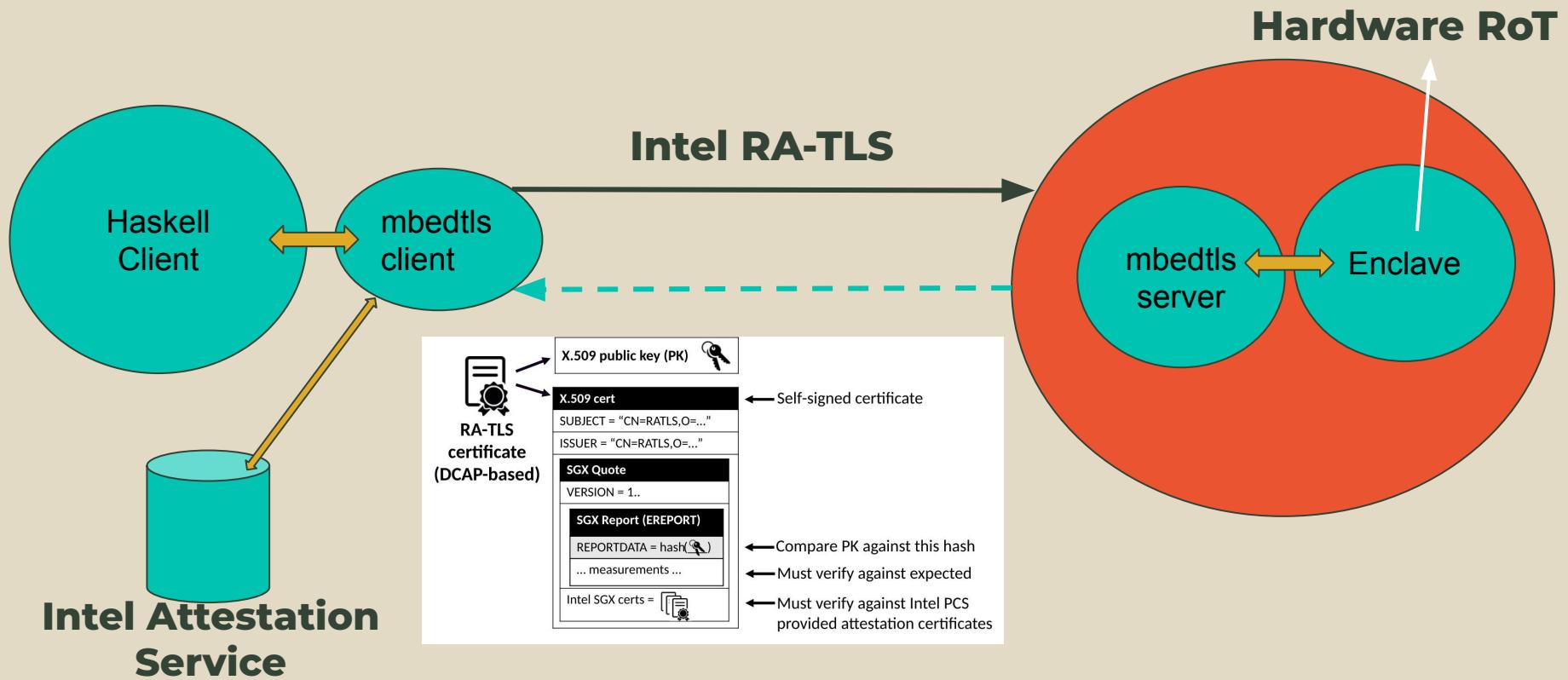
Attestation



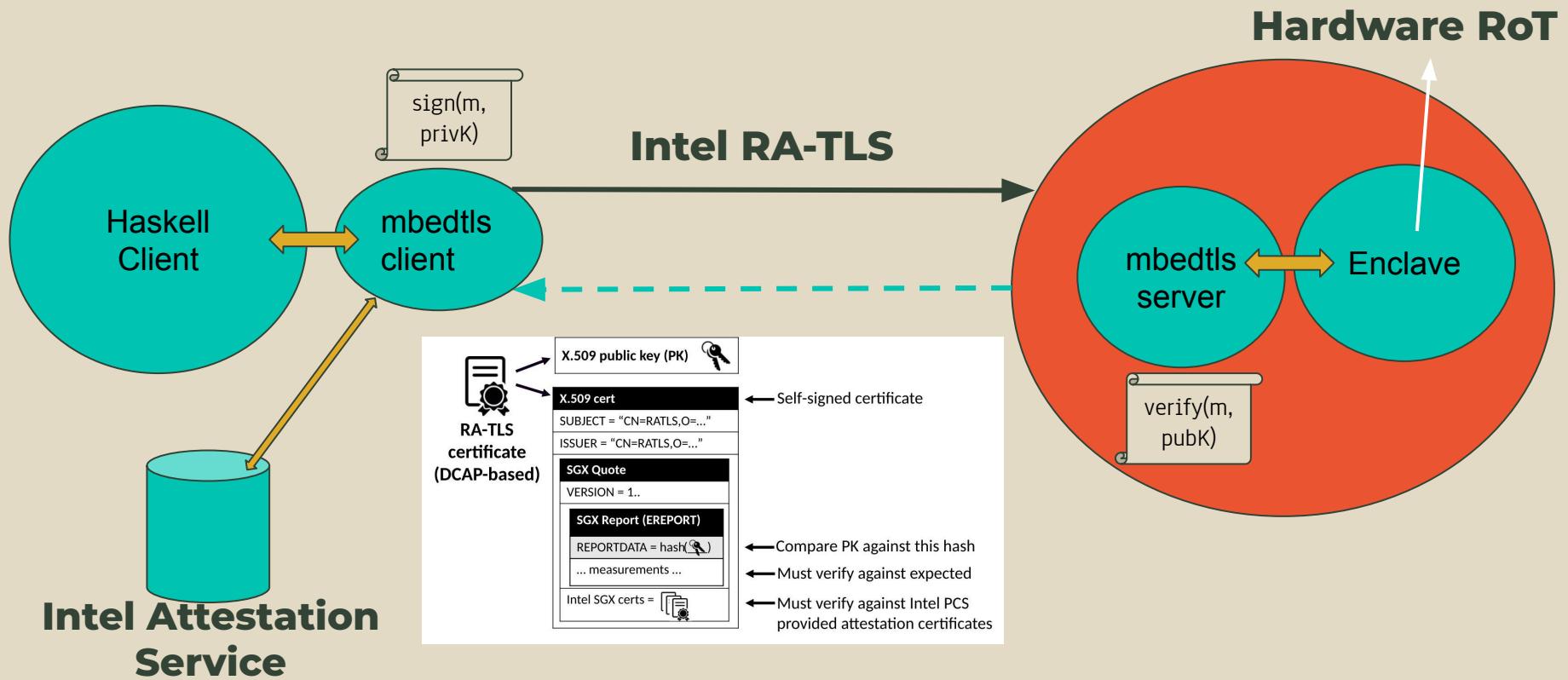
Attestation



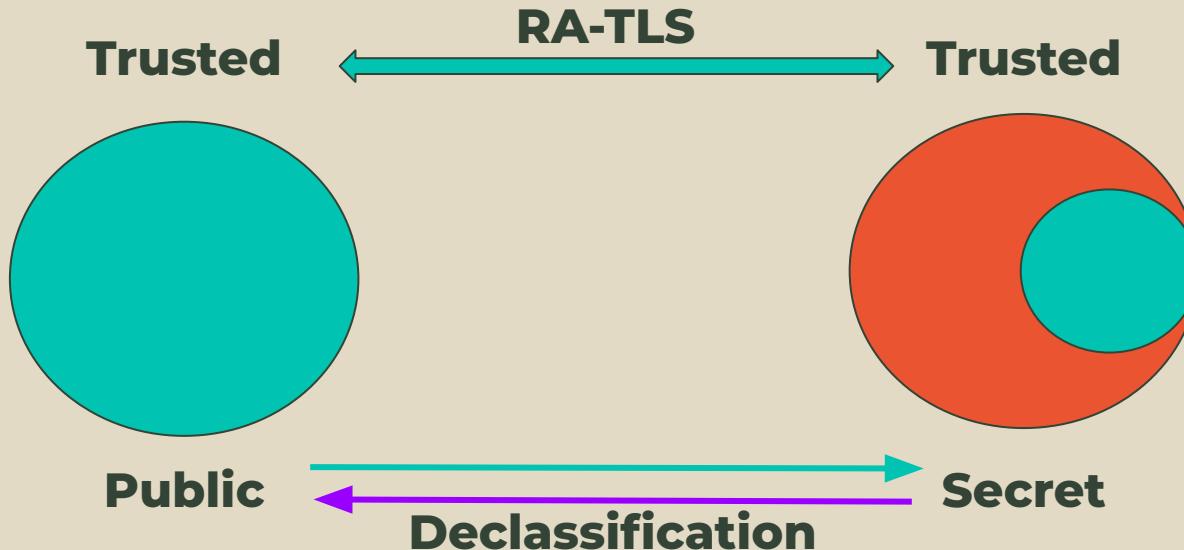
Attestation



Attestation



Information Flow



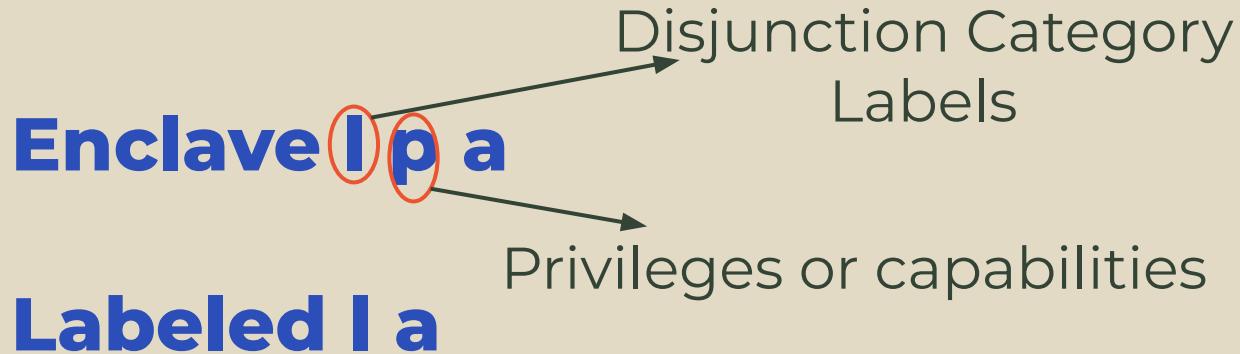
Declassification

Enclave I p a

Labeled I a

1. Stefan D, Russo A, Mitchell JC, Mazières D. Flexible Dynamic Information flow control in Haskell. Haskell Symposium 2011.
2. Stefan, D., Russo, A., Mazières, D., & Mitchell, J. C. (2012). Disjunction Category Labels. *NordSec 2011*

Declassification



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Declassification

Enclave I p a

Labeled I a

Clearance
Floating Label  $L \sqcup L_{cur} \vdash \dots$

1. Stefan D, Russo A, Mitchell JC, Mazières D. Flexible Dynamic Information flow control in Haskell. Haskell Symposium 2011.
2. Stefan, D., Russo, A., Mazières, D., & Mitchell, J. C. (2012). Disjunction Category Labels. NordSec 2011

Declassification

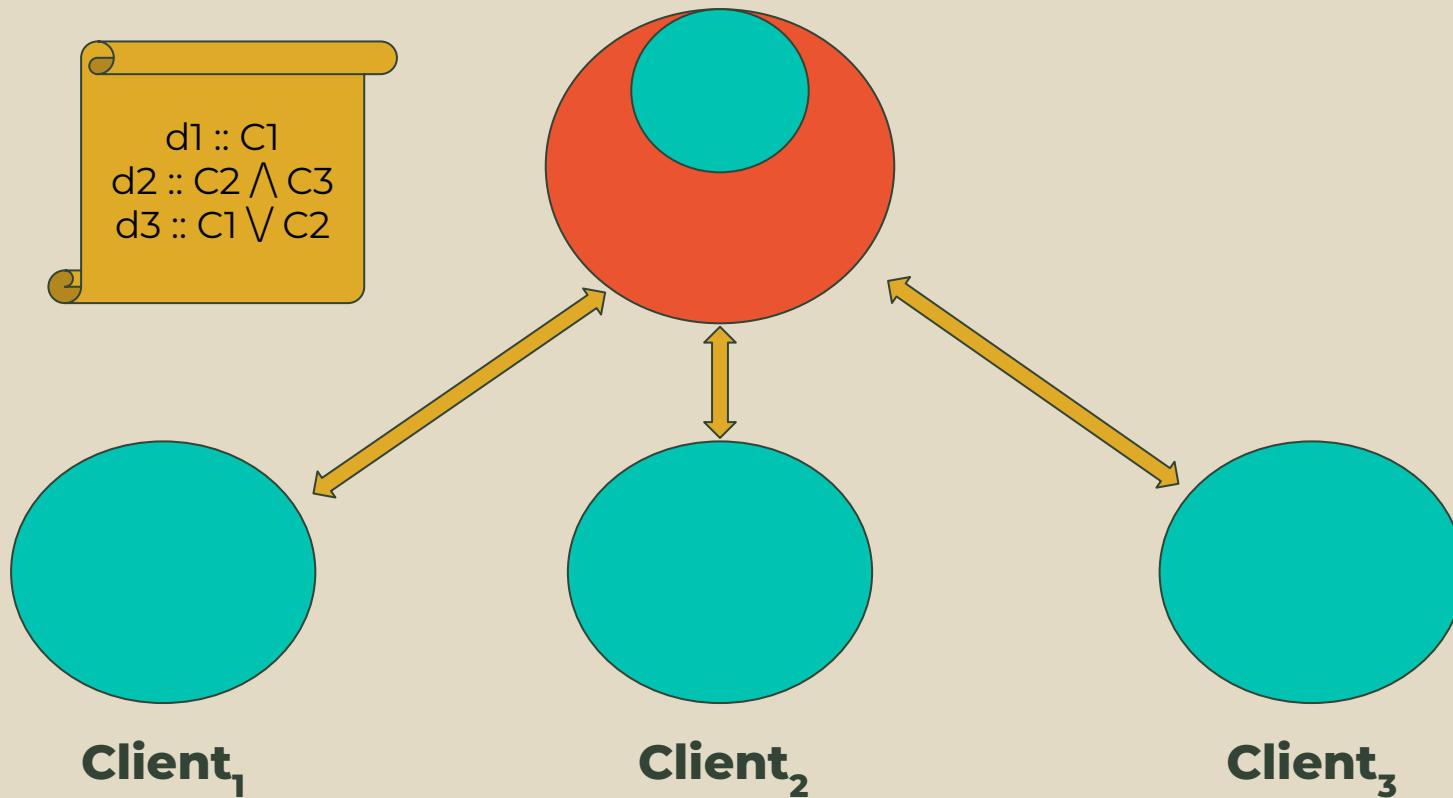
Enclave I p a

Labeled I a

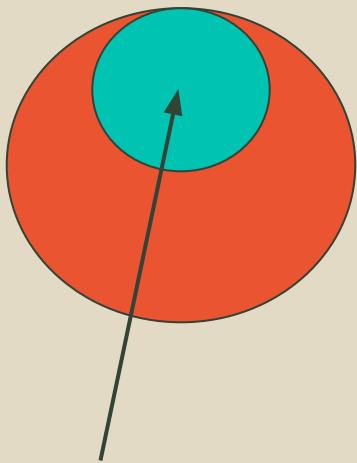
Clearance
Floating Label $\dashv \uparrow$ $L \sqcup L_{\text{cur}} \vdash \dots$

1. Stefan D, Russo A, Mitchell JC, Mazières D. Flexible Dynamic Information flow control in Haskell. Haskell Symposium 2011.
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Data Clean Room

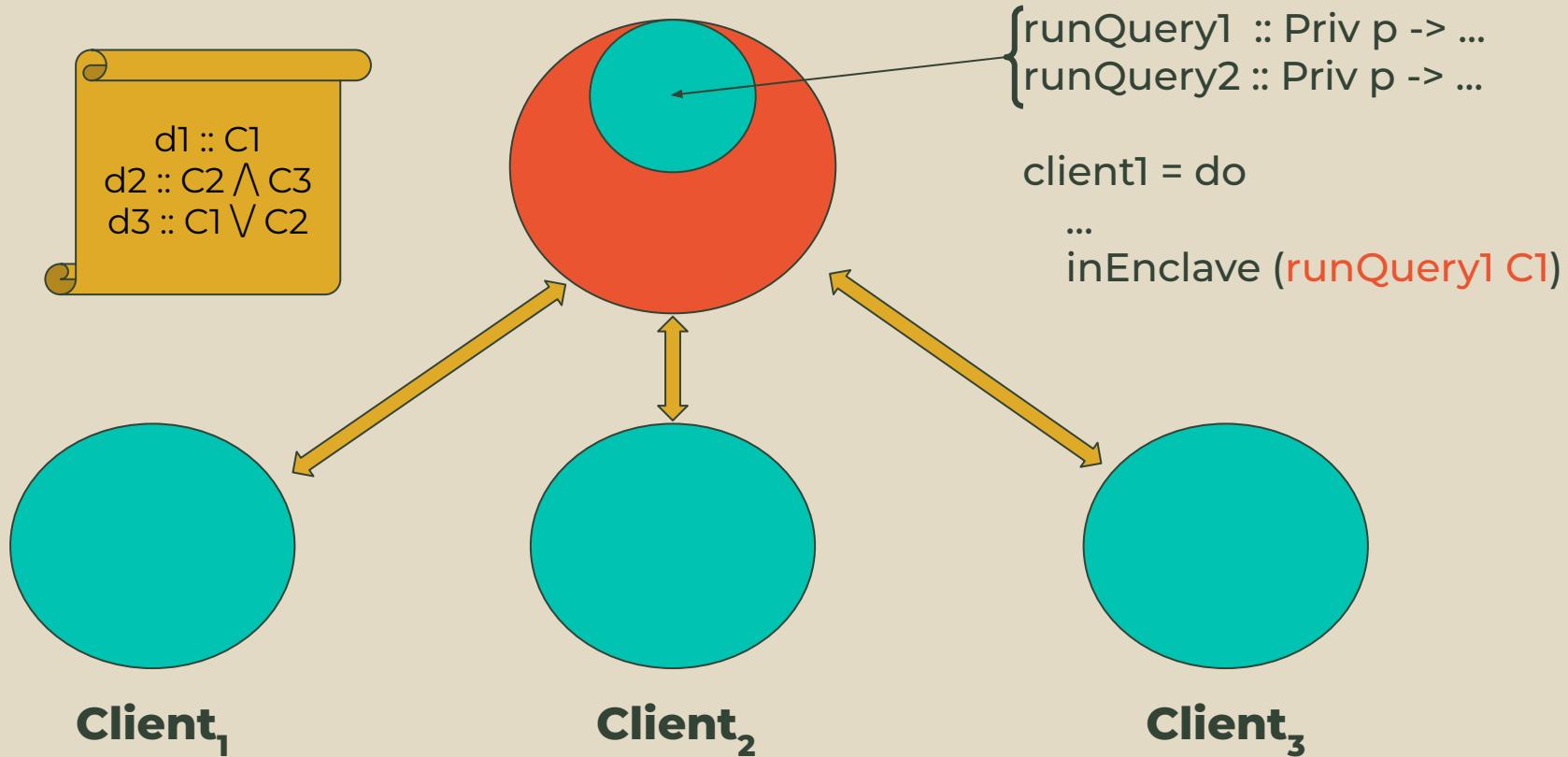


Data Clean Room



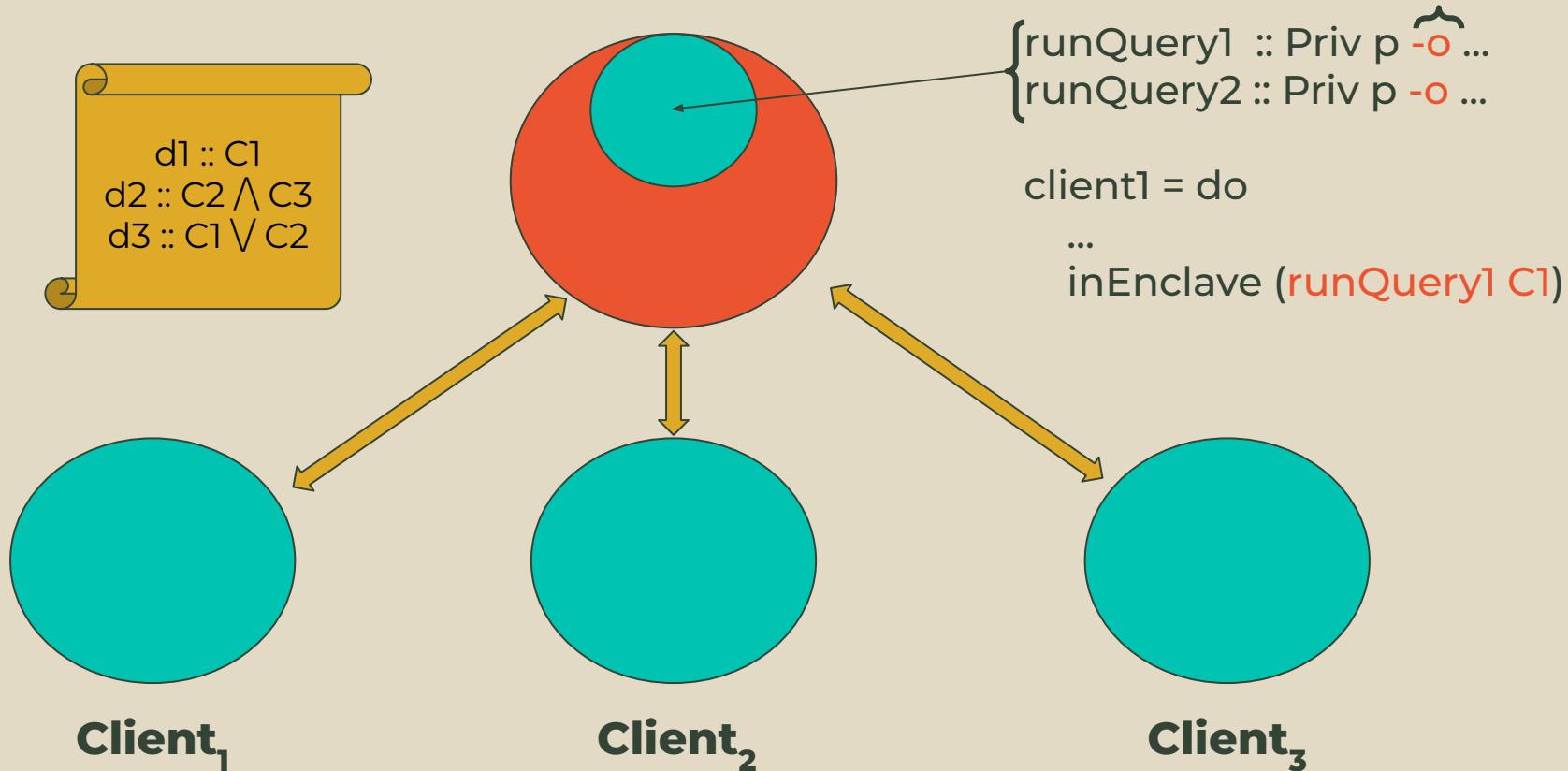
$f1_{C1} :: \dots$ Carries privilege to declassify C1
 $f2_{C2} :: \dots$

Data Clean Room



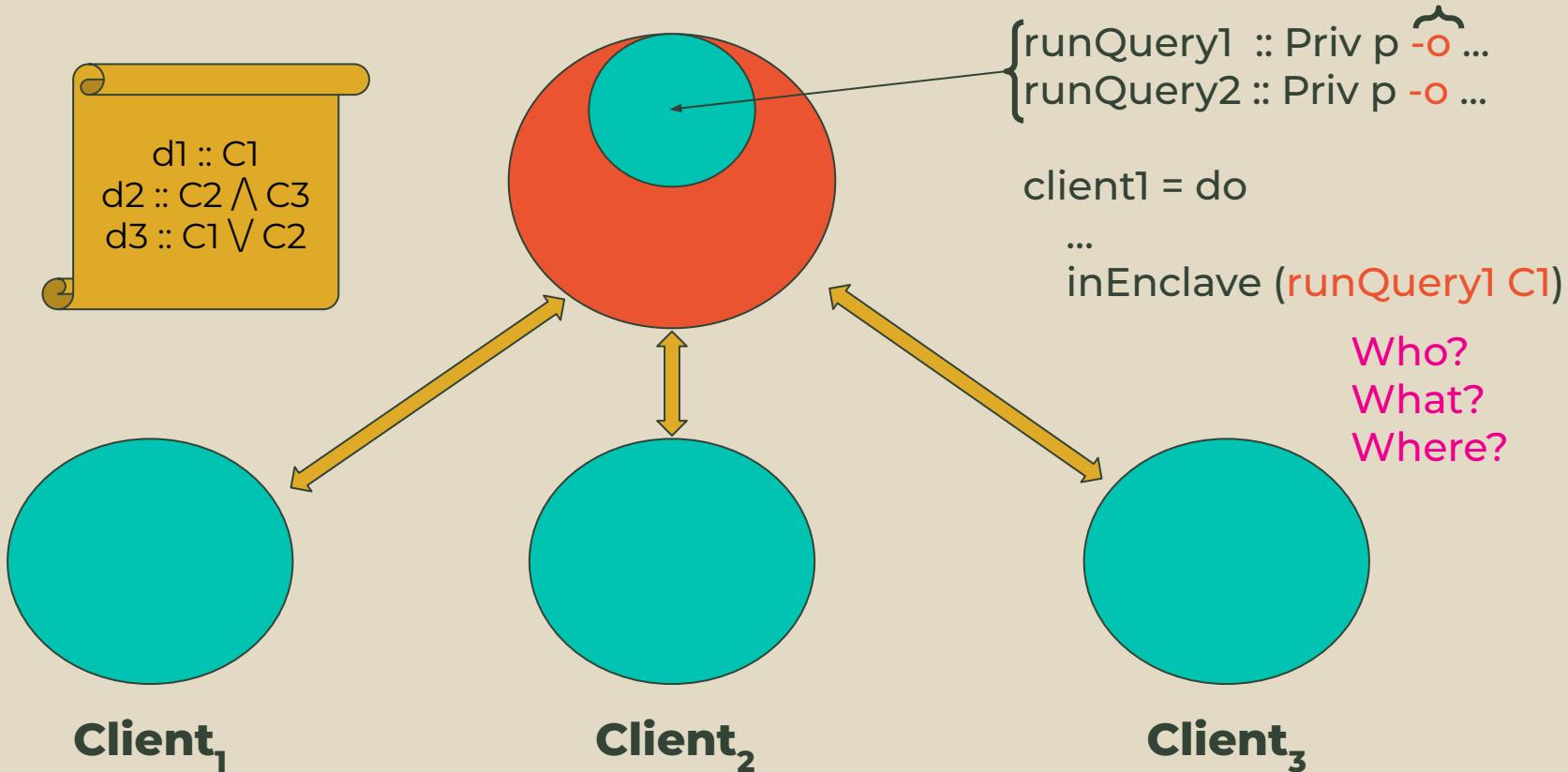
Data Clean Room

Linear arrow for “unforgeability”

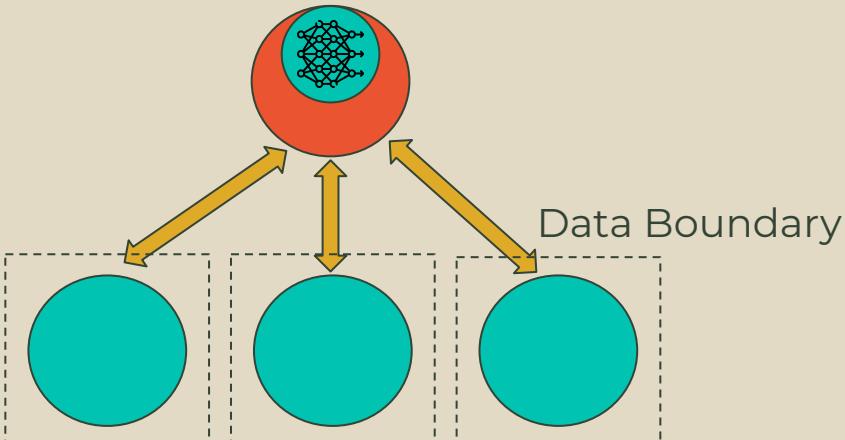


Data Clean Room

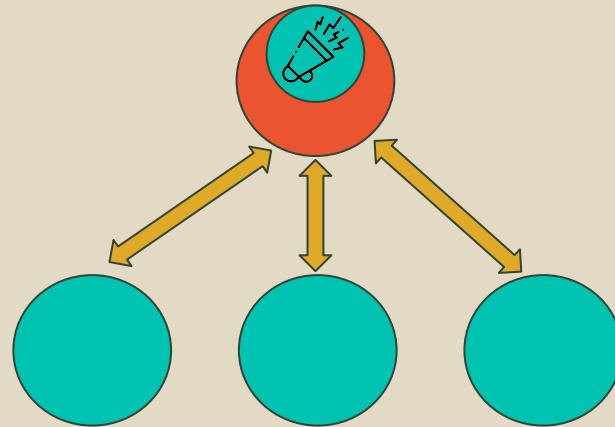
Linear arrow for “unforgeability”



More case studies...

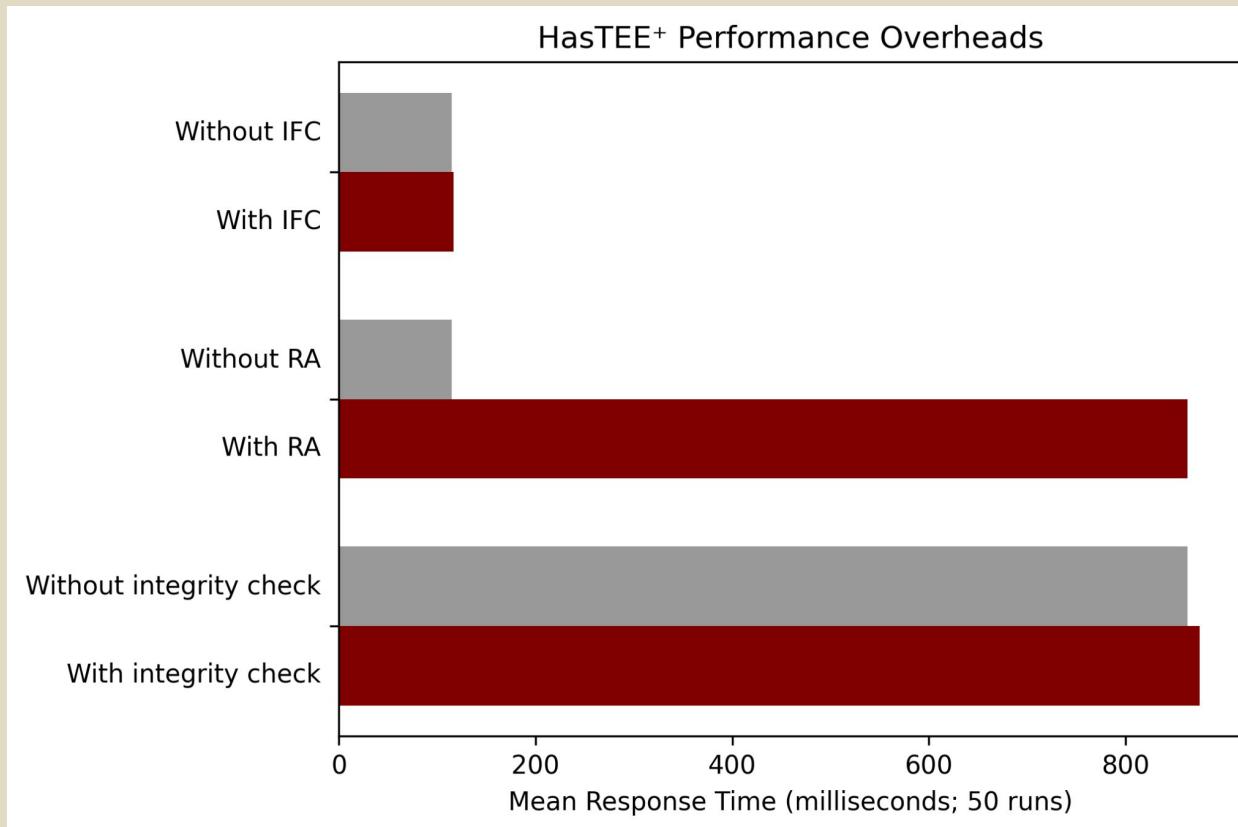


Federated Learning with TEEs
and homomorphic encryption

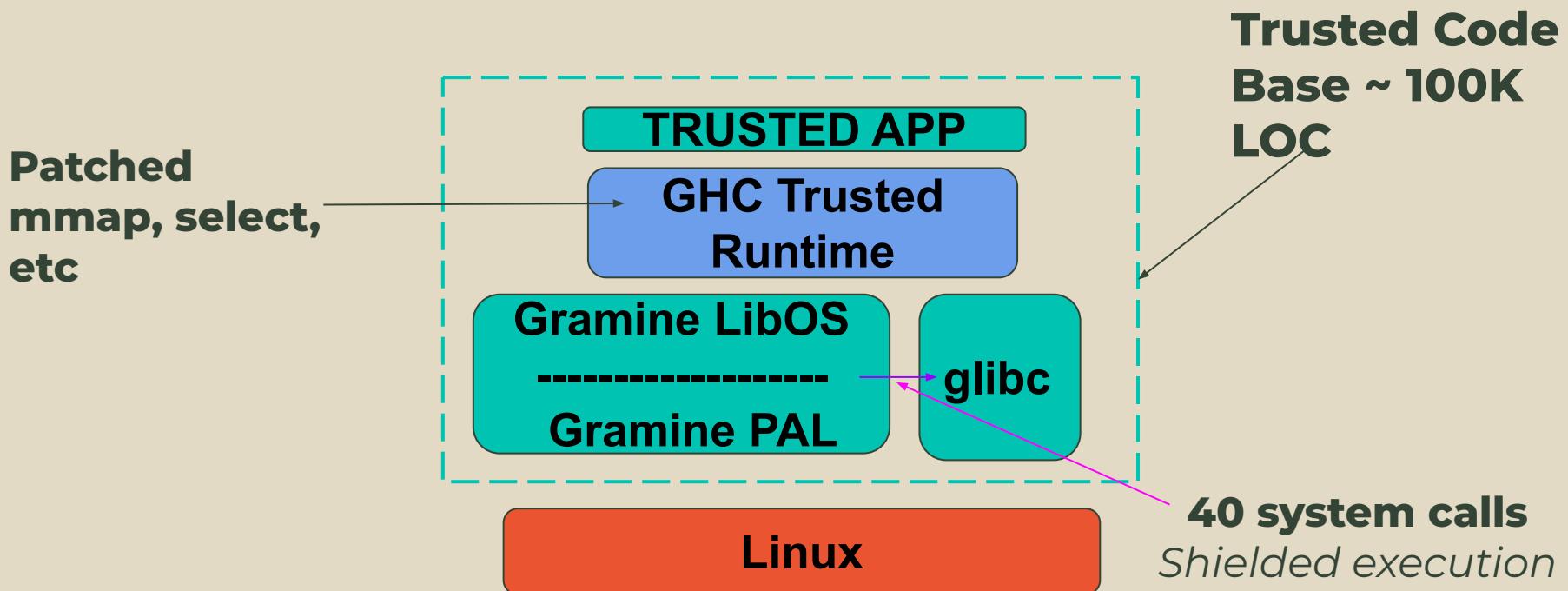


Data Clean Room with
differential privacy

Performance Overheads



Trusted GHC



Trusted GHC

Memory	RSS	Virtual Size	Disk Swap
At rest	19,132 KB	287,920 KB	0 KB
Peak	20,796 KB	290,032KB	0 KB

LATENCY ~ 60 ms

vs

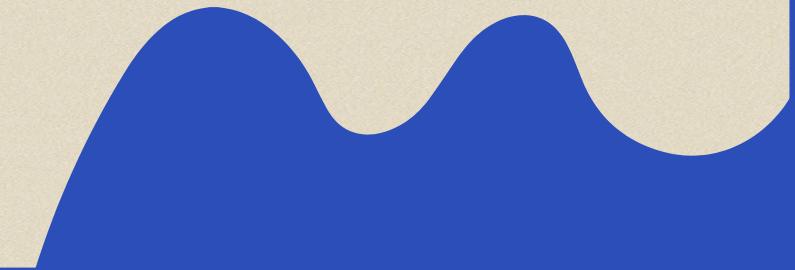
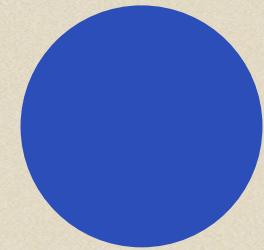
0.6 ms in native SDK





Part II

SynchronVM



ATTACKER MODELS

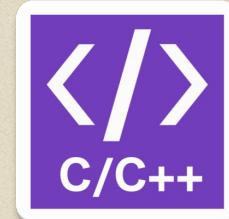
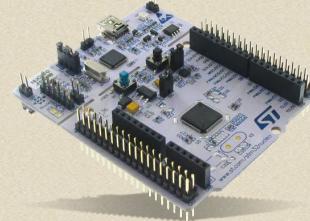
Attacker Model 1



TRUST

in the OS and other low-level software

Attacker Model 2



MEMORY UNSAFETY

to accommodate resource constraints

Synchron - An API and Runtime for Embedded Systems

Abhiroop Sarkar  

Chalmers University, Sweden

Bo Joel Svensson  

Chalmers University, Sweden

Mary Sheeran  

Chalmers University, Sweden

Abstract

Programming embedded systems applications involves writing concurrent, event-driven and timing-aware programs. Traditionally, such programs are written in low-level machine-oriented programming languages like C or Assembly. We present an alternative by introducing Synchron, an API that offers high-level abstractions to the programmer while supporting the low-level infrastructure in an associated runtime system and one-time-effort drivers.

Embedded systems applications exhibit the general characteristics of being (i) concurrent, (ii) I/O-bound and (iii) timing-aware. To address each of these concerns, the Synchron API consists of three components - (1) a Concurrent ML (CML) inspired message-passing concurrency model, (2) a message-passing-based I/O interface that translates between low-level interrupt based and memory-mapped peripherals, and (3) a timing operator, `syncT`, that marries CML's `sync` operator with timing windows inspired from the TinyTimber kernel.

We implement the Synchron API as the bytecode instructions of a virtual machine called SynchronVM. SynchronVM hosts a Caml-inspired functional language as its frontend language, and the backend of the VM supports the STM32F4 and NRF52 microcontrollers, with RAM in the order of hundreds of kilobytes. We illustrate the expressiveness of the Synchron API by showing examples of expressing state machines commonly found in embedded systems. The timing functionality is demonstrated through a music programming exercise. Finally, we provide benchmarks on the response time, jitter rates, memory, and power usage of the SynchronVM.

2012 ACM Subject Classification Computer systems organization → Embedded software; Software and its engineering → Runtime environments; Computer systems organization → Real-time languages; Software and its engineering → Concurrent programming languages

Hailstorm : A Statically-Typed, Purely Functional Language for IoT Applications

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ABSTRACT

With the growing ubiquity of *Internet of Things* (IoT), more complex logic is being programmed on resource-constrained IoT devices, almost exclusively using the C programming language. While C provides low-level control over memory, it lacks a number of high-level programming abstractions such as higher-order functions, polymorphism, strong static typing, memory safety, and automatic memory management.

We present Hailstorm, a statically-typed, purely functional programming language that attempts to address the above problem. It is a high-level programming language with a strict typing discipline. It supports features like higher-order functions, tail-recursion, and automatic memory management, to program IoT devices in a declarative manner. Applications running on these devices tend to be heavily dominated by I/O. Hailstorm tracks side effects like I/O in its type system using *resource types*. This choice allowed us to explore the design of a purely functional standalone language, in an area where it is more common to embed a functional core in an imperative shell. The language borrows the combinators of arrowed FRP, but has discrete-time semantics. The design of the full set of combinators is work in progress, driven by examples. So far, we have evaluated Hailstorm by writing standard examples from the literature (earthquake detection, a railway crossing system and various other clocked systems), and also running examples on the GRISp embedded systems board, through generation of Erlang.

CCS CONCEPTS

• Software and its engineering → Compilers; Domain specific languages; • Computer systems organization → Sensors and actuators; Embedded software.

September 8–10, 2020, Bologna, Italy. ACM, New York, NY, USA, 16 pages. <https://doi.org/10.1145/3414080.3414092>

1 INTRODUCTION

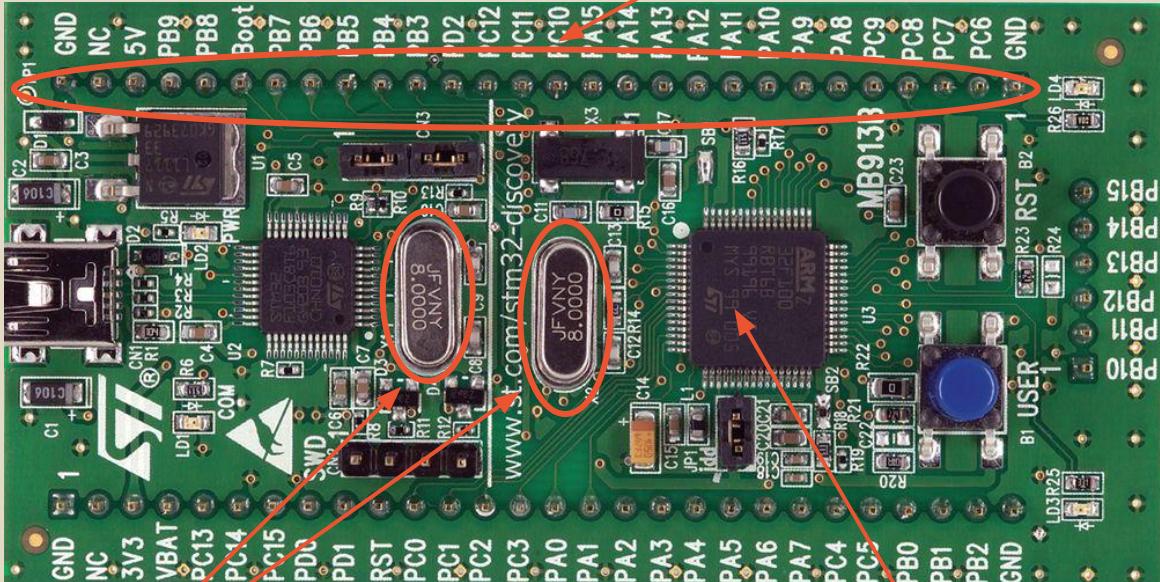
As the density of IoT devices and diversity in IoT applications continue to increase, both industry and academia are moving towards decentralized system architectures like *edge computing* [38]. In edge computation, devices such as sensors and client applications are provided greater computational power, rather than pushing the data to a backend cloud service for computation. This results in improved response time and saves network bandwidth and energy consumption [50]. In a growing number of applications such as aeronautics and automated vehicles, the real-time computation is more robust and responsive if the edge devices are compute capable.

In a more traditional centralized architecture, the sensors and actuators have little logic in them; they rather act as data relaying services. In such cases, the firmware on the devices is relatively simple and programmed almost exclusively using the C programming language. However with the growing popularity of edge computation, more complex logic is moving to the edge IoT devices. In such circumstances, programs written using C tend to be verbose, error-prone and unsafe [17, 27]. Additionally, IoT applications written in low-level languages are highly prone to security vulnerabilities [7, 58].

Hailstorm is a domain-specific language that attempts to address these issues by bringing ideas and abstractions from the functional and reactive programming communities to programming IoT applications. Hailstorm is a *pure, statically-typed* functional programming language. Unlike *impure* functional languages like ML and Scheme, Hailstorm restricts arbitrary side-effects and

I/O-Bound

192 KB RAM
168 MHz clock



Clocked

Bare-metal concurrent

Programming Microcontrollers

Memory Unsafe



no real-time
constructs

!Concurrent

I/O-Bound



Virtual Machine

**Timing
aware**

Concurrent

I/O-Bound

- Categorical Abstract Machine
- Pointer-reversal GC
- *Earliest-deadline-first* based real-time scheduler

**Timing
aware**

Concurrent

Complete Synchron API

```
spawn      : ((() -> ()) -> ThreadId
channel   : ()    -> Channel a
send       : Channel a -> a -> Event ()
recv       : Channel a -> Event a
choose     : Event a     -> Event a     -> Event a
wrap       : Event a     -> (a -> b) -> Event b
sync       : Event a     -> a
 syncT     : Time -> Time -> Event a -> a
 spawnExternal : Channel a -> Driver -> ExternalThreadId
```

Complete Synchron API

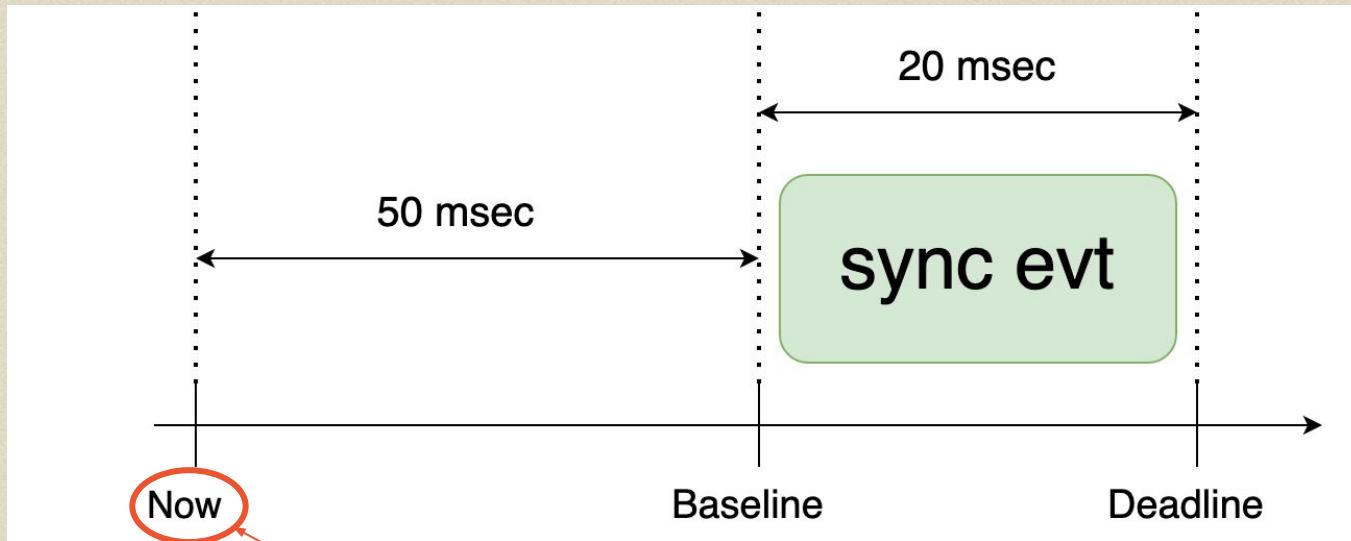
```
spawn      : ((() -> ()) -> ThreadId
channel   : ()    -> Channel a
send       : Channel a -> a -> Event ()
recv       : Channel a -> Event a
choose     : Event a     -> Event a     -> Event a
wrap       : Event a     -> (a -> b) -> Event b
sync       : Event a     -> a
 syncT     : Time -> Time -> Event a -> a
 spawnExternal : Channel a -> Driver -> ExternalThreadId
```

Timed Synchronisation

syncT : Time -> Time -> Event a -> a

The diagram illustrates the components of a timed synchronisation function. It features three main elements: two red-outlined ovals containing the word "Time" and one oval containing the word "Event". The first "Time" oval is connected by a red arrow labeled "Relative Baseline" pointing to the second "Time" oval. The second "Time" oval is connected by a red arrow labeled "Relative Deadline" pointing to the "Event" oval.

syncT (msec 50) (msec 20) evt

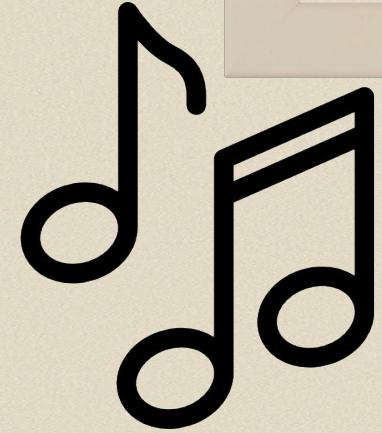
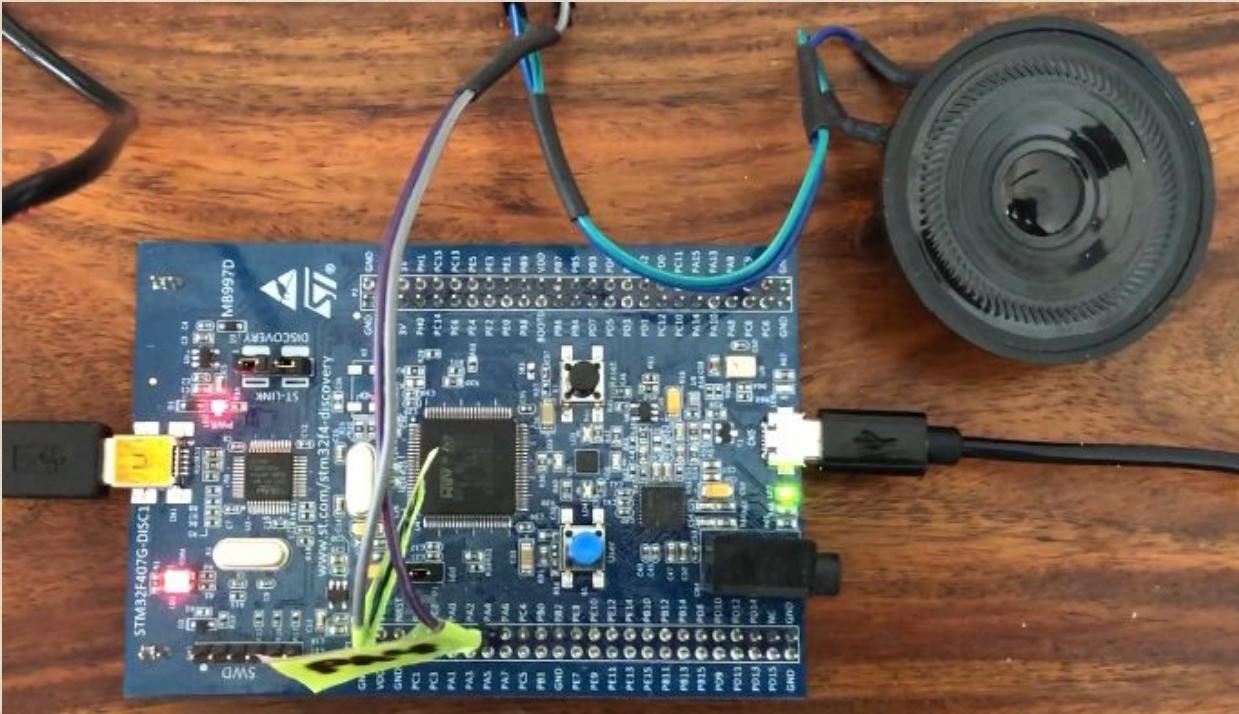


Logical “now”

1. Berry G. The Foundations of Esterel. MIT Press 2000.

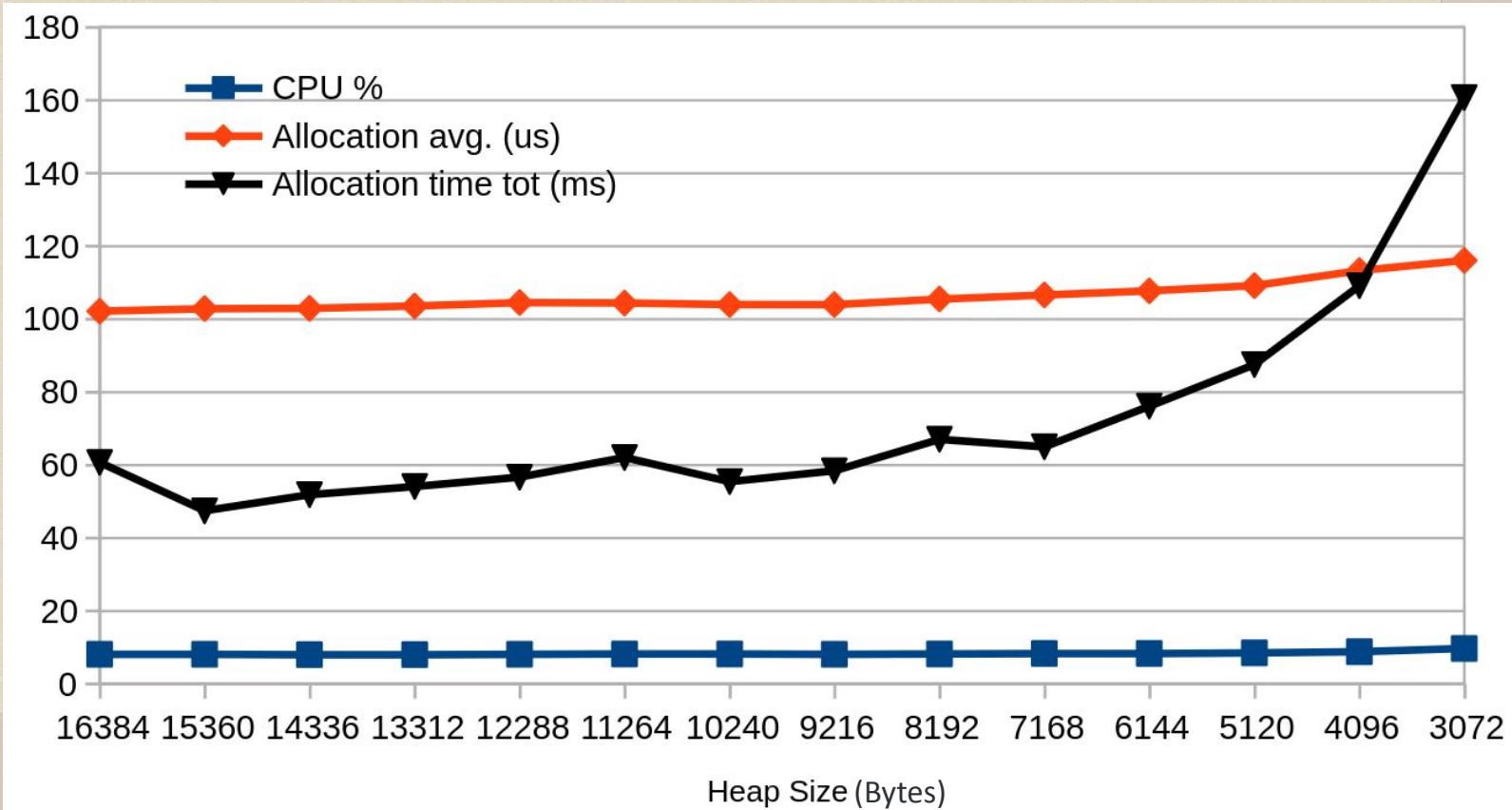
2. Nordlander J et al. Timber: A programming Language for Real-Time Embedded Systems. Technical Report 2002.

CASE STUDY

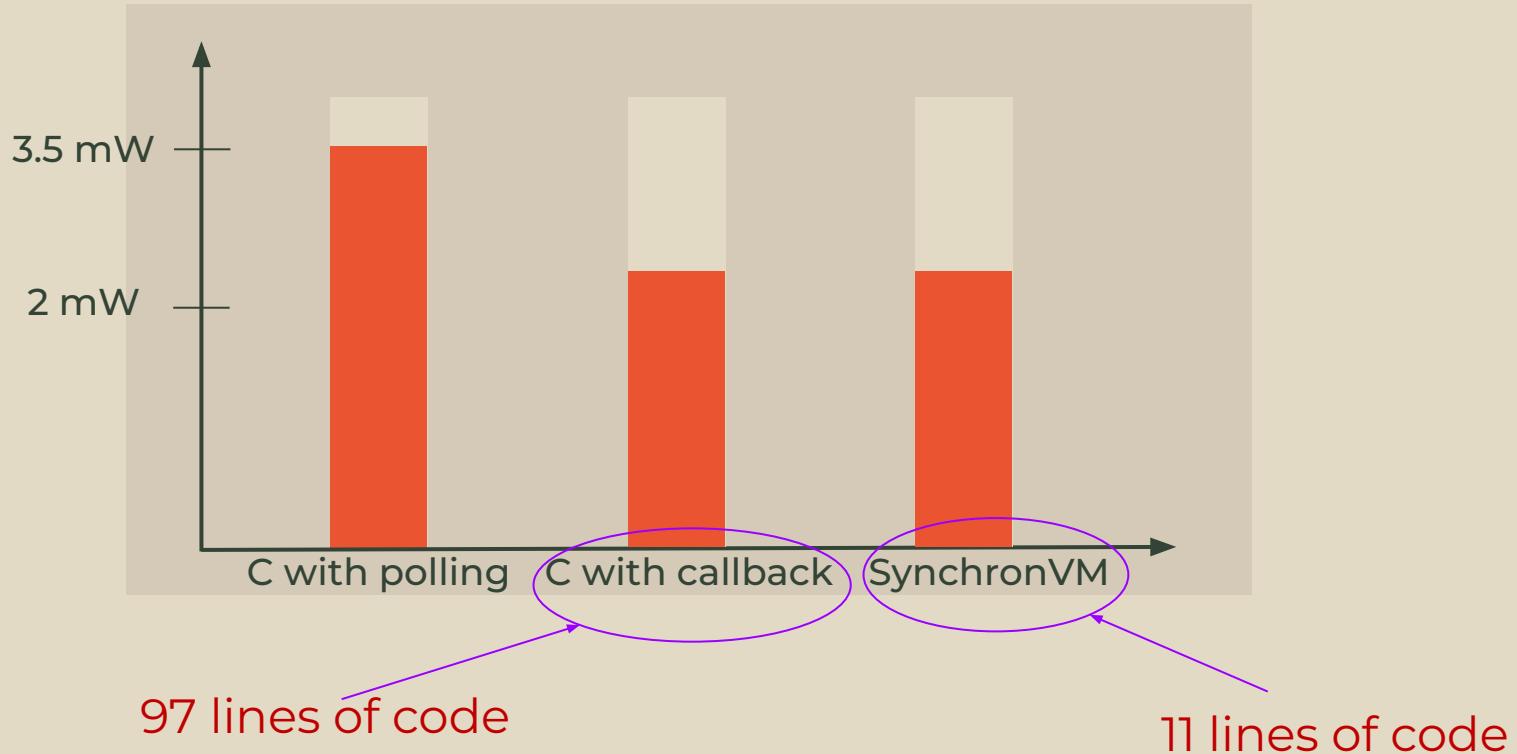


- Soft real-time
- ~440 Hz note frequency

Measurements



Button Blinky Power Usage



Jitter and Precision

```
while (1) {
    uint32_t state = GPIO_READ(23);
    if (state) {
        GPIO_CLR(23);
    } else {
        GPIO_SET(23);
    }
    usleep(400);
}
// main method and other setup elided
```

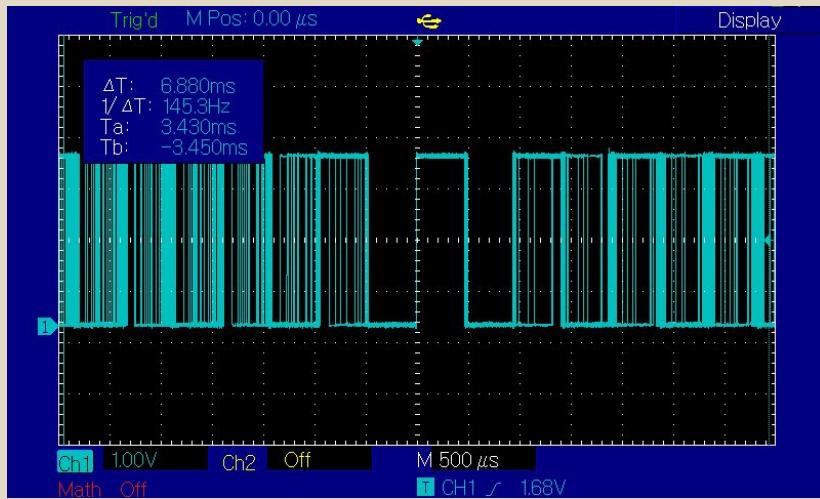
```
ledchan = channel ()

foo : Int -> ()
foo val =
    let _ = syncT 500 0 (send ledchan val)
    in foo (not val)

main =
    let _ = spawnExternal ledchan 1
    in foo 1
```

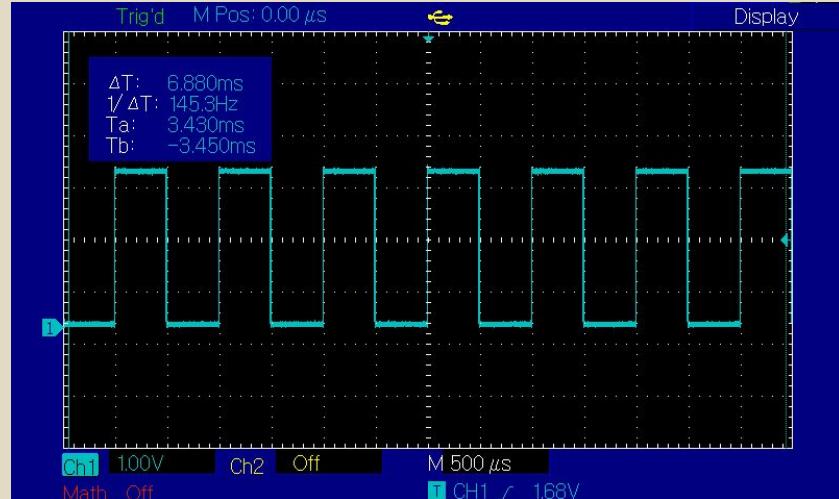
1 KHz Square Wave

Jitter and Precision



C / Raspberry Pi

1 KHz Square Wave



Synchron/STM32F4



Contributions

Attacker Model 1



HasTEE⁺

for reducing ***trust*** on
low-level software

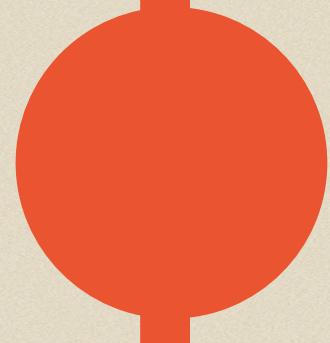
Attacker Model 2

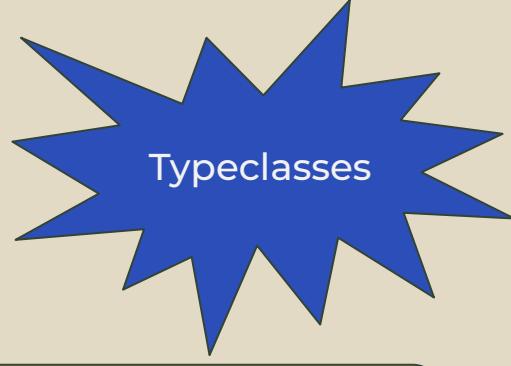
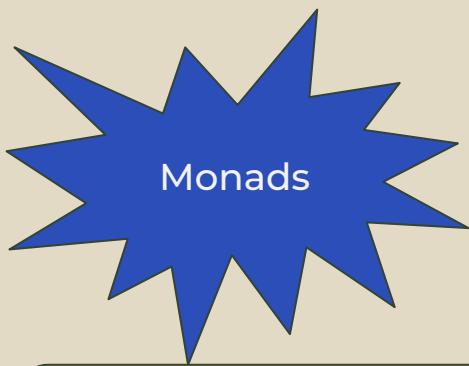


SynchronVM

for ***memory-safe, soft real-time***
embedded systems

“Securing Digital Systems
through Programming
Language Techniques”





Program Partitioning
(Papers I, II)

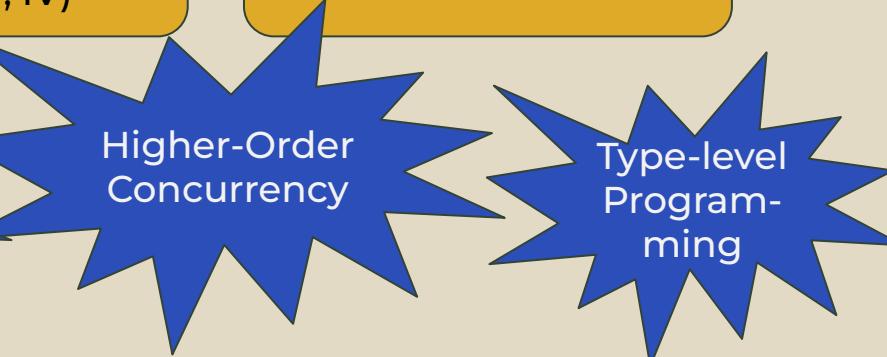
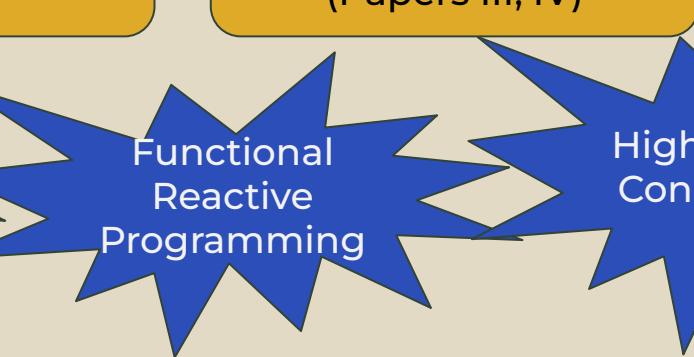
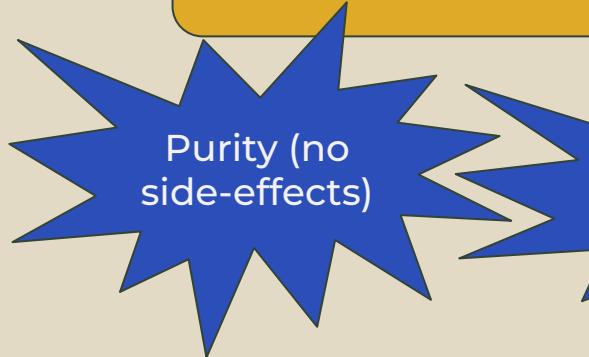
Information Flow
Control (Papers I, II)

Temporal
Programming
(Paper III)

Tierless Programming
(Paper II)

Structured
Concurrency
(Papers III, IV)

Resource Tracking
(Paper IV)



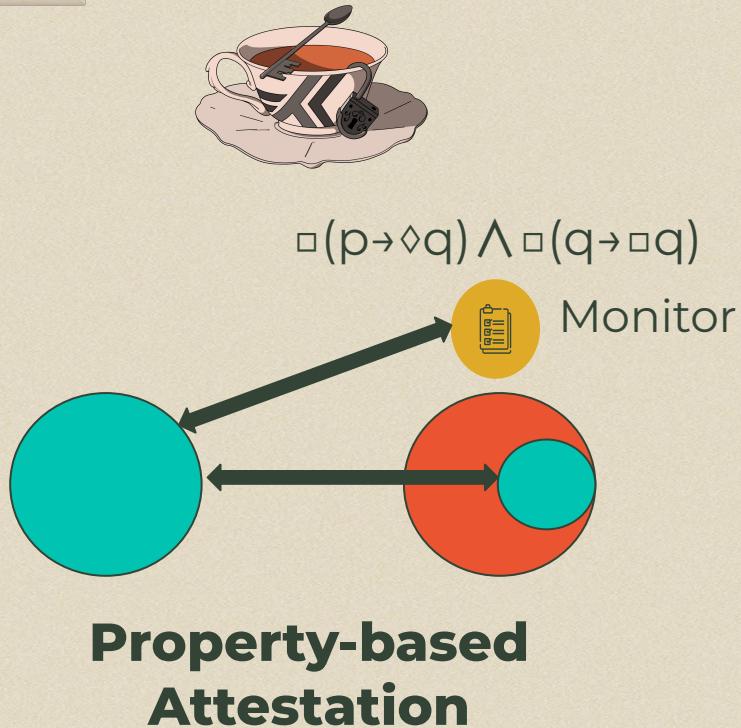
“Securing Digital Systems
through Functional
Programming Abstractions”



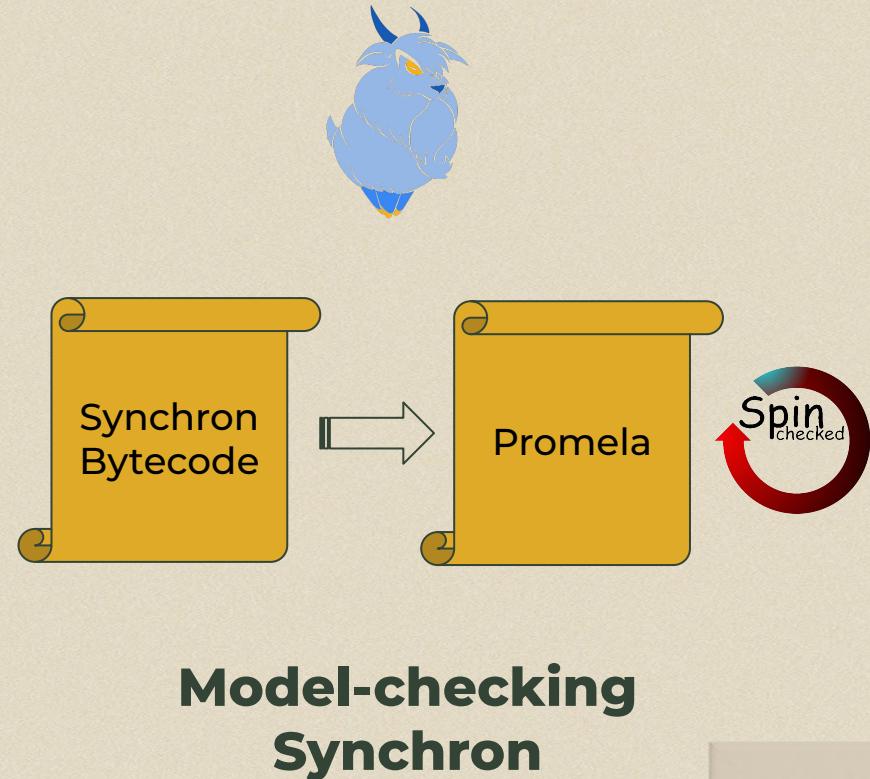
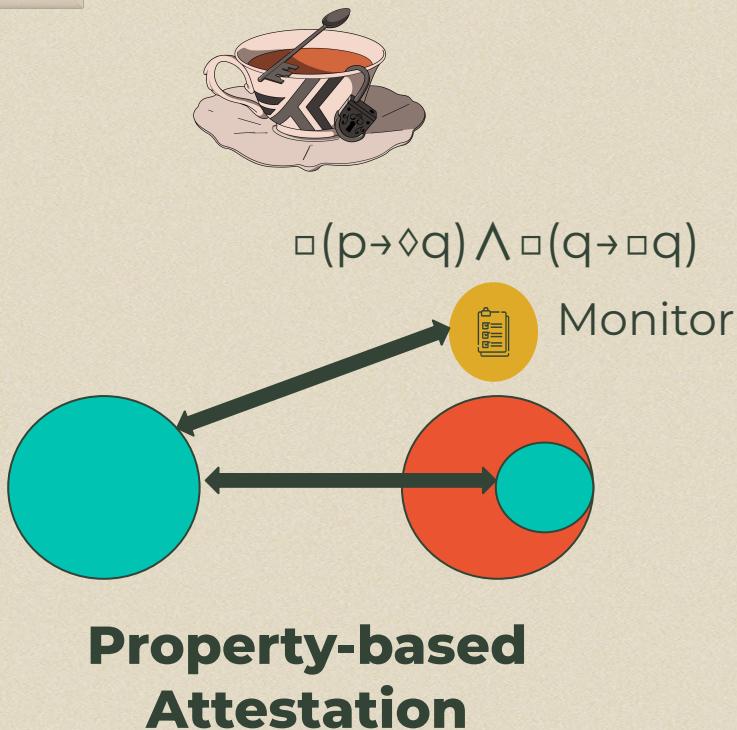
Future Work



Future Work



Future Work



ACKNOWLEDGEMENT

Thanks to Andrea Svensson for the cool logos.



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Thanks to the SSF Octopi project for funding this research.

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Thanks to the SSF Octopi project for funding this research.

Thanks to my co-authors.



Mary



Joel



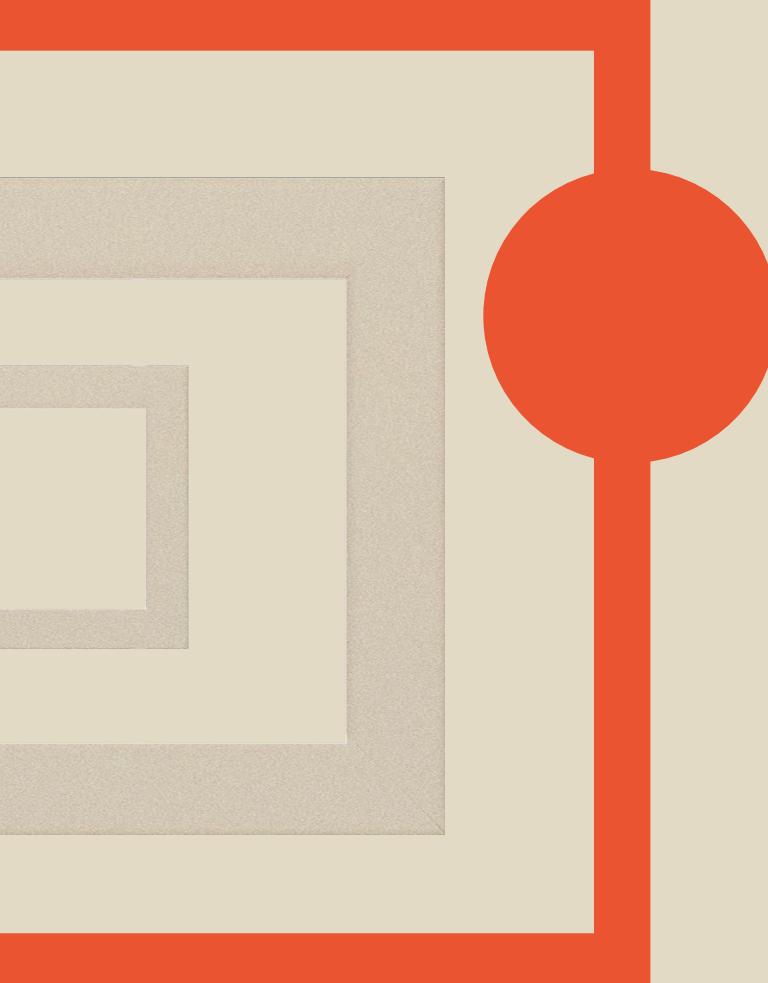
Alejandro



Robert



Koen



THANKS!

<https://github.com/Abhiroop/HasTEE>

<https://github.com/SynchronVM/SynchronVM>

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