

Cryptographic Hash Functions

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Cryptographic Hash Functions

- Important building block in cryptography
- Provide data integrity by construction of a short fingerprint or *message digest*
- Map arbitrary length inputs to fixed length outputs
 - For example, output length can be 256 bits
- Applications
 - Password hashing
 - Digital signatures on arbitrary length data
 - Commitment schemes

Properties

- Let $H : \mathcal{X} \mapsto \mathcal{Y}$ denote a cryptographic hash function
- $H(x)$ can be computed efficiently for all $x \in \mathcal{X}$
- If H is considered secure, three problems are difficult to solve
 - Preimage
 - Given $y \in \mathcal{Y}$, find $x \in \mathcal{X}$ such that $H(x) = y$
 - Second Preimage
 - Given $x \in \mathcal{X}$, find $x' \in \mathcal{X}$ such that $x' \neq x$ and $H(x) = H(x')$
 - Collision
 - Find $x, x' \in \mathcal{X}$ such that $x' \neq x$ and $H(x) = H(x')$
- If $|\mathcal{X}| \geq 2|\mathcal{Y}|$, then we have

Collision resistance \implies Second preimage resistance \implies Preimage resistance

(Proof in Section 4.2, Stinson, 3rd edition)

SHA-256

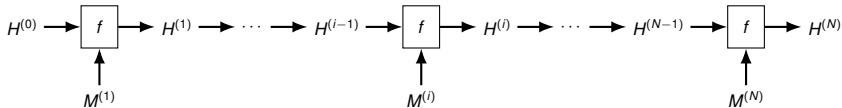
- SHA = Secure Hash Algorithm, 256-bit output length
- Accepts bit strings of length upto $2^{64} - 1$
- Announced in 2001 by NIST, US Department of Commerce
- Output calculation has two stages
 - Preprocessing
 - Hash Computation
- Preprocessing
 1. The input M is padded to a length which is a multiple of 512
 2. A 256-bit state variable $H^{(0)}$ is set to

$$\begin{aligned}H_0^{(0)} &= \mathbf{0x6A09E667}, & H_1^{(0)} &= \mathbf{0xBB67AE85}, \\H_2^{(0)} &= \mathbf{0x3C6EF372}, & H_3^{(0)} &= \mathbf{0xA54FF53A}, \\H_4^{(0)} &= \mathbf{0x510E527F}, & H_5^{(0)} &= \mathbf{0x9B05688C}, \\H_6^{(0)} &= \mathbf{0x1F83D9AB}, & H_7^{(0)} &= \mathbf{0x5BE0CD19}.\end{aligned}$$

SHA-256 Hash Computation

1. Padded input is split into N 512-bit blocks $M^{(1)}, M^{(2)}, \dots, M^{(N)}$
2. Given $H^{(i-1)}$, the next $H^{(i)}$ is calculated using a function f

$$H^{(i)} = f(M^{(i)}, H^{(i-1)}), \quad 1 \leq i \leq N.$$



3. f is called a *compression function*
4. $H^{(N)}$ is the output of SHA-256 for input M

SHA-256 Compression Function Building Blocks

- U, V, W are 32-bit words
- $U \wedge V, U \vee V, U \oplus V$ denote bitwise AND, OR, XOR
- $U + V$ denotes integer sum modulo 2^{32}
- $\neg U$ denotes bitwise complement
- For $1 \leq n \leq 32$, the shift right and rotate right operations

$$\text{SHR}^n(U) = \underbrace{000 \cdots 000}_n u_0 u_1 \cdots u_{30-n} u_{31-n},$$

$$\text{ROTR}^n(U) = u_{31-n+1} u_{31-n+2} \cdots u_{30} u_{31} u_0 u_1 \cdots u_{30-n} u_{31-n},$$

- Bitwise choice and majority functions

$$\text{Ch}(U, V, W) = (U \wedge V) \oplus (\neg U \wedge W),$$

$$\text{Maj}(U, V, W) = (U \wedge V) \oplus (U \wedge W) \oplus (V \wedge W),$$

- Let

$$\Sigma_0(U) = \text{ROTR}^2(U) \oplus \text{ROTR}^{13}(U) \oplus \text{ROTR}^{22}(U)$$

$$\Sigma_1(U) = \text{ROTR}^6(U) \oplus \text{ROTR}^{11}(U) \oplus \text{ROTR}^{25}(U)$$

$$\sigma_0(U) = \text{ROTR}^7(U) \oplus \text{ROTR}^{18}(U) \oplus \text{SHR}^3(U)$$

$$\sigma_1(U) = \text{ROTR}^{17}(U) \oplus \text{ROTR}^{19}(U) \oplus \text{SHR}^{10}(U)$$

SHA-256 Compression Function Calculation

- Maintains internal state of 64 32-bit words $\{W_j \mid j = 0, 1, \dots, 63\}$
- Also uses 64 constant 32-bit words K_0, K_1, \dots, K_{63} derived from the first 64 prime numbers $2, 3, 5, \dots, 307, 311$
- $f(M^{(i)}, H^{(i-1)})$ proceeds as follows

1. Internal state initialization

$$W_j = \begin{cases} M_j^{(i)} & 0 \leq j \leq 15, \\ \sigma_1(W_{j-2}) + W_{j-7} + \sigma_0(W_{j-15}) + W_{j-16} & 16 \leq j \leq 63. \end{cases}$$

2. Initialize eight 32-bit words

$$(A, B, C, D, E, F, G, H) = (H_0^{(i-1)}, H_1^{(i-1)}, \dots, H_6^{(i-1)}, H_7^{(i-1)}).$$

3. For $j = 0, 1, \dots, 63$, iteratively update A, B, \dots, H

$$T_1 = H + \Sigma_1(E) + \text{Ch}(E, F, G) + K_j + W_j$$

$$T_2 = \Sigma_0(A) + \text{Maj}(A, B, C)$$

$$(A, B, C, D, E, F, G, H) = (T_1 + T_2, A, B, C, D + T_1, E, F, G)$$

4. Calculate $H^{(i)}$ from $H^{(i-1)}$

$$(H_0^{(i)}, H_1^{(i)}, \dots, H_7^{(i)}) = (A + H_0^{(i-1)}, B + H_1^{(i-1)}, \dots, H + H_7^{(i-1)}).$$

References

- Chapter 5 of *Introduction to Modern Cryptography*, J. Katz, Y. Lindell, 2nd edition
- Chapter 4 of *Cryptography: Theory and Practice*, Douglas R. Stinson, 3rd edition
- Chapter 8 of *A Graduate Course in Applied Cryptography*, D. Boneh, V. Shoup, www.cryptobook.us
- Chapter 3 of *An Introduction to Bitcoin*, S. Vijayakumaran, www.ee.iitb.ac.in/~sarva/bitcoin.html