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Subject Name: Operating System

Subject Code: CS603

Dept/Batch: B.Tech (CS-E)

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Answer for CS603

1→2) FIFO: We have to find the first segment large enough to accommodate the incoming segment. If reallocation is not possible & no one segment is large enough select a combination of segments ~~that~~ whose memories are contiguous, which are "closest to the first of the list" & which can accommodate the new segment. If ~~relabla~~ reallocation is possible, rearrange the memory so that the first N segment large enough for the incoming segment are contiguous in memory. Add any leftover space to the free-space list in both cases.

LRU : We have to select the segment that has not been used for the longest period of time and that is large enough, adding any leftover space to the free space list. If no one segment is large enough, select a combination of the "oldest" segments that are large enough. If reallocation is available, rearrange the oldest N segments to be contiguous in memory & replace those with the new segment.

2-2) Semaphore is simply a variable that is non-negative & shared between threads. A semaphore is signalling mechanism, and a thread that is waiting on a semaphore can be signaled by another thread. It uses two atomic operations, 1) wait, & 2) signal for the process synchronization. There are two common kinds of semaphores i.e.

- Counting semaphore: This type of semaphore uses a count that helps how to be acquired or released numerous times.
- Binary semaphore: The binary semaphore are quite similar to counting semaphore but their value is restricted to $0 \leq 1$. In this type of semaphore the wait operation works only if semaphore = 1, & the signal operation succeeds when semaphore = 0. It is easy to implement than counting semaphore.

Structure for operation $P() \leftarrow V()$ are

$P(\text{semaphore } S) \{$

while $(S = 0) ; /* wait until S=0 */$

$S = S - 1 ;$

$\}$

$V(\text{semaphore } S) \{$

$S = S + 1 ;$

$\}$

Implementate of binary semaphore

struct semaphore {

enum value (0, 1);

Queue < process > q;

} P(semaphore s)

{

if $(s.\text{value} == 0) \{$

$s.\text{value} = 0;$

$\}$

```
else {  
    q.push(P);  
    sleep(1);  
}  
}  
v(somethu s)  
{  
    if (s.q is empty) {  
        s.value = 1;  
    }  
    else {  
        q.pop();  
        wakeup();  
    }  
}
```


3⇒1) If we simulate deadlock with a Table which is standing on its four legs then we can also simulate four legs with the four conditions which when occurs simultaneously. However it can be prevented by four condition:

- Mutual Exclusion

- Mutual exclusion from the resource POV is the fact that a resource can never be used by more than one process simultaneously which is fair enough but that is the main reason behind the deadlock.

However, if we can be able to violate resource behaving in the mutually exclusive manner then the deadlock can be prevented.

Spooling: By using FCFS, the process doesn't have to wait for the printer & it can continue whatever it was doing.

- Hold and Wait

- This condition lies when a process holds a resource & waiting for some other resource to complete its task. Deadlock occurs because there can be more than one process which are holding one resource & waiting for others in the cyclic order.

A process must not wait for any resource once the execution has been started.

$!(\text{Hold and wait}) = !\text{hold} \text{ or } !\text{wait}$ (negation of hold & wait is, either you don't hold or you don't wait)

- No Preemption

- Deadlock arises due to the fact that a process can't be stopped once it starts. However, if we take the resource away from the process which is causing deadlock we can prevent deadlock.

- Circular wait

- To violate circular wait we can assign a priority no. to each of the resource. A process can't request for a lesser priority resource.

This ensures that not a single process can request a resource which is being utilized by some other process and no cycle is formed.

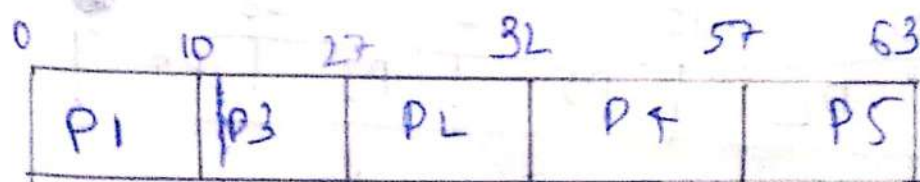
Condition	Approach	Is practical
Mutual Exclusion	Spooling	X
Hold & wait	Request for all the resources	X
No preemption	Share all the resources	X
Circular wait	Assign priority to each resource	✓

4 → 2)

P-id	A.T	B.T
D ₁	0	10
P ₂	1	5
P ₃	0	17
P ₄	2	25
P ₅	7	6

For FCFS

Gantt chart

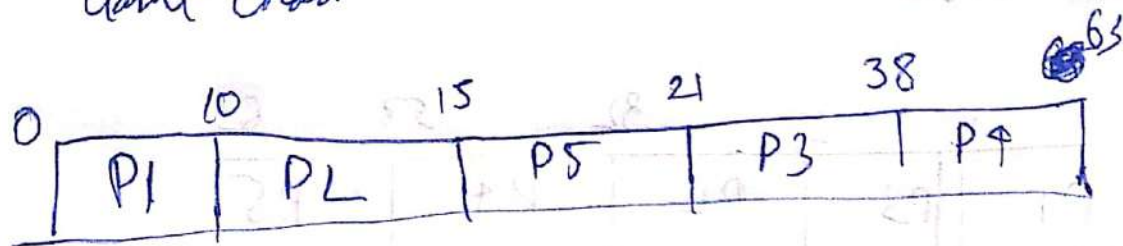
~~A.T~~

	F.T	TAT	CT
P ₁	10	10 - 0 = 10	10 - 10 = 0
P ₂	32	32 - 1 = 31	31 - 5 = 26
P ₃	27	27 - 0 = 27	27 - 17 = 10
P ₄	57	57 - 2 = 55	55 - 25 = 30
P ₅	63	63 - 7 = 56	56 - 6 = 50

$$AWT = \frac{0 + 26 + 10 + 30 + 50}{5} = \frac{116}{5} = 23.2$$

For
SJF,

Gantt chart



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	E T	TAT	U.T
P_1	10	10-0=10	10-10=0
P_2	15	15-1=14	14-5=9
P_3	38	38-0=38	38-17=21
P_4	63	63-2=61	61-25=36
P_5	21	21-7=14	14-6=8

$$AWT = \frac{0 + 9 + 21 + 36 + 8}{5} = \underline{\underline{14.8}}$$

5⇒1) File operations: A file is an abstract data type. In defining a file properly, we need to consider the operation that can be performed on files. There are six basic operations within an operating system.

(i) Creating a file: There are two steps necessary for creating a file. First space in the file system must be found for the file. Second, an entry for the new file must be made in the directory.

ii) Writing a file: To write a file, we make a system call which specifies about both the name of the file along with the information to be written to the file.

iii) Reading a file: To read a file, we use a system call which specifies the name of the file & where within memory the next block of file should be placed.

repositioning inside a file. The directory is then searched for the suitable entry, and the 'current file positioning' pointer is repositioned to a given value. This file operation is also known as "file seek".

Deleting a file: Deletes the file or directory, releases all file space so that other files can re-use that space.

Truncating a file: The user may want to remove the content of a file but keep the attributes same. Rather than deleting the file and then recreating it, this mode utility allows ~~as to~~ all attributes to remain unchanged.

file type	usual extension	function
Executable	exe, com, bin, or none	read to run machine language program
Object	obj, o	Compiled, machine- language, not linked.
Batch	bat, sh	Commands to the command interpreter
Text	txt, doc	Record data, document
Multimedia	mpeg, mov, rm	Binary file contains audio or AV information

6.7) Race condition: It is an undesirable situation that occurs when a device or system attempts to perform two or more operations at the same time, but because of the nature of the device or system, the operations must be done in the proper sequence to be done correctly.

Example: A simple example is light switch. In some homes there are multiple light switches connected to a common ceiling light. When these types of circuits are used, the switch position becomes irrelevant. If the light is on, moving either switch from its current position turns the light off. Similarly, in the turning off the light. Imagine if what might happen if two people tried to turn on the light using two different switches at exactly the same time. One might cancel the other or the two actions might trip the circuit breaker.

Critical section: It is a segment of code which can be accessed by a ^{single} process at a specific point of time. The section consists of shared data resources that require to be accessed by other processes.

- The entry ~~lock~~ to the critical section is handled by the `wait()` & represented as `P()`.
- The exit from a critical section is controlled by `signal()` function, represented as `V()`.

Example:

Process P_i

`FLAG[i] = true`

`while (turn != i) AND`

`(CS is ! free)`

`{ wait;`

`}`

P_1

P_2

P_3

P_n

Flag

False

True

True

False

Critical Section `FLAG[i] = false`

`turn = i;` // choose another process to go to CS

sleep and wake: If a process is unable to enter a critical section, it is put to sleep/ temporarily suspended. Afterwards when the critical section is no longer occupied, the process is woken up.

There is a popular example called producer consumer problem which is most popular in problem illustrating sleep & wake mechanism

```
# define N 100
```

```
int count = 0
```

```
void Producer(void)
```

```
int item;
```

```
while(true)
```

```
produce_item (& item);
```

```
if (count == N) sleep(producer);
```

```
enter_item(item); // puts them in buffer
```

```
count ++;
```

```
if (count == 1) wake up (consumer);
```


7 → 2) → i) The content of Matrix need is given below

	A	B	C	D
P ₀	0	0	0	0
P ₁	0	7	5	0
P ₂	1	0	0	2
P ₃	0	0	2	0
P ₄	0	6	4	2

ii) The system is in safe state.
AND the safe Sequence is
P₀, P₁, P₃, P₄.

iii) After satisfying P₁ request, the system becomes the following state

	ABCD	ABCD	ABCD	ABCD
P ₀	0012	0012	0000	1100
P ₁	1420	1750	0330	
P ₂	1354	2356	1002	
P ₃	0632 0014	0656	0020 0642	
P ₄	0014	0656	0642	

• The system is still in safe state &
P₀ P₂ P₁ P₃ P₄
is safe sequence

Q-2) Race condition : It is an undesirable situation that occurs when a device or system attempts to perform two or more operations at the same time, but because of the nature of the device or system, the operations must be done in the proper sequence to be done correctly.

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light. When these types of circuits are used, the switch position becomes irrelevant.

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`while (turn != i) AND`

`(CSK is ! free))`

`{ wait;`

`}`

Critical Section `FLAG[i] = false`

`turn = i; // choose another process to go to CS`

Flag
P_1 False
P_2 True
P_3 True
\vdots
P_n False

sleep and wake: If a process is unable to enter a critical section, it is put to sleep/ temporarily suspended. Afterwards when the critical section is no longer occupied, the process is woken up.

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define N 100

int count = 0

void Producer(void)

int item;

while(true)

produce_item (&item);

if (count == N) sleep(producer);

enter_item(item); // puts them in buffer

count++;

if (count == 1) wake up (consumer);

Q24) Binding of instructions and data to memory address can be done at

- Compile time: If memory location known as prior, absolute code can be generated. must recompile code if starting location changes.

- MS-DOS.COM format programs are absolute code.

- Load time: Must generate relocatable code if memory location is not known at compiling time.

- Execution time: Binding delayed until run time if the program can be moved during its execution from one memory segment to another. Need hardware support for address maps (eg. base and limit registers).

Base register & limit register are special hardware registers. When a process is run the base register is loaded with the physical location where the process begins in memory.

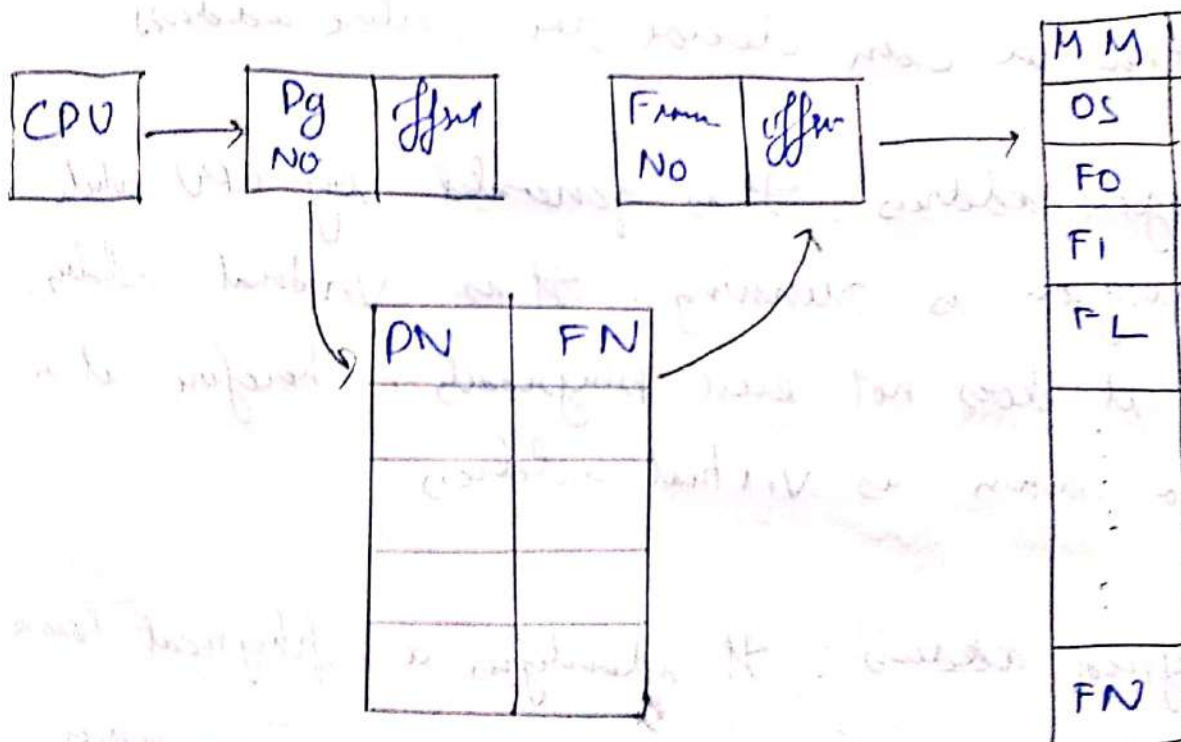
The limit register is loaded with the length of the process. In other words they define the logical address space.

External fragmentation occurs when the memory allocator leaves sections of unused memory blocks b/w portions of allocated memory.

For eg. if several memory blocks between portions of allocated ~~memory~~ is a continuous line but one of the middle block in the line is freed, the free block is fragmented. The block is still available for use by the allocator later if there is need for memory that fits the block but the block is now unusable for larger memory needs.

10-2) Some of the important issues of memory management

- (i) Memory fragmentation (internal & external)
- (ii) Memory leak
- (iii) Wrong address assignment
- (iv) Virtual memory management (paging)



When the system allocates a frame to any page, it translates this logical address into a physical address & creates entry into the page table to be used throughout the execution of the program.

Address translation in contiguous memory is an easy task because if we pick a data from secondary memory & push it into main memory - they are picked the data in a contiguous form which means if we know the base address of data then we can decide the entire address.

Logical address: It is generated by CPU while program is running. It is virtual address as it does not exist physically; therefore, it is also known as virtual address.

Physical address: It identifies a physical location of required data in a memory. The user never directly deals with the physical address but can access by its corresponding logical address.