Integration Jesting Deturning the correctness of interface is the focus of Integration Isstry. why Integration & required O Data may be lost during interfacing global dalta may also chuse publica Sub function Imay not nork be nihen combleined Method to integrate & Fist Unit Component are taken one by one and integrated incrementally Debugging & fault isolation betypne easfer

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3	Cluster is tested
	Driver is removed & the cluster
	are combined mith new module
	morning up in hierarchy.
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Validation Testing \* Jocus on used - visible Action & user recognizable output from the system + ( latidation succeeds when software lunction in a can be expected by the custom " Validation occtivian: ( us is & Section in ISRS adocurrent which colesquibes who dissud fundionally forms the basis of uplidation feets You is validation achieved? Though a series of lest that identificate the existence of fundament read by user in Sh Twhat idoes Validation Testing Enquire & functionality a achieved. 1) correct behaviour of a schierer # ferformance constraints met. I Document were correct \* Various non-functional require (Usubely) or A deficiency D list may be dist out in cake something the missing ( incorrect Alpha Bela (406)

The manufacture with the transfer the System Justing + It encopuedt software with other cystem elements elements

r System Testing ensure that all demins
harder integrated will with the software yes of System Testing Recovery Jesting It must endou that system must suconer from faults & ensume perocusing with little ) or no downline Aim: To develop a fault tolerant system Any processing fault Dshould not beling down the entire) system · 8/w must reconder & resume its normal process author specified timeperoid Houe it is Done? r be intentionally force the system to fail differen marys of their me treerify that the System is done Speroperly. Verconey 01 If the eccourty is automated reinitral, aly checkpointing michanism, data recovery enslort and enalisated for connections I sucoway suguise humban intervention 1 the mean - time - to -repair (MTTR) is chaliated to determine whether it is within acceptance limity

Security Testing wify that a puotiction mechanism built with system will actually puolect it from attacks. lister Responsibility o of attack is highertan eact was an intender true Testing It execute the system in manner demands elecuralis in abnormal quantily Plu mun excession memory Seguite dalabase reguest Semultaneous log-in Elormance Lesting Ar Ainsed at listry the centure kerpancine of Rofferay or consider that pulformace is monitore cont beging from listing itself