

Tutorial - I.

①

11) Three - identical stages in a multistage amplifier have an overall upper 3dB frequency of 40KHz and lower 3dB frequency of 15KHz. What is the operating Bandwidth of each amplifier stage?

sol

no. of stages = 3

Identical stages so,

$$f_L^1 = f_L^2 = f_L^3$$

$$f_H^1 = f_H^2 = f_H^3$$

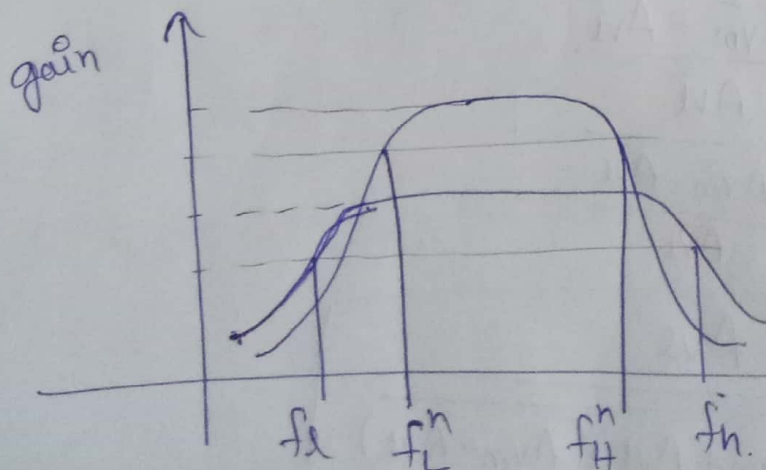
The overall lower 3dB frequency $f_L^3 = 15\text{KHz}$

The overall ~~lower~~ upper 3dB frequency $f_H^3 = 40\text{KHz}$.

$$f_L = f_L^n \left[\sqrt{2^{1/n} - 1} \right]$$

$$= 15 \times 10^3 \left[\sqrt{2^{1/3} - 1} \right] = 7.647\text{KHz}$$

$$f_H = f_H^n / \sqrt{2^{1/n} - 1} = 40 \times 10^3 / \sqrt{2^{1/3} - 1} = 78.458\text{KHz}$$



$$f_L = 7.65\text{KHz}$$

$$f_L^n = 15\text{KHz}$$

$$f_H = 78.49\text{KHz}$$

$$f_H^n = 40\text{KHz}$$

② The RC coupled amplifier has mid frequency gain = 100. The values of lower and upper cutoff frequencies are $f_L = 80\text{ Hz}$ and $f_H = 80\text{ kHz}$. Find the frequency at which the gain is reduced to 80.

sol Given lower cutoff frequency $f_L = 80\text{ Hz}$.

upper cutoff frequency $f_H = 80\text{ kHz}$.

mid frequency gain $A_{vm} = 100$.

where

$$\left(\frac{A_{vl}}{A_{vm}}\right) = \frac{1}{\sqrt{1 + (f_L/f)^2}}$$

$$\left(\frac{A_{vl}}{A_{vm}}\right)^2 = \frac{1}{1 + (f_L/f)^2}$$

$$1 + (f_L/f)^2 = \frac{A_{vm}^2}{A_{vl}^2}$$

$$(f_L/f)^2 = \frac{A_{vm}^2}{A_{vl}^2} - 1$$

$$(f_L/f)^2 = \frac{A_{vm}^2 - A_{vl}^2}{A_{vl}^2}$$

$$f_L/f = \sqrt{\frac{A_{vm}^2 - A_{vl}^2}{A_{vl}^2}}$$

$$f = \frac{f_L \cdot A_{vl}}{\sqrt{(A_{vm} + A_{vl})(A_{vm} - A_{vl})}}$$

$$f = \frac{80 \times 80}{\sqrt{(100+80)(100-80)}} = 106.66 \text{ Hz}$$

at $f = 106.66 \text{ Hz}$, the gain is reduced to 80.

similarly

$$\left(\frac{A_{vh}}{A_{vm}} \right) = \frac{1}{\sqrt{1 + (f/f_h)^2}}$$

$$1 + (f/f_h)^2 = \left(\frac{A_{vm}}{A_{vh}} \right)^2$$

$$(f/f_h)^2 = \left(\frac{A_{vm}}{A_{vh}} \right)^2 - 1$$

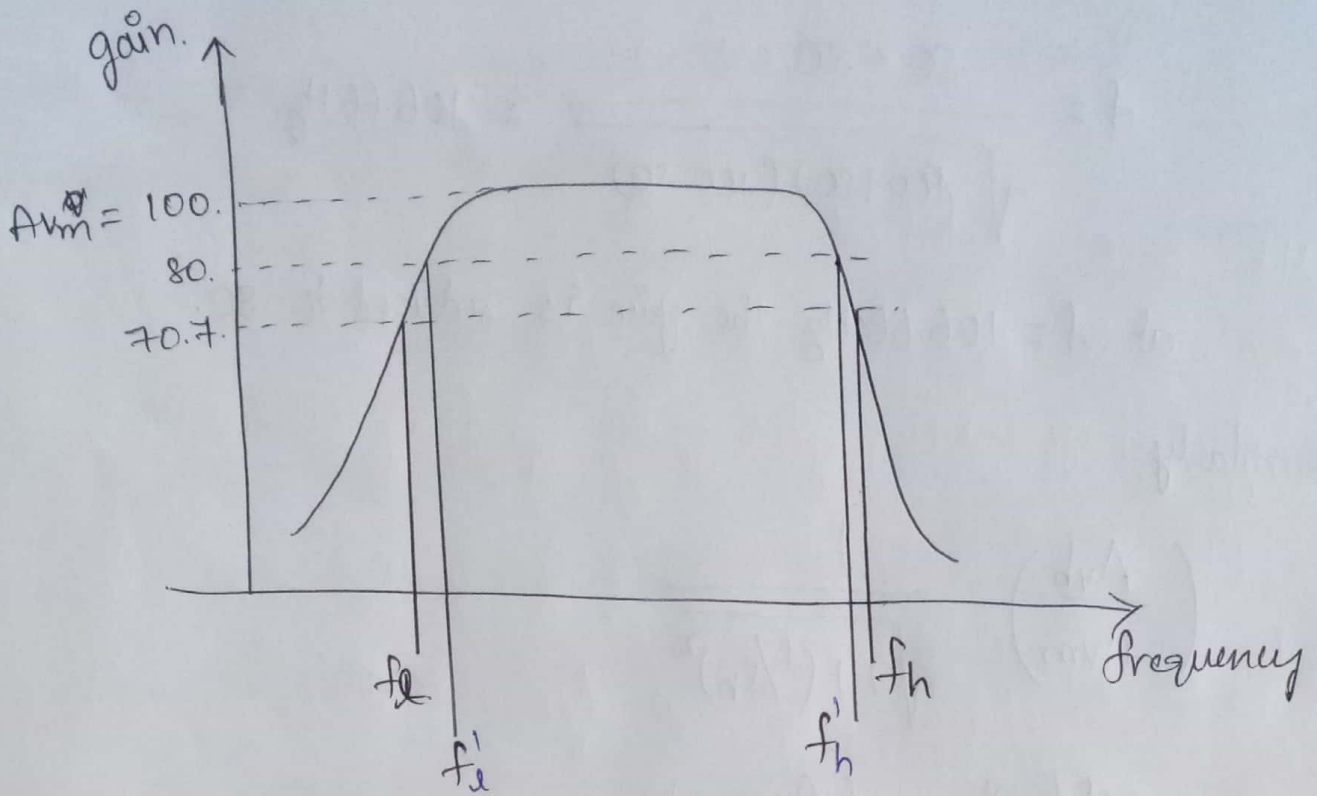
$$f/f_h = \sqrt{\frac{A_{vm}^2 - A_{vh}^2}{A_{vh}^2}}$$

$$f = \frac{f_h \cdot \sqrt{(A_{vm} + A_{vh})(A_{vm} - A_{vh})}}{A_{vh}}$$

$$f = \frac{80 \times 10^3}{80} \sqrt{(100+80)(100-80)}$$

$$f = 60 \text{ kHz}$$

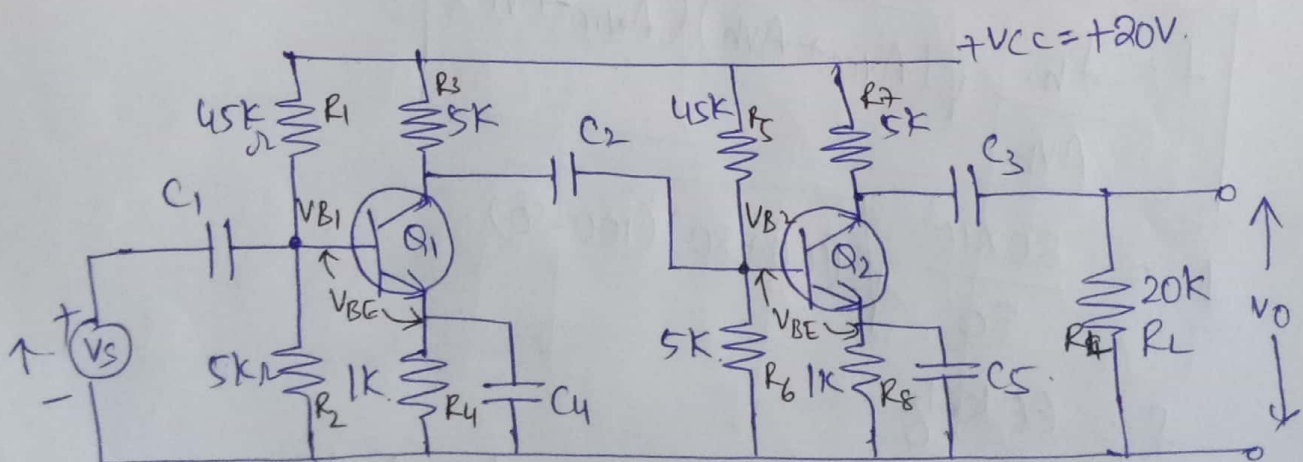
At $f = 60 \text{ kHz}$, the gain is reduced to 80.



$$f_L = 80 \text{ Hz} \quad f_L' = 106.6 \text{ Hz}$$

$$f_h = 80 \text{ kHz} \quad f_h' = 60 \text{ kHz}$$

③ Compute the overall voltage gain for the 2-stage RC coupled amplifiers shown in below figure. Express the gain in dB by considering $V_{BE} = 0.7 \text{ V}$, $\beta_1 = \beta_2 = 100$.



sol To find the overall gain of the above circuit, Initially individual stages gains will be calculated as a function of impedance ratios.

(5)

Due to V_{CC} the voltage across Base of second transistor Q_2 V_{B2} is given as

$$V_{B2} = \frac{V_{CC} \cdot R_6}{R_5 + R_6} = \frac{20 \times 5 \times 10^3}{50 \times 10^3} = 2V.$$

$$V_{B2} = V_{BE} + V_{E2}.$$

$$V_{E2} = V_{B2} - V_{BE} = 2V - 0.7 = 1.3V.$$

Now the current through R_8 is given as

$$I_{E2} = \frac{V_{E2}}{R_8} = \frac{1.3V}{1K} = 1.3mA.$$

The internal forward resistance r_{e2} is depends on the thermal voltage and the current I_{E2} .

$$r_{e2} = \frac{V_T}{I_{E2}}$$

where $V_T \rightarrow$ Thermal voltage at $T = 27^\circ$

$$V_T = \frac{T}{11,600} = \frac{300}{11,600} = 25.86mV.$$

Then

$$r_{e2} = \frac{25.86m}{1.3m} = 19.89\Omega.$$

The transistor having $\beta_2 = 100$, then the the impedance at base of second transistor is

$$Z_{in(B2)} = r_{e2} \times \beta_2 = 1.989K\Omega.$$

The total i/p impedance to the second stage is the parallel combination of $Z_{in(B2)}$, R_5 & R_6 .

$$Z_{in2} = R_5 \parallel R_6 \parallel Z_{in(B2)}$$

$$Z_{in2} = 1.378 \text{ k}\Omega$$

The Effective collector load for 2nd stage

$$R_{ac2} = R_7 \parallel R_L$$

$$R_{ac2} = 4 \text{ k}\Omega$$

Then the voltage gain of second stage

$$A_{V2} = - \frac{R_{ac2}}{r_{e2}} = \frac{4 \text{ k}}{19.89} = -201.1$$

$$\boxed{A_{V2} = -201.1}$$

similarly the base voltage at ~~trans~~ base terminal of the first transistor Q_1 is

$$V_{B1} = \frac{R_2}{R_1 + R_2} \cdot V_{CC} = \frac{5 \times 10^3 \times 20}{20 \times 10^3} = 2 \text{ V}$$

$$V_{E1} = V_{B1} - V_{BE} = 2 - 0.7 = 1.3 \text{ V}$$

$$I_{E1} = \frac{V_{E1}}{R_{E1}} = \frac{1.3}{1 \text{ k}} = 1.3 \text{ mA}$$

$$r_{e1} = \frac{V_T}{I_{E1}} = 19.89$$

$$A_{V1} = - \frac{R_{ac1}}{r_{e1}}$$

$$R_{ac1} = R_3 \parallel Z_{in2} = 1.080 \text{ k}\Omega$$

$$A_{V1} = - \frac{1.080 \times 10^3}{19.89} = -54.31$$

The overall voltage gain is given as

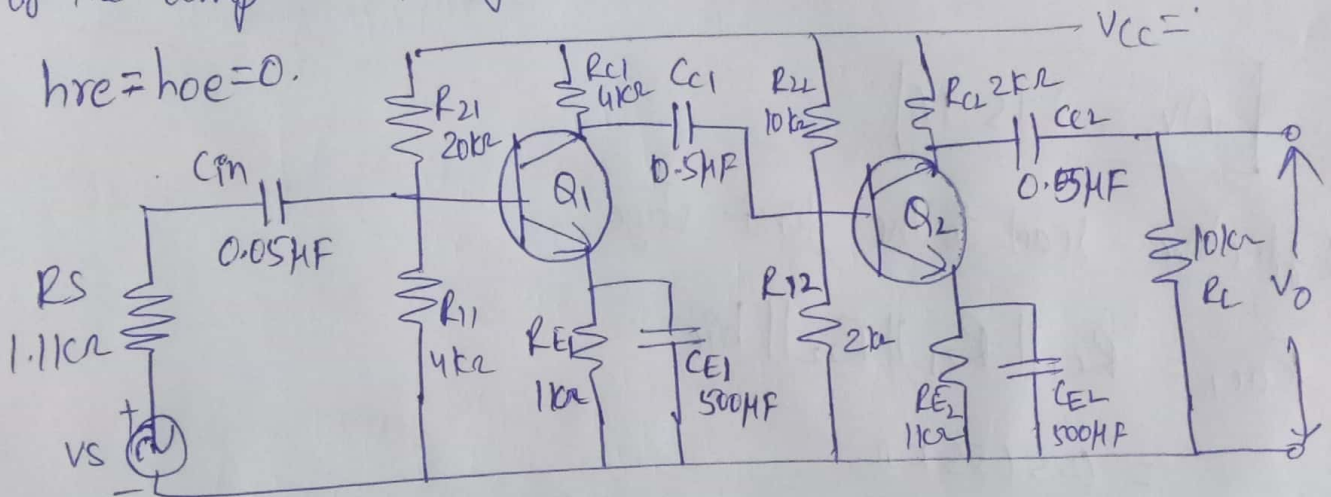
$$A_v = A_{v1} \cdot A_{v2}$$

$$= (-201.1)(-54.31)$$

$$A_v = 10,922.2$$

$$(A_v)_{dB} = 8.76 \text{ dB}$$

- ④ Determine the input impedance, output impedance and overall voltage gain for a 2-stage RC coupled amplifier shown in below figure. Load resistance connected across the output terminals of the amplifier is of $10k\Omega$. $h_{ie} = 1.1k\Omega$, $h_{fe} = 50$, $h_{re} = h_{oe} = 0$.



sol when looking into the base terminals of the transistor Q_1 , h_{ie} is seen to be in series with $h_{re}V_{out}$. Since $h_{re} = 0$, so the i/p impedance to the transistor base

$$Z_b = h_{ie}$$

Input impedance to the circuit is

$$Z_{in} = R_{11} \parallel R_{21} \parallel h_{ie} = 827\Omega$$

⑧

The output impedance of the circuit is

$$Z_{out} = \frac{1}{h_{oe}} \parallel R_{C2} = R_{C2}$$

$$Z_{out} = 2k\Omega$$

voltage gain of the second stage is

$$A_{V_2} = \frac{-h_{fe}}{h_{ie}} (R_{C2} \parallel R_L)$$

CE amplifier

$$[A_v = -\frac{h_{fe}}{h_{ie}} \cdot R_L]$$

$$= \frac{-h_{fe} R_{C2} R_L}{h_{ie} (R_{C2} + R_L)} = \frac{-50 \times 2 \times 10^3 \times 10 \times 10^3}{1.1 \times 10^3 (12) \times 10^3}$$

$$\boxed{A_{V_2} = -75.76}$$

Effective load to the first stage,

$$R_{ac1} = R_{C1} \parallel R_{21} \parallel R_{22} \parallel h_{ie}$$

$$= 0.5685k\Omega$$

voltage gain of the first stage is

$$A_{V_1} = \frac{-h_{fe}}{h_{ie}} \cdot R_{ac1} = \frac{-50}{1.1 \times 10^3} \times 0.5685 \times 10^3 = -25.84$$

overall voltage gain $A_v = A_{V_1} \cdot A_{V_2}$ $A_{V_1} = -25.84$

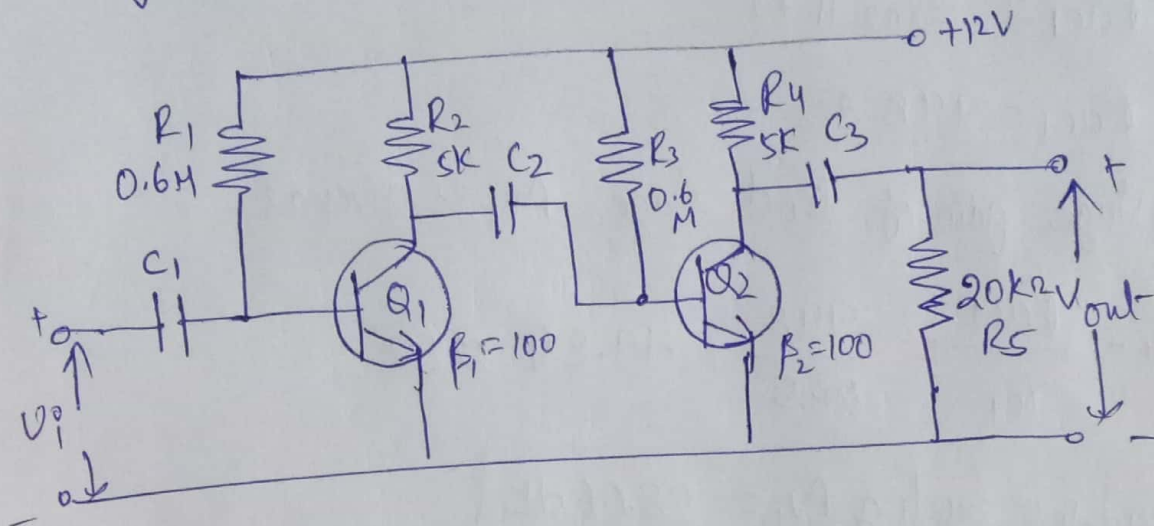
$$A_v = -75.76 \times -25.84$$

$$\boxed{A_v = 1958}$$

⑤ For the two-stage RC coupled two level audio amplifier shown in below figure. Find the following

(i) r_i (ii) A_{V1} (iii) A_{V2} (iv) A_V in dB.

Neglect V_{BE} and take $r_e = 25mV/IE$.



Sol

The current at the base of first transistor Q_1

$$I_{B1} = \frac{V_{CC}}{R_1} = \frac{12}{0.6M} = 0.02mA$$

Emitter current in fixed bias

$$I_{E1} \approx I_{C1} = \beta_1 I_{B1} = 100 \times 0.02m = 2mA$$

$$r_{e1} = \frac{25mV}{2mA} = 12.5\Omega$$

$$Z_{B1} = \beta_1 r_{e1} = 100 \times 12.5 = 1250$$

The i/p impedance r_i is given as

$$r_i = R_1 \parallel Z_{B1} = \frac{0.6 \times 10^6 \times 1250}{1250 + 0.6 \times 10^6} = 1247\Omega$$

$$r_i = 1.247K\Omega$$

As $R_1 = R_3$, the base current

$$\left. \begin{aligned} I_{B2} &= 0.02 \text{ mA} \\ Z_{B2} &= 1250 \\ Z_{in2} &= 1.247 \text{ k}\Omega \end{aligned} \right\} V_{e2} = 12.5$$

then $R_{ac1} = Z_{in2} \parallel R_2$.

$$R_{ac1} = 998 \Omega$$

The voltage gain of first stage A_{v1} is given as

$$A_{v1} = \frac{R_{ac1}}{V_{e1}} = \frac{998}{12.5} = 79.84 \approx 80$$

$$(A_{v1})_{dB} = 20 \log A_{v1} = 38.06 \text{ dB}$$

$$\begin{aligned} \text{The } R_{ac2} &= R_4 \parallel R_5 \\ &= 4000 \Omega \end{aligned}$$

The voltage gain of second stage A_{v2} is given as

$$A_{v2} = \frac{R_{ac2}}{V_{e2}} = \frac{4000}{12.5} = 320$$

$$(A_{v2})_{dB} = 20 \log 320 = 50.1 \text{ dB}$$

The overall voltage gain $A_v = A_{v1} \cdot A_{v2}$

$$A_v = 80 \times 320 = 25.6 \text{ K}$$

$$(A_v)_{dB} = 20 \log A_v = 88 \text{ dB}$$

$$(A_v)_{dB} = 88 \text{ dB}$$