

# Computer Organization and Architecture lab (KAS 352 C)

ABHISHEK RAJPUT

2100290120007

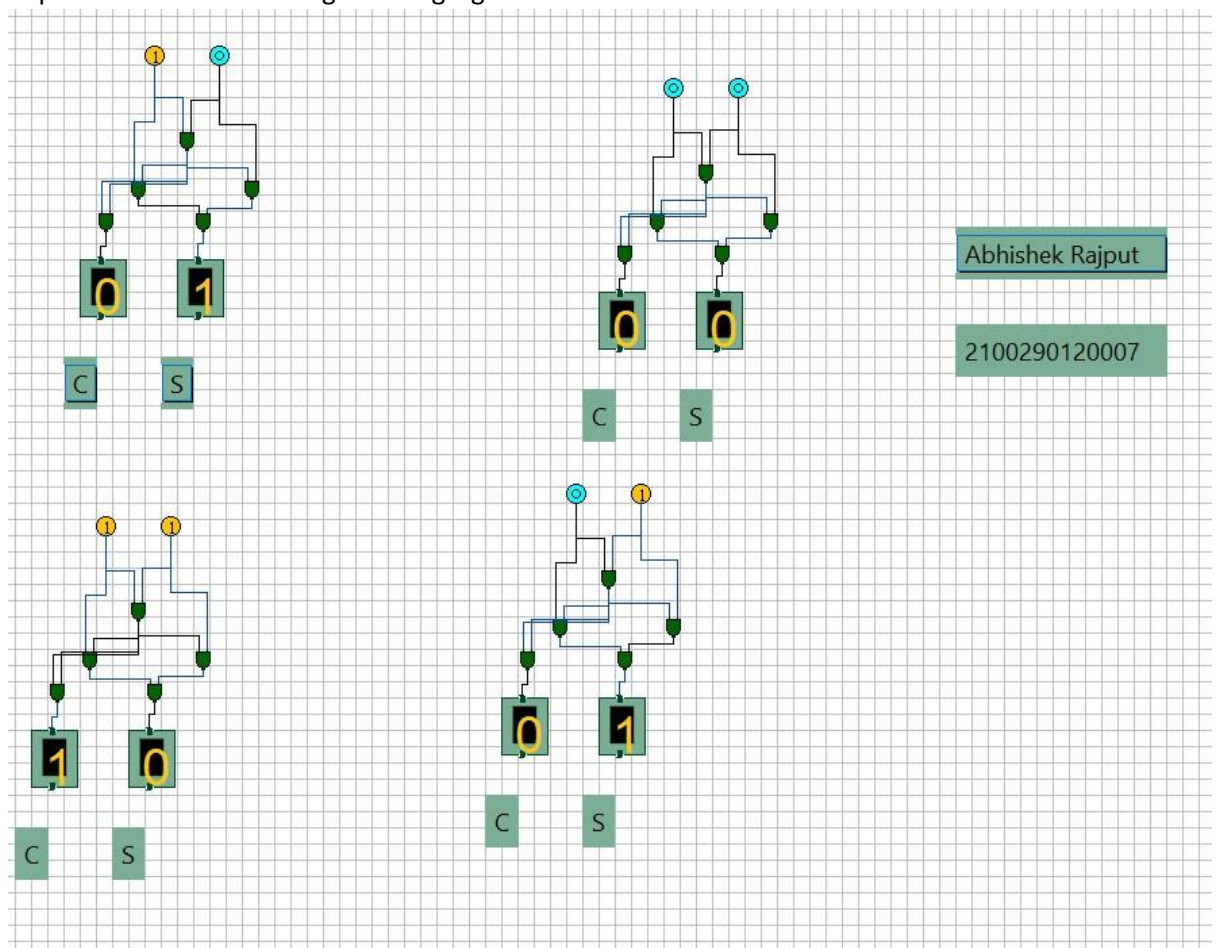
## LAB – 3

Implement half adder using basic logic and universal gates.

The addition of 2 bits is called Half adder the input variables are augend and addend bits and output variables are sum & carry bits. A and B are the two input bits.

- a) To design and implement half adder using basic logic gates

The addition of 2 bits is called Half adder the input variables are augend and addend bits and output variables are sum & carry bits. A and B are the two input bits. a) To design and implement half adder using basic logic gates



Abhishek Rajput

2100290120007

## LAB – 4

Implement full adder using basic logic, universal gates and using 2 half adders.

a) to design full adder using basic logic

