

Computer Organization and Architecture lab (KAS 352 C)

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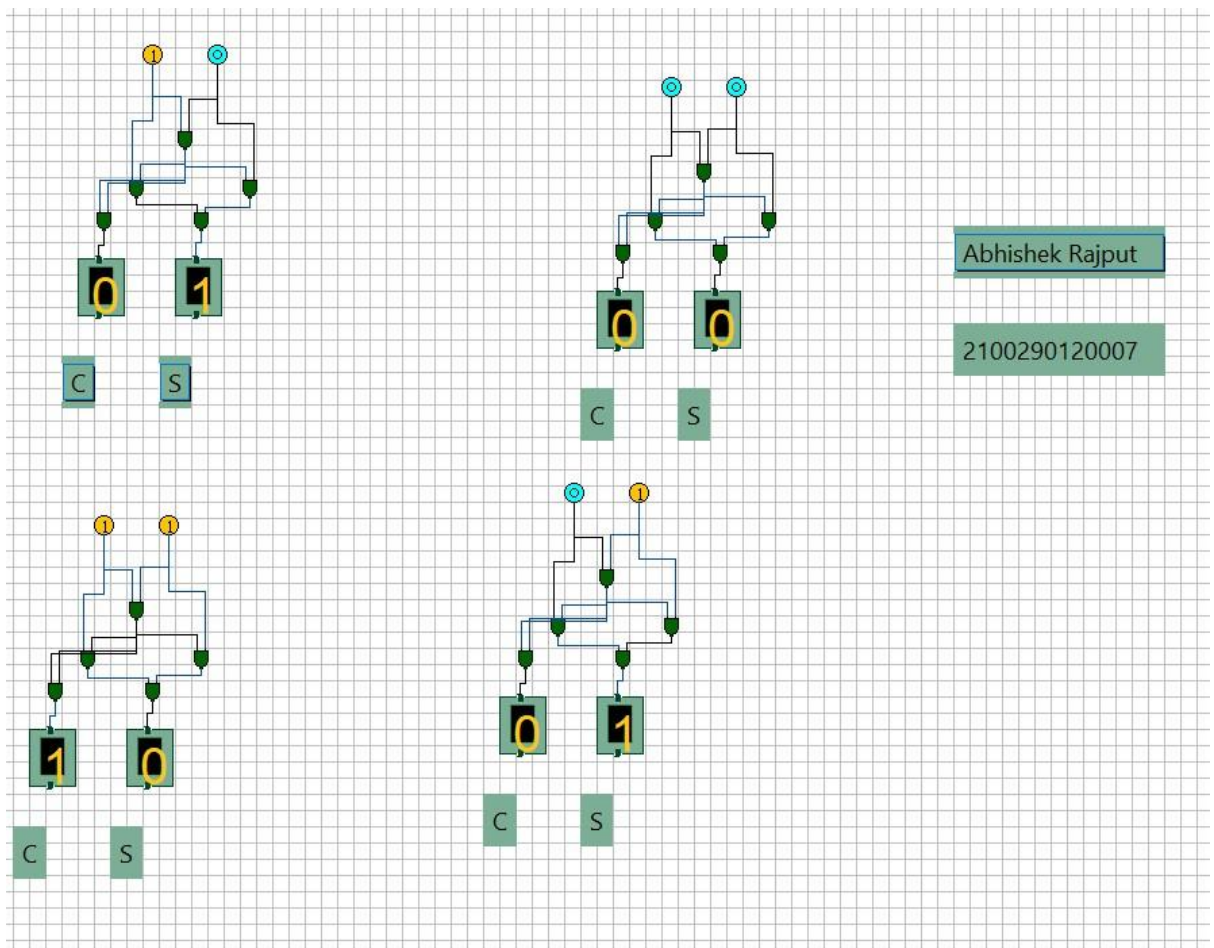
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LAB – 3

Implement half adder using basic logic and universal gates.

The addition of 2 bits is called Half adder the input variables are augend and addend bits and output variables are sum & carry bits. A and B are the two input bits.

- a) To design and implement half adder using basic logic gates



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