



## Continuous Assessment Test-1

Programme Name & Branch: B. Tech ECE

Winter Semester 2019-2020

Course Name & Code: Analog Communication Systems & ECE 3001

Class Numbers: VL2019205005183/5181/5174/5175/5178/6856

Slot: F1

Exam Duration: 90 min

Max Marks: 50

## General instructions: Answer all questions

What do you mean by simplex, half-duplex and full-duplex communication	1 1
systems? Give example for each type.	
by List the portions of the electromagnetic spectrum used for VHF and UHF	[3
services. Determine the minimum size of an antenna that can operate in the Orir	
bund.	14
c) "The motivation to proceed from standard AM to DSB-SC is to reduce the	
amount of power wasted in transmitting the carrier component." Justify the above	
statement by proving that the carrier component uses up a significant amount of	
the total power needed to transmit an AM signal by considering the modulation	
index m=0.4. Give the ratio of the sideband power to the total power.	
a) Give a basic block diagram and explain the main logic of the square law	[3]
modulator for the generation of an AM signal without any mathematical	
derivations.	
b) Rohit used a message signal 5cos (100nt) and the carrier signal	[7]
10cos (250πt) as the input to the square-law device mentioned above. Determine	50
the signal vout at the output of the square-law device and draw its frequency	
spectrum. Determine whether Rohit can get a pure AM signal at the output after	
suitable filtering. Also, predict whether the same carrier and message signal could	
be used in a switching modulator to successfully generate an AM signal.	
An amplitude modulated signal is shown in Fig.1 below. Determine the following	[10]
a) the modulation index and the expression of the expression of the modulated signal.	

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