

Invigilator's Signature :



- iv) In 8085 CPU, the JUMP instructions address affects the
 - a) Accumulator b) Stack Pointer
 - c) H-L Pair d) Program Counter.
- v) To sense the pending Interrupts instruction is used.
 - a) SIM b) RIM
 - c) SOD d) SID.
- vi) BSR & I/O Modes are part of
 - a) 8279 b) 8255
 - c) 8251 d) 8253.
- vii) 8085 is call 8 bit microprocessor due to 8 bit
 - a) Data bus b) ALU
 - c) Flags d) Accumulator.
- viii) The BIU of 8086
 - a) decodes the instructions
 - b) executes the instructions
 - c) generates the timing signal
 - d) fetches the instructions cycle.
- ix) 8237 is a programmable
 - a) DMA controller b) Interval timer
 - c) Interrupt controller d) Keyboard display.
- x) The segment & offset address of the instruction to be executed by 8086 are pointed by
 - a) CS, SI b) DS, IP
 - c) CS, SP d) CS, IP.
- xi) DAD instructions
 - a) add register pair to HL register pair
 - b) decimal adjust with D register
 - c) add register pair to DC register pair
 - d) direct add accumulator with HL register pair.

a) 1

d) none of these.

c) 3

Answer any *three* of the following $3 \times 5 = 15$

- Answer any *three* of the following. $3 \times 15 = 45$

- [Turn over



- c) Write a programme to count from 0 to 20H with a delay of 100 ms between each count. After the count 20H the counter should reset itself and repeat the sequence. Use register pair DE as delay register. $5 + 5 + 5$
10. a) Define Interrupts. Explain in detail the different types of interrupts in 8085.
- b) Explain following instructions :
- i) DAD ii) DI iii) STAX B iv) RAR
- c) Explain the control word format for I/O mode of 8255. $(2 + 5) + 4 + 4$
11. Write short notes on any *three* of the following : 3×5
- a) DMA controller
- b) SIM and RIM Instructions
- c) Interrupt Service Subroutine
- d) SOD lines SID
- e) Segment registers of 8086 μ p.

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