Reg. No.:

Name :



Continuous Assessment Test II - March 2022

Programme	: B.Tech(CSE)	Semester	FS 2021-22	
Course	: Microprocessor and Interfacing	Code	: CSE2006	
Faculty	Dr. S. Muthulakshmi & Prof.Ralph Thangaraj	Slot	: A2	
		Class Nbr	CH2021225000508 & CH2021225000509	
Time	: 90 Minutes	Max. Marks	: 50	

Answer ALL the questions

5 X 10 = 50 MARKS

						*		
O.No.	Sub. Sec.		Quest	ion Descrip	tion			Marks
X	"racecar" i	is a palindron e". If not, disp	ne or not. If it	t is a palindi sage "it's no	eck whether tome, display at a palindrome rminated succ	the messa e" using I	ige "It's a DOS	[10]
				0				
1	Which port can you use for BSR mode in 8255? Configure control word to set bit number 3 and reset bit number 5 of specific port of 8255. Also, discuss the ports used in mode 2 of 8255.						[10]	
3/	Write an ALP to use counter 1 of 8254 to generate a square wave with frequency of 1KHz. Assume the clock frequency of 8086 is 5 KHz. Assume address of control word as 0746H						[10]	
A	Discuss the architecture of 8259 in detail with proper block diagram.							[10]
3	Consider an application designed using 8255 and 8086, where port A and Port C(upper 4 bits) are used as an input ports and Port B and Port C (lower 4 bits) are used an output ports. Derive the control word for this to use 8255 in mode 1. Discuss the control word format in detail.						4 bits) are mode 1.	[10]
	E/ISCUSS	inc	control	word	format	in	detail.	
			100			Т	otal Mocks	[50]
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