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VELAGA BIRAMAKRISHN	A	 1 1

SIDDHARTHA ENGINEERING COLLEGE

(AUTONOMOUS)

II/IV B.Tech. DEGREE EXAMINATION, DECEMBER - 2024

Third Semester

ELECTRONICS AND INSTRUMENTATION ENGINEERING

23EI3304 DIGITAL CIRCUITS AND SYSTEMS

Time: 3 hours

Max. Marks: 70

Part-A is compulsory

Answer One Question from each Unit of Part - B

Answer to any single question or its part shall be written at one place only

PART-A

 $5 \times 2 = 10M$

1. a. Convert the following decimal numbers in to 8 bit binary (i) 12 (ii) 69.

(CO1 K1)

b. Convert the binary 1010110 in to gray code. (CO2 K1)

c. Write down the excitation table of D flip flop. (CO3 K1)

d. Compare PLA and PAL. (CO4 K1)

e. Draw the block diagram of a shift register. (CO3 K1)



23EI3304 PART-B

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 $4 \times 15 = 60M$

UNIT-I

2. a. Convert the following number systems in to indicated number system.

i)(7564.32)₁₀ to octal

ii) ABSCE to binary

ii)AB8CF to binary

iii)101110011 to decimal.

(CO1 K2) 7M

Implement Ex-OR gate by using NOR-NOR and NAND –NAND Logic.
 (CO1 K2) 8M

(or)

- a. Simplify the Boolean function using Quine-McCluskey methodF(A,B,C,C,D)=Σm(0,1,2,5,7,8,9,10,13,15) and verify using K-Map.
 - b. Simplify the following expression using K-Map and implement using NOR gate F(W,X,Y,Z)=XZ+W¹XY¹+WXY+W¹YZ+WY¹Z (CO1 K3) 7M

UNIT-II

- 4. a. Implement priority encoder and explain its operations. (CO2 K2) 7M
 - b. Implement 1x16 De-Mux using 1x8 De-Mux and explain.

(CO2 K2) 8M

(or)

5. a. Implement 8x1 Mux using 2x1 mux ad explain.

(CO2 K2) 8M

b. Design 2 bit binary to gray code convertor.

(CO2 K2) 7M

6. a. Explain RS latch and D flip flop with necessary diagrams.

(CO3 K2) 7M

b. Convert JK flip flop in to T flip flop.

(CO3 K2) 8M

(or)

UNIT-III

7. a. Design Updown synchronous counter and explain with a neat diagram. (CO3 K2) 8M

b. Design a Johnson counter and explain its operation with neat diagram.
 (CO3 K2) 7M

UNIT-IV

8. a. Explain programmable logic array with an example. (CO4 K2) 8M

b. Explain characteristics of digital ICs.

(CO4 K2) 7M

(or)

9. a. Explain ROM as PLD.

(CO4 K2) 8M

b. Differentiate static RAM and Dynamic RAM.

(CO4 K2) 7M

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