



**SCHOOL OF COMPUTER ENGINEERING  
KALINGA INSTITUTE OF INDUSTRIAL TECHNOLOGY  
DEEMED TO BE UNIVERSITY  
BHUBANESWAR**

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**Course:** Operating System (Credits 3) (L-T-P) (3-0-0)

**Course Code:** CS20002

**Session:** Spring, 2025

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**Course Outcomes:**

1. Able to understand the difference between different types of modern operating systems, virtual machines and their structure of implementation and applications.
2. Able to understand the difference between process & thread, issues in the scheduling of user-level processes/threads.
3. Able to understand and analyze the use of locks, semaphores, monitors for synchronizing multiprogramming / multithreaded systems and design solutions for multithreaded programs.
4. Able to understand the concepts of deadlock in operating systems and how they can be managed/avoided.
5. Able to understand the design and management concepts along with issues and challenges of main memory, virtual memory, and file system.
6. Able to understand the types of I/O management, disk scheduling, protection and security problems faced by operating systems and how to minimize these problems.

Module name	Topic/Coverage	No. of lectures	Days
<b>Introduction</b>	<ul style="list-style-type: none"> <li>● Introduction to OS</li> <li>● Operating system structure</li> </ul>	3	Day 1
	<ul style="list-style-type: none"> <li>● Types of OS               <ul style="list-style-type: none"> <li>◦ Batch OS</li> <li>◦ Multiprogramming, multitasking OS</li> <li>◦ Multithreading, Multiprocessing System</li> <li>◦ Distributed OS, Network OS</li> <li>◦ Real-Time OS</li> </ul> </li> </ul>		Day 2
			Day 3

<b>Process</b>	<ul style="list-style-type: none"> <li>● Process description</li> <li>● Process States (5-state model, 7 State model)</li> <li>● Contents of PCB</li> </ul>	6	Day 4
	<ul style="list-style-type: none"> <li>● Process scheduling               <ul style="list-style-type: none"> <li>◦ Scheduling Queue</li> <li>◦ Process schedulers (LT , ST and MT)</li> <li>◦ Context switching</li> <li>◦ Multithreading and their functionality.</li> </ul> </li> </ul>		Day 5
	<ul style="list-style-type: none"> <li>● CPU scheduling criteria</li> <li>● CPU scheduling algorithms               <ul style="list-style-type: none"> <li>◦ FIFO</li> <li>◦ SJF, SRTN</li> </ul> </li> </ul>		Day 6
	<ul style="list-style-type: none"> <li>◦ Priority</li> <li>◦ Round Robin</li> </ul>		Day 7
	<ul style="list-style-type: none"> <li>◦ HRRN</li> <li>◦ Multi-level feedback scheduling</li> <li>◦ Multi-level feedback queue scheduling</li> </ul>		Day 8
	<ul style="list-style-type: none"> <li>● Comparative Analysis of the CPU scheduling algorithms</li> </ul>		Day 9
Activity	Activity 1	1	Day 10
<b>Concurrent Process</b>	<ul style="list-style-type: none"> <li>● Race Condition</li> <li>● Process Synchronization</li> </ul>	6	Day 11
	<ul style="list-style-type: none"> <li>● Critical section problems</li> <li>● Requirements for a solution to critical section problems</li> </ul>		Day 12
	<ul style="list-style-type: none"> <li>● 2-process software solutions (Peterson Solution)</li> <li>● Semaphore</li> <li>● Lock.</li> </ul>		Day 13
	<ul style="list-style-type: none"> <li>● Classical problems of synchronization               <ul style="list-style-type: none"> <li>◦ Producer-Consumer Problem</li> <li>◦ Reader Writer Problem</li> </ul> </li> </ul>		Day 14
	<ul style="list-style-type: none"> <li>◦ Dining Philosopher Problem</li> <li>◦ Disadvantages of Semaphore</li> </ul>		Day 15
	<ul style="list-style-type: none"> <li>● High-level synchronization tools               <ul style="list-style-type: none"> <li>◦ Critical region</li> <li>◦ Introduction to Monitor</li> </ul> </li> </ul>		Day 16
Activity	Activity 2	1	Day 17
<b>Deadlock</b>	<ul style="list-style-type: none"> <li>● Deadlock</li> <li>● Handling deadlock (Resource Allocation Graph (RAG))</li> </ul>	4	Day 18
	<ul style="list-style-type: none"> <li>● Deadlock ignorance</li> <li>● Deadlock prevention</li> </ul>		Day 19
	<ul style="list-style-type: none"> <li>● Deadlock avoidance (Banker's Algorithm)</li> <li>● Deadlock detection and recovery</li> </ul>		Day 20

			Day 21
Activity	Activity 3	1	Day 22
	<b>MID SEMESTER</b>		
<b>Memory Management</b>	<ul style="list-style-type: none"> <li>Address binding</li> <li>Logical and physical address</li> </ul>	8	Day 23
	<ul style="list-style-type: none"> <li>Memory management using partitioning               <ul style="list-style-type: none"> <li>Fixed Partitioning, Drawbacks</li> <li>Dynamic Partitioning, Drawbacks</li> </ul> </li> </ul>		Day 24
	<ul style="list-style-type: none"> <li>Virtual Memory:               <ul style="list-style-type: none"> <li>Translation look-aside buffers (TLBs)</li> <li>Hierarchical paging</li> <li>Inverted Page Tables</li> </ul> </li> </ul>		Day 25
	<ul style="list-style-type: none"> <li>Paging</li> <li>Segmentation</li> <li>Segmentation with Paging</li> </ul>		Day 26
	<ul style="list-style-type: none"> <li>Demand Paging</li> <li>Dealing with Page faults</li> </ul>		Day 27
	<ul style="list-style-type: none"> <li>Page replacement algorithms:               <ul style="list-style-type: none"> <li>First-In-First-Out(FIFO)</li> <li>Optimal Page Replacement (OPT)</li> </ul> </li> </ul>		Day 28
	<ul style="list-style-type: none"> <li>Least Recently Used (LRU)</li> <li>Most Recently Used (MRU)</li> </ul>		Day 29
	<ul style="list-style-type: none"> <li>Thrashing</li> </ul>		Day 30
Activity	Activity 4	1	Day 31
<b>File Management</b>	<ul style="list-style-type: none"> <li>File concept</li> <li>Access Methods</li> <li>Directory structure.</li> </ul>	3	Day 32
	<ul style="list-style-type: none"> <li>File system mounting</li> <li>File System structure</li> </ul>		Day 33
	<ul style="list-style-type: none"> <li>File system Implementation</li> <li>Allocation methods</li> <li>Free space management</li> </ul>		Day 34
<b>I/O Management</b>	<ul style="list-style-type: none"> <li>I/O Devices</li> <li>Device controller</li> <li>Device Drivers</li> <li>Application I/O Interface</li> </ul>	2	Day 35
			Day 36
Activity	Activity 5	1	Day 37

<b>Disk Management</b>	<ul style="list-style-type: none"> <li>• Disk Structure</li> <li>• Disk Scheduling <ul style="list-style-type: none"> <li>◦ FCFS</li> <li>◦ SCAN</li> </ul> </li> </ul>	2	Day 38
	<ul style="list-style-type: none"> <li>• Disk Scheduling <ul style="list-style-type: none"> <li>◦ C-SCAN</li> <li>◦ LOOK</li> <li>◦ C-LOOK</li> </ul> </li> </ul>		Day 39
<b>OS protection and security</b>	<ul style="list-style-type: none"> <li>• Domain of Protection</li> <li>• Access matrix</li> </ul>	1	Day 40
	<ul style="list-style-type: none"> <li>• Implementation of Access matrix</li> <li>• System security</li> </ul>		

\* The Total number of classes is approx 40, which includes lectures and activities.

**Text Book:** Abraham Silberschatz, Peter B. Galvin, and Greg Gagne, “Operating System Concepts”, 10<sup>th</sup> edition, Wiley Publisher, May-2018.

**Reference book:**

1. William Stallings, “Operating Systems: Internals and Design Principles”, 9th edition, Published by Pearson, July 2021.
2. Deitel, Deitel, and Choffnes, “Operating Systems-Part I”, 3rd Edition, Published by Pearson, 2004.

Teaching Pedagogy: Whiteboard/Marker, PowerPoint Presentations, Web Resources

**Web References:**

1. <http://faculty.cs.tamu.edu/bettati/>
2. <http://u.cs.biu.ac.il/ariel/download/os288/ppts/>
3. <http://www.cs.rutgers.edu/pxk/416/index.html>
4. [http://www.tutorialspoint.com/operating\\_system/os\\_multi\\_threading.htm](http://www.tutorialspoint.com/operating_system/os_multi_threading.htm)
5. <http://cs.nyu.edu/courses/spring02/V22.0202-002/class-notes.html>

### Evaluation Scheme

Assessment Methodology: Mid Term: 20; End Term: 50

Distribution of Internal Marks:- Total 50 Marks

Mid Semester Examination = 20 Marks

Activity-based assessment=30 Marks

- i. Assignments/quizzes/Activities (20 Marks)
- ii. Class Participation (10 Marks)

Activity-based assessment includes Problem Solving, Critical Thinking, Creation along with Quiz which should cover all the COs.

Course Coordinator  
Spring, 2025