

MIXER

Aim:

To generate the difference signal using Mixer circuit

Components Required

1. BJT - 2N904 - 1 Nos
2. Resistor - $10K\Omega$, $22K\Omega$, $4.7K\Omega$ - (4) each.
3. Capacitor - $0.1\mu F$, $0.001\mu F$, $1\mu F$ - 1, 2, 1
4. Bread Board - - - 1 Nos
5. Function generator - $20MHz$ - 2 Nos
6. VRPS - $(0-30)V$ - 1
7. DSO - $200MHz$ - 1

Theory:

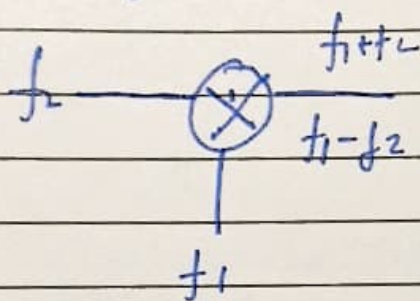
Mixers are used for frequency conversion and are critical components in Modern Radio frequency (RF) system. A Mixer converts RF Power at one frequency into Power at another frequency to make Signal Processing easier and also inexpensive. A fundamental reason for frequency conversion is to allow amplification of the received signal at a frequency other than the RF, or the audio frequency. A receiver may require as much as 140 decibels (dB) of gain. It might not be possible to put more than 40 decibels of gain into RF Section without risking instability and potential Oscillation. Likewise the gain of the audio Section might be limited to 60dB because of Parasitic feedback Paths, and

microphones. The additional gain needed for a sensitive receiver is normally achieved in an intermediate frequency (IF) section of the receiver.

The Ideal Mixer, is a device which multiplies two input signals. If the inputs are sinusoidal, the ideal mixer output is sum and difference frequencies given by

$$V_o = [A_1 \cos(\omega_1 t)] [A_2 \cos(\omega_2 t)] = \frac{A_1 A_2}{2} [\cos(\omega_1 - \omega_2 t) + \cos(\omega_1 + \omega_2 t)]$$

Typically, either the sum, or the difference, frequency is removed with a filter.



Circuit Symbol for a Mixer.

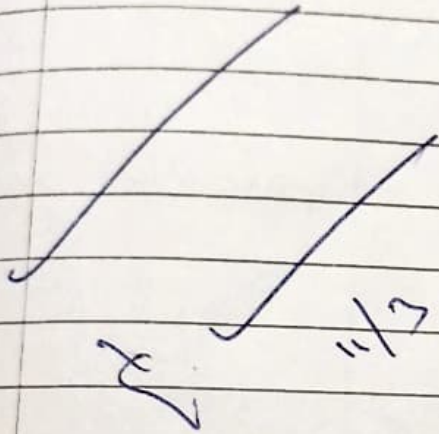
Procedure:

1. Connections are made as per the circuit diagram.
2. With the oscilloscope, adjust $V_x = 5 \text{ V}_{p-p}$. Set the frequency to 100 KHz.
3. Next adjust V_y to 5 V_{p-p} and set the frequency to 116 KHz.

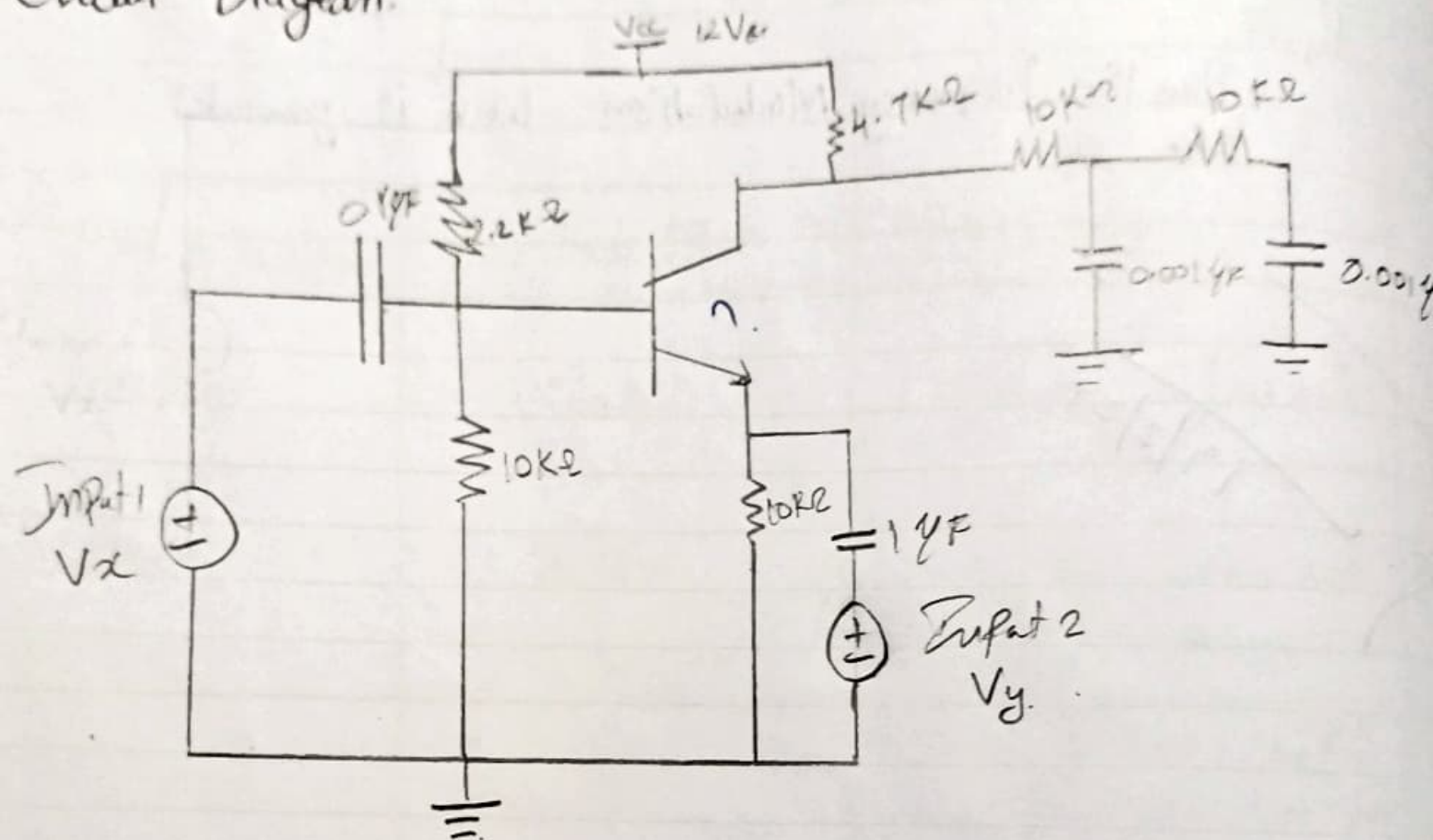
4. Measure the signal frequency at the output of the Mixer.

Result:

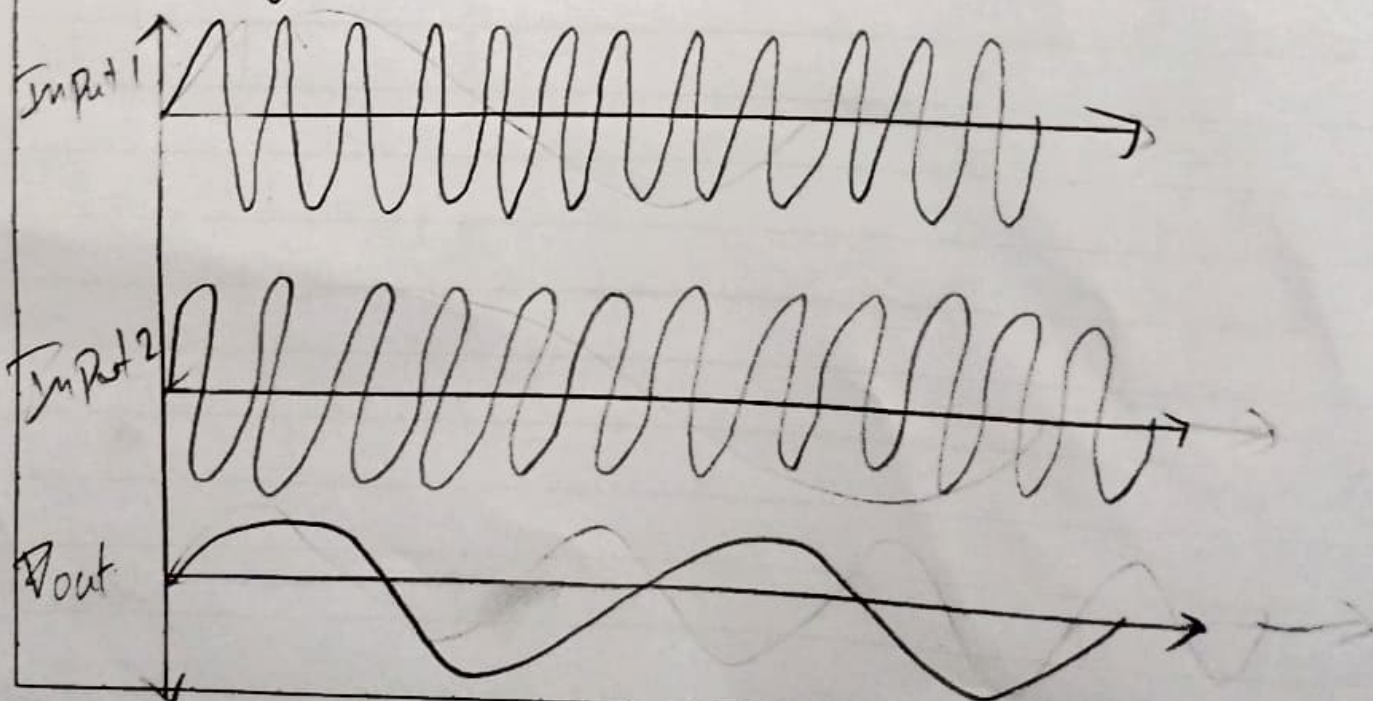
We have got the difference between the two input signal via the Mixer circuit.



Circuit Diagram.



Model graph.



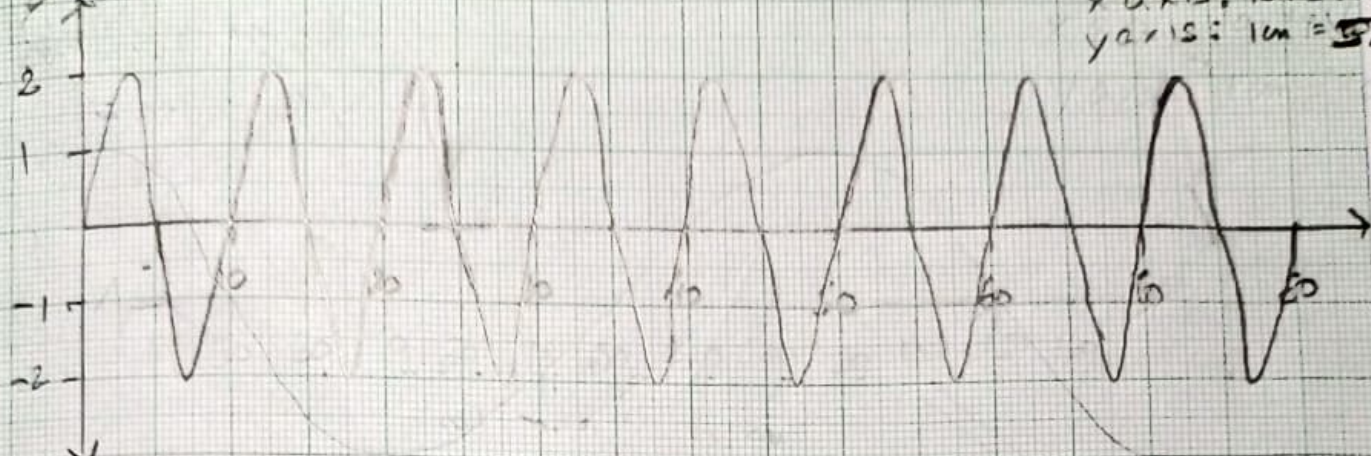
Observation Table:-

Input 1	Input 2	Difference Frequency
100	116 KHz	16.419 KHz
100	105 KHz	5.0023 KHz
100	110 KHz	10.01 KHz

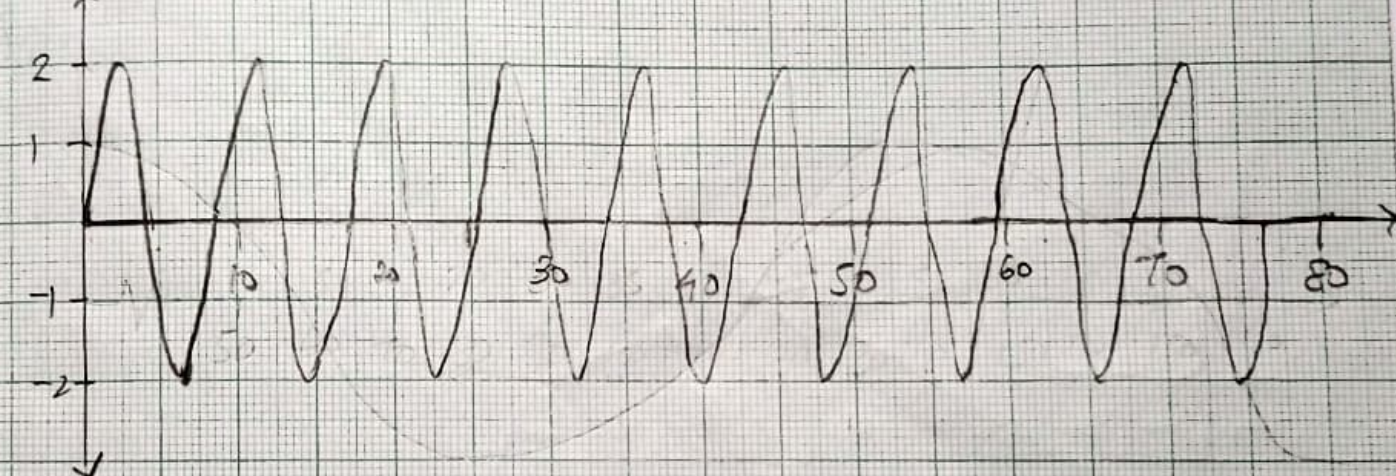
EXP 3

Mixer

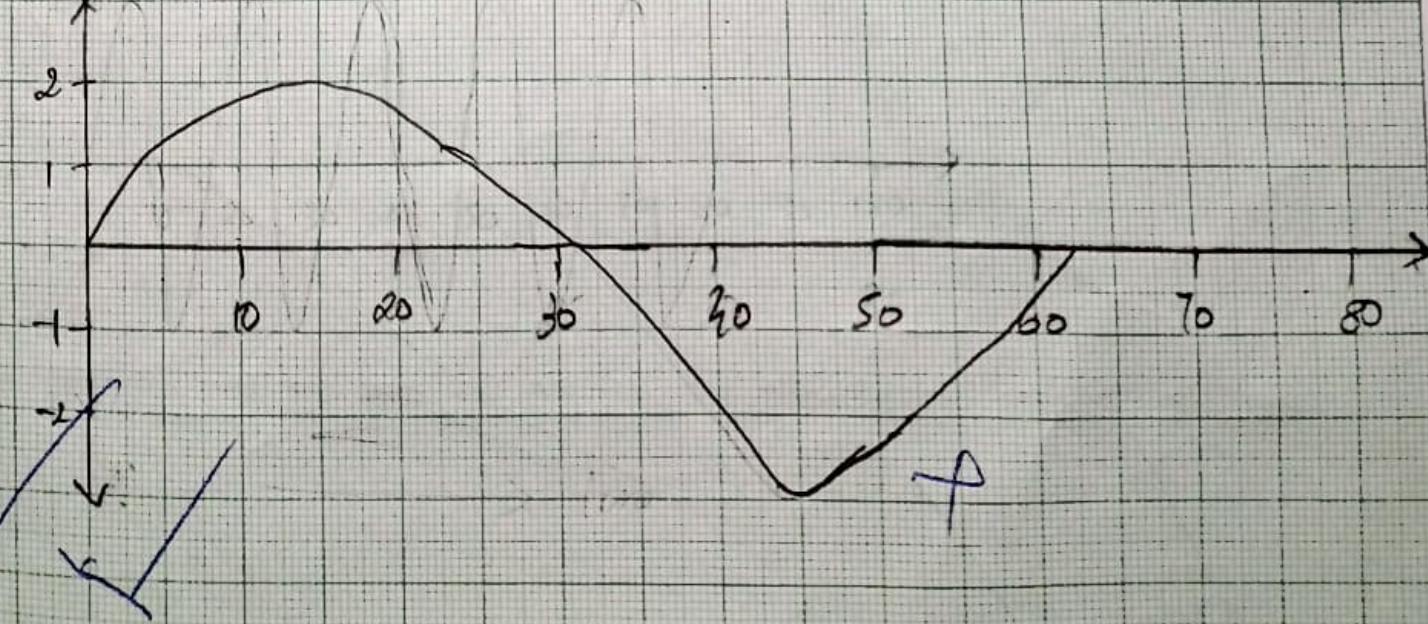
Input 1



Input 2



Output



Pulse Amplitude Modulation.

Aim:-

To Construct a Pulse amplitude Modulation circuit and observe its Waveform.

Components Required:-

Transistor - BC147 - 1 Nos

Resistor - 1K Ω - 1 Nos

2.2K Ω - 1 Nos

22K Ω - 1 Nos

10K Ω - 1 Nos

Theory:

In Pulse amplitude modulation, the amplitude of individual pulses in the pulse train is varied from its default value in accordance with the ~~instantaneous~~ instantaneous amplitude of the modulating signal at sampling intervals. The width and position of the pulses is kept constant.

The PAM Transmitter design is Very Simple Since the Very act of Sampling the modulating signal at regular intervals produces Pulse amplitude modulation. Main advantage of PAM are Simple Transmitter and receiver design. PAM is Used to carry information as well as to generate other Pulse modulations.

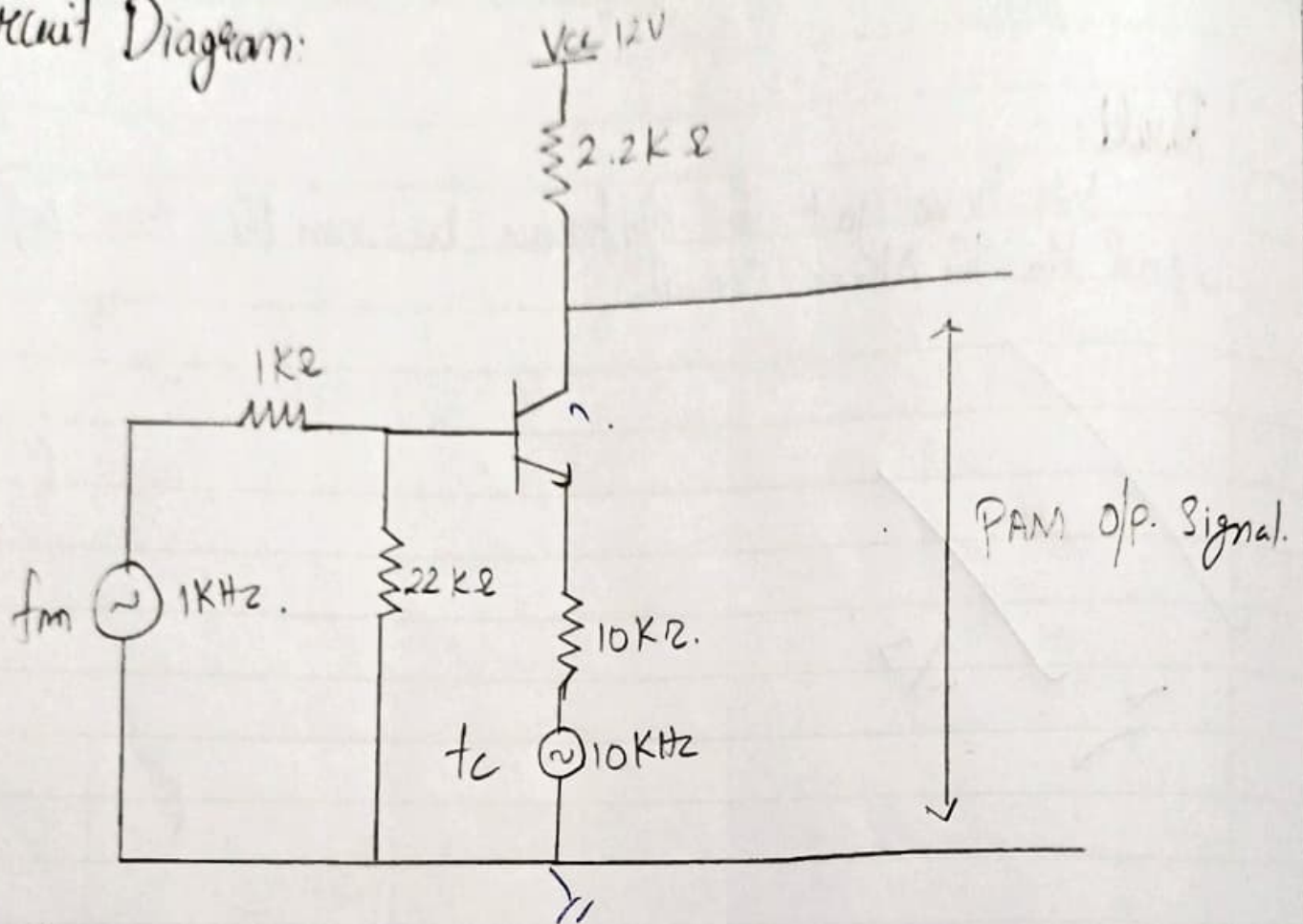
Procedure:-

1. Connection are made as per the circuit diagram.
2. Apply a modulating Signal with 1 KHz frequency and carrier Pulse Signal with 100 KHz frequency from the function generator.
3. Apply 12V dc Supply to the collector.
4. Take the modulated Supply Signal output at the collector and observe the same on CRO.
5. Note the reading of Amplitude and frequency of PAM Output Signal.

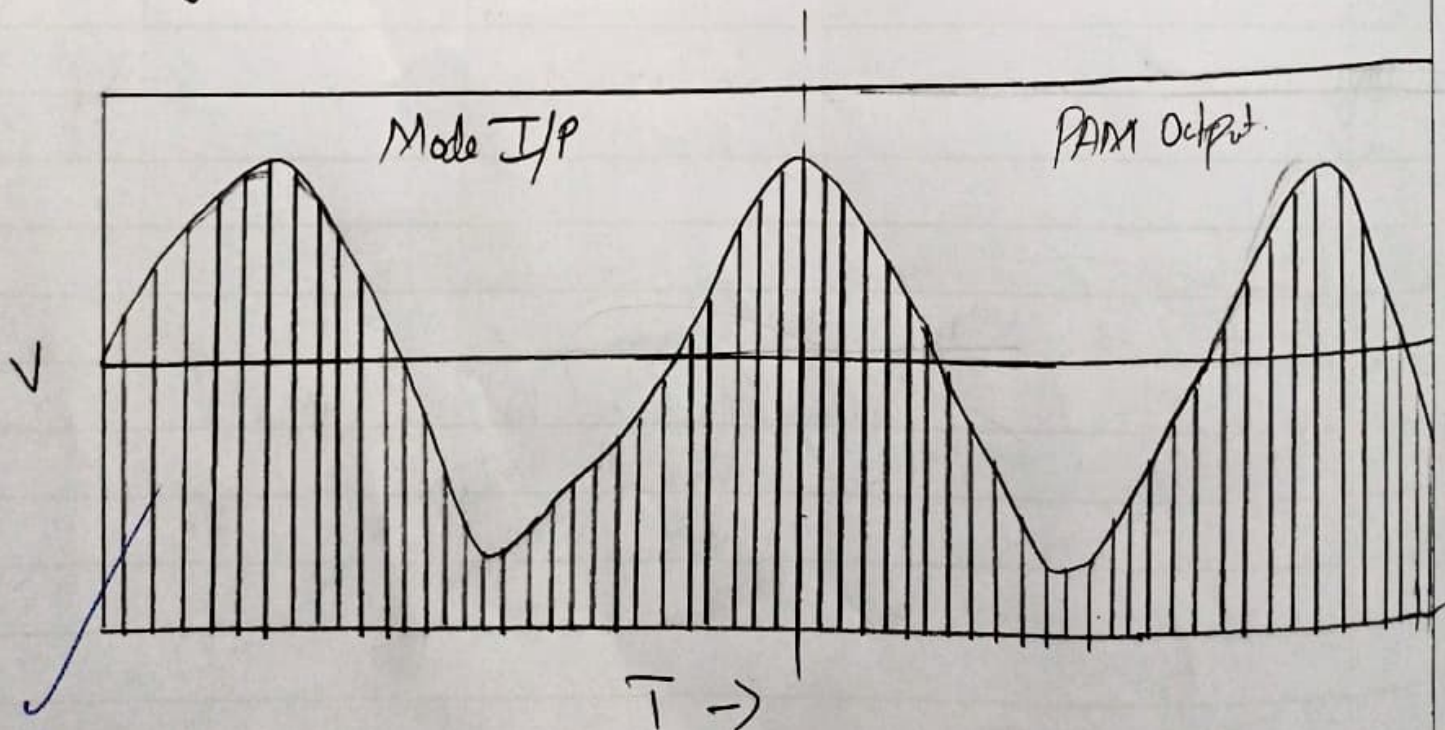
Result:

The PAM i.e. Pulse Amplitude Modulation is constructed and the waveform is been observed.

Circuit Diagram:



Model graph:



Observation Table:

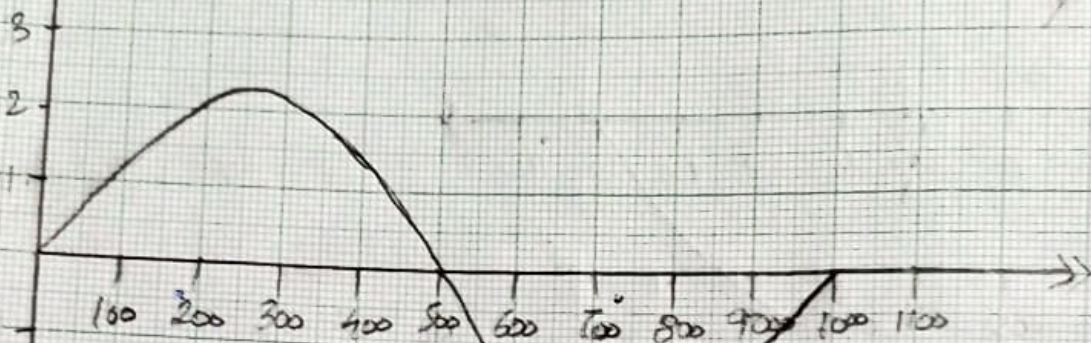
Name of the Signal	Amplitude (V)	frequency (Hz)
Message Signal	2.2V	1.0081 KHz
Carrier Signal	3.94V	$T_{ON} : 12 \mu s$ $T_{OFF} : 90 \mu s$ $Freq : 9.803 KHz$
PAM Signal.	$PAM_1 = 362.5 mV$ $PAM_2 = 475 mV$ $PAM_3 = 725 mV$ $PAM_4 = 985.5 mV$ $PAM_5 = 1.175 V$ $PAM_6 = 1.181 V$ $PAM_7 = 1.05 V$ $PAM_8 = 806.25 mV$ $PAM_9 = 556.25 mV$ $PAM_{10} = 381.5 mV$	$T_{ON} : 12 \mu s$ $T_{OFF} : 90 \mu s$ $Frequency : 9.803 kHz$

Exp. 4 PAM.

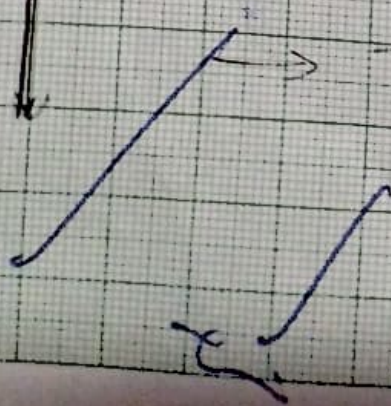
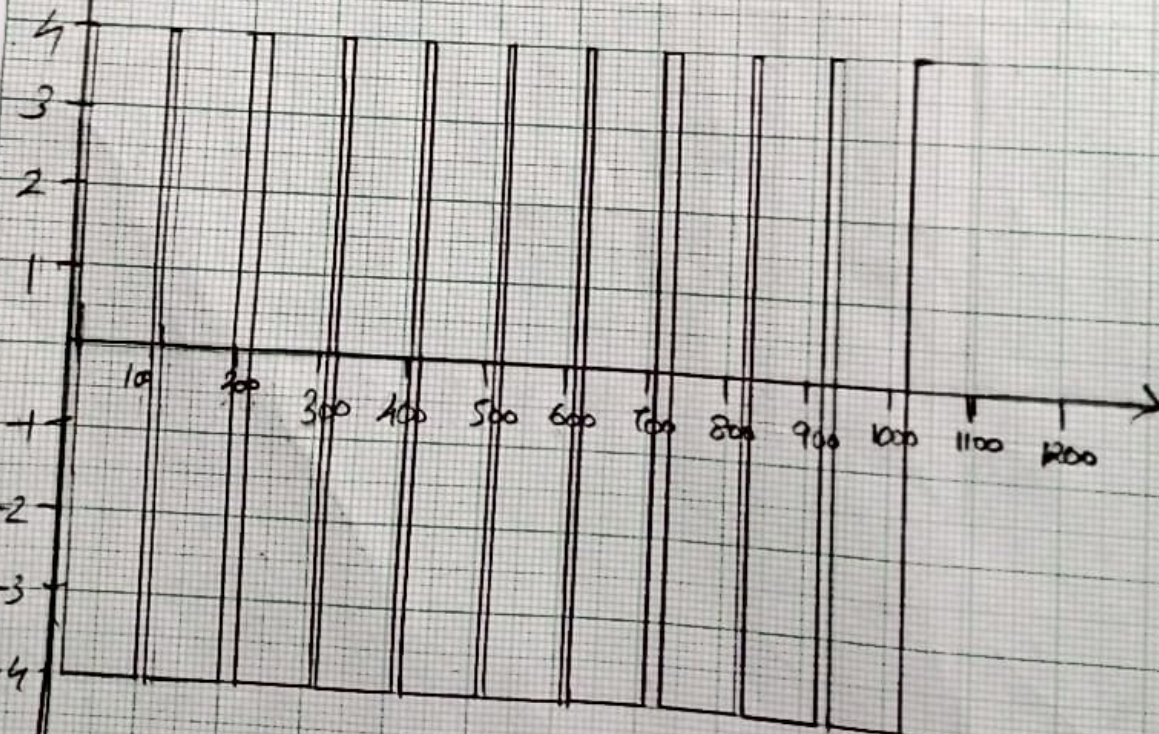
Scale

X axis 1 cm = 100 μ s
Y axis 1 cm = 1V

Message



Carrier

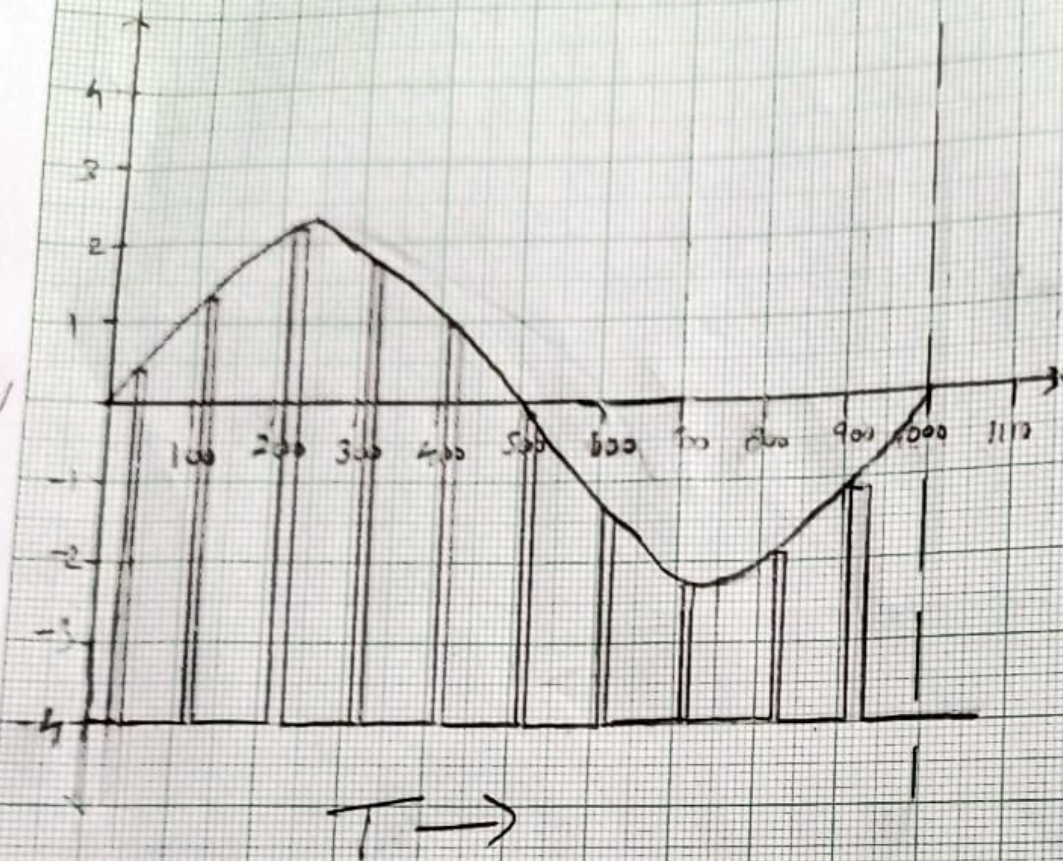


PAM output

Scale:

x axis: 1cm = 100 μ s

y axis: 1cm = 1V



11/3

Pulse Width Modulation

Aim:

To construct and generate Pulse Width Modulation and Plot the Output Waveform.

Components Required:

1. IC 555 - 1 No
2. Resistor - 1K Ω - 2 Nos
3. CRO - 30MHz - 1 No
4. Function generator - (0-10MHz) - 1 No
5. Power Supply - (0-30V) - 1 No
6. Bread Board - 1 No
7. Capacitor ~~100~~ - 0.01 μ F, 10 μ F - 1 No.

Theory:

In Pulse width Modulation, the width of individual Pulses in the Pulse Train is varied from its default Value in accordance with the instantaneous amplitude of the modulating signal at sampling intervals.

The amplitude and Position of the Pulse is kept constant. Pulse width modulation is also called as Pulse length modulation (PLM) or Pulse duration Modulation (PDM).

Pulse width modulation is mostly used in control applications.

Such as motor control and light control system.

Pin diagram:

Ground	1	8	VCC
Trigger	2	7	Discharge
Output	3	6	Threshold
Reset	4	5	Control Voltage

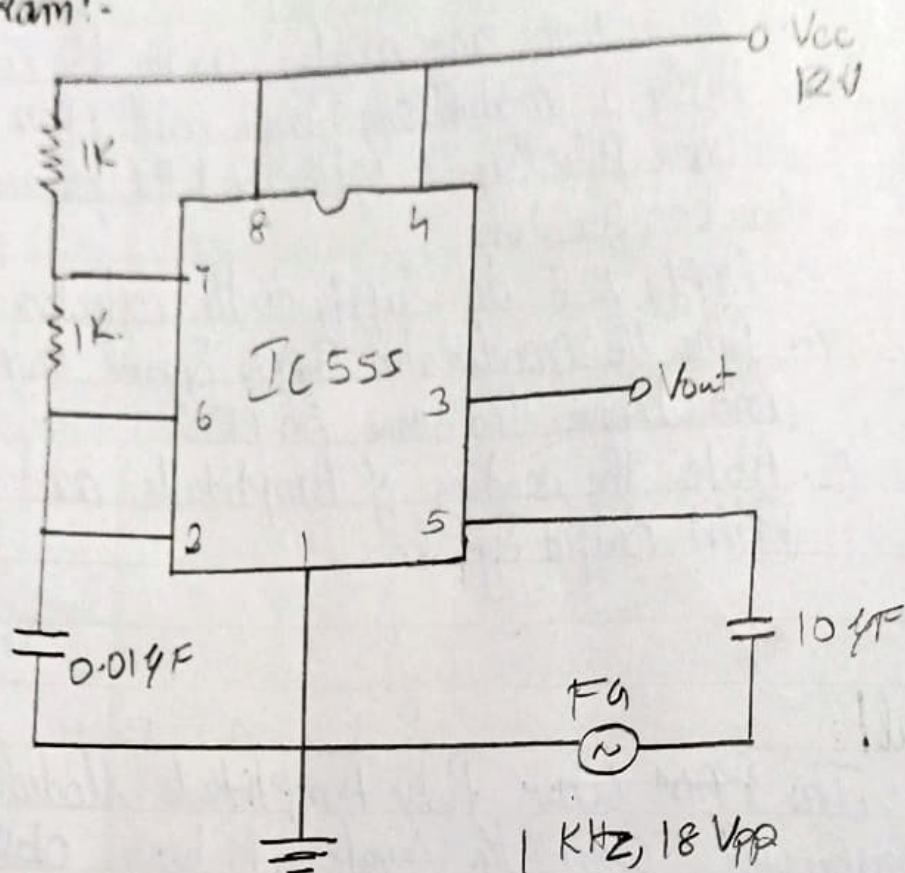
Procedure:

1. Connect the circuit as per shown in the diagram
2. Set the amplitude of the input signal 5V
3. Observe the PWM output at pin no. 3
4. Plot the graph for both input and output signal.

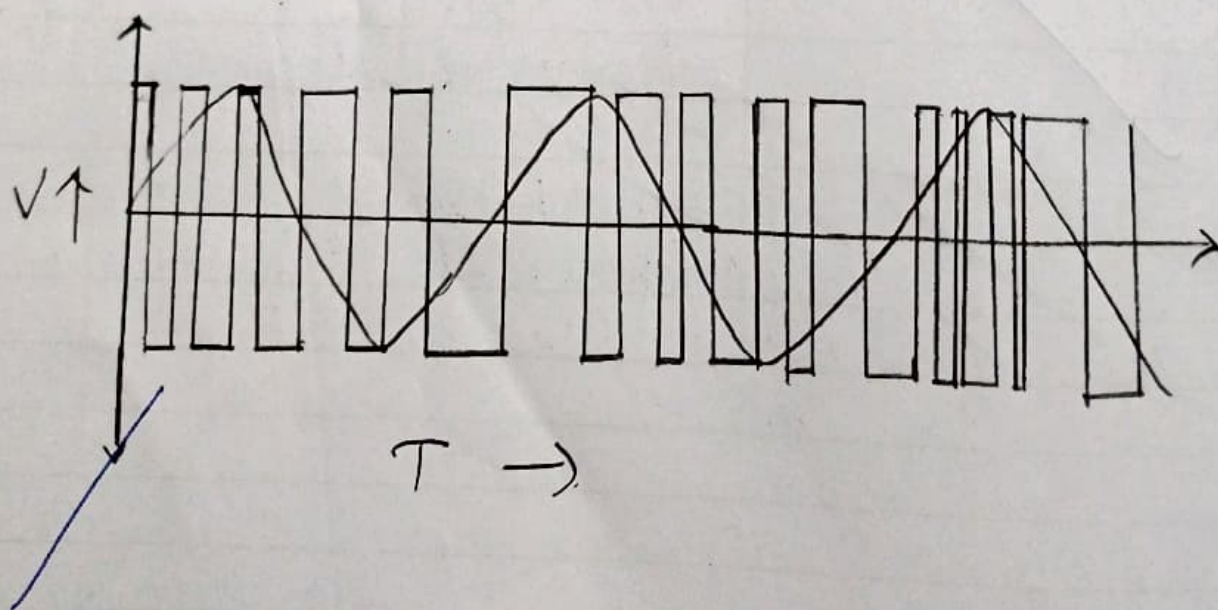
Result:

We have generated the Pulse Width Modulation and the graph has been plotted.

Circuit Diagram:-



Model graph:

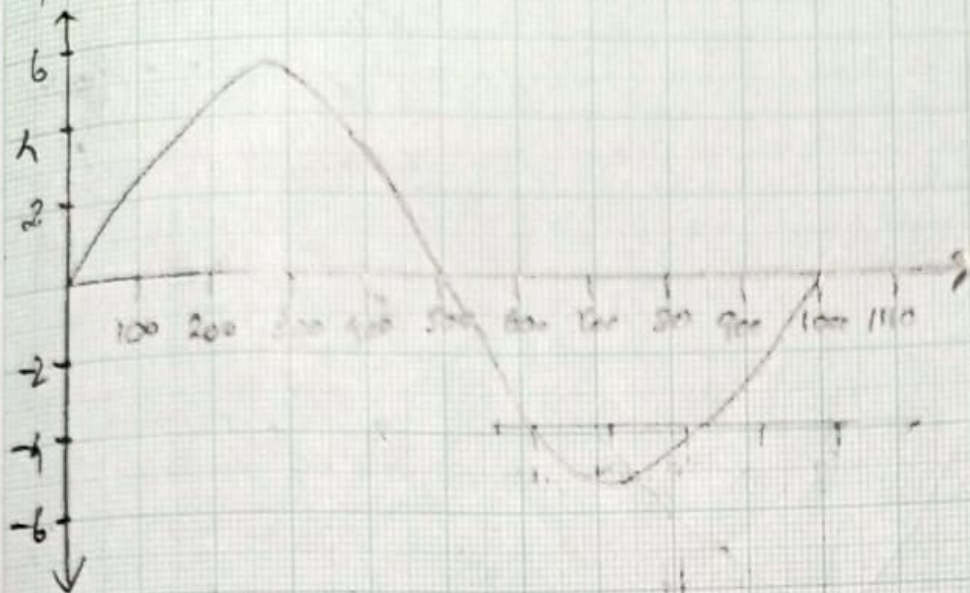


Observation Table:

Name of the Signal.	Amplitude (V)	Time period & Frequency (Hz)
Message	5.5V	T: 1.006 ms F = 999.36 Hz
Carrier Signal.	5.4V	T _{ON} : 92 μ s T _{OFF} : 28 μ s F = 7.1 KHz
PWM Output	5.2V	T _{ON} = 25 μ s T _{OFF} = 40 μ s T _{ON} = 30 μ s T _{OFF} = 34 μ s T _{ON} = 400 μ s T _{OFF} = 52 μ s T _{ON} = 78 μ s T _{OFF} = 70 μ s T _{ON} = 80 μ s T _{OFF} = 24 μ s T _{ON} : 566 μ s T _{OFF} : 458 μ s T: 1024 μ s F: 976.5625.

Exps : PAM PWM.

Message.

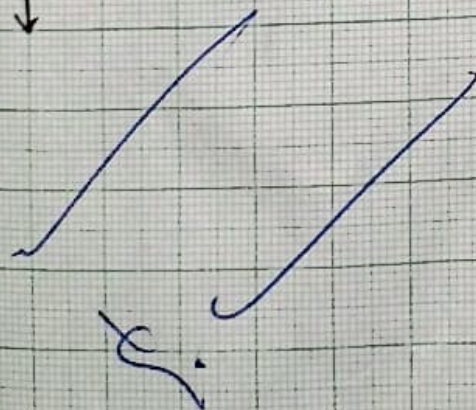
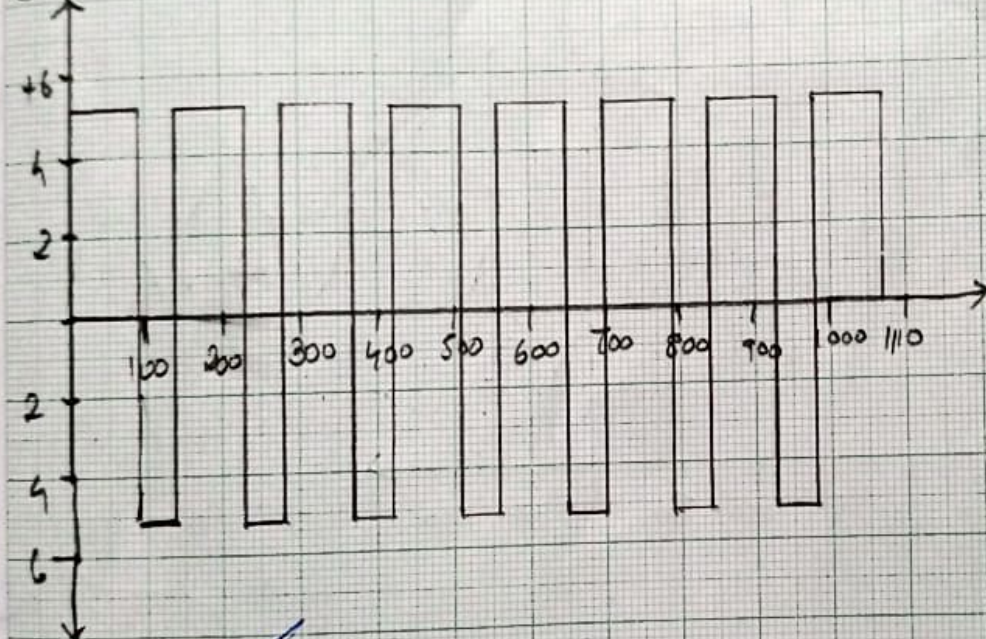


Scale.

$$X: 1 \text{ cm} = 100 \mu\text{s}$$

$$Y: 1 \text{ cm} = 2 \text{ V.}$$

Carrier.

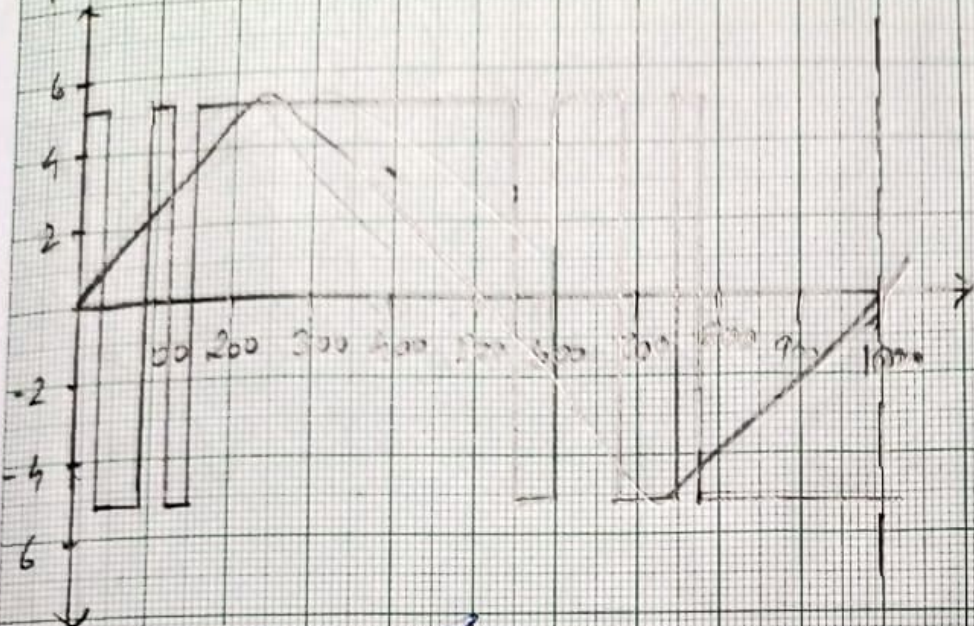


PWM output

Scale

X axis: 1cm = 100 μ s

Y axis: 1cm = 2V



1/2