Fixed Interconnection Networks

- No shared memory
- Each processor is connected to some of the other processors
- Messages can be sent along the connections
- A message is a word
- Global clock

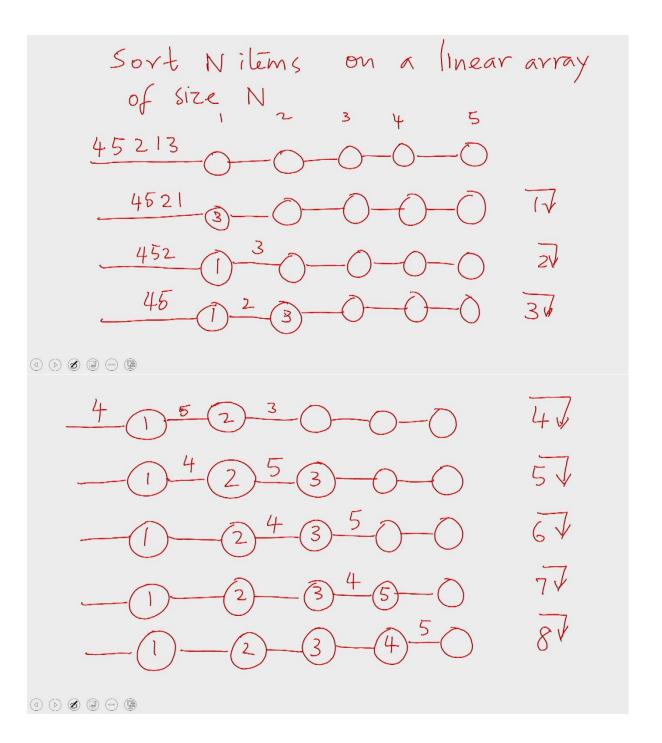
- In each step, each processor
 - · Receives input into local store
 - · Computes and updates local store
 - Generates output

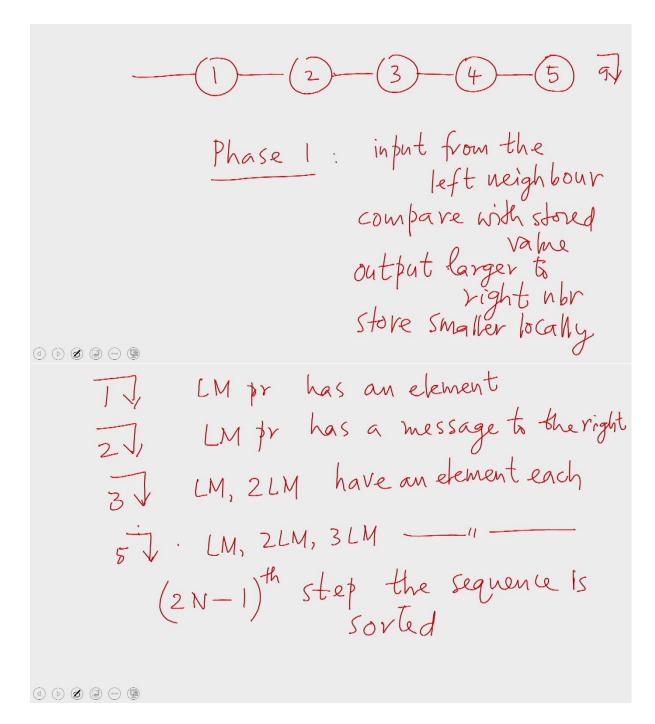
Linear Array

N processor

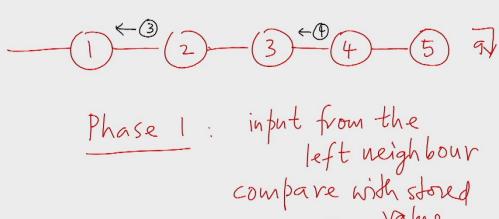
N-1 connections

degree < 2





Phase 2 Getting the values from the left most processor No more input start sending to the left



output larger to right nbr store smaller locally

By the Nth step all the input has con has come

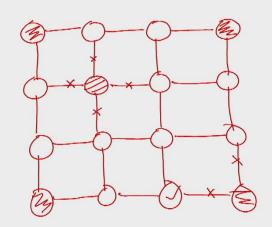
2nd output: N+3

Nth output: N+ (2N-1)=3N-1

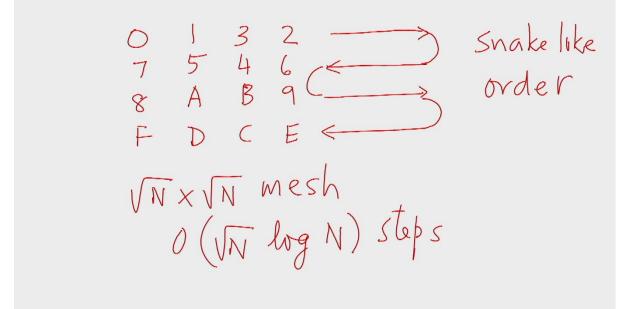
1st 2 5, 8 1, 6 4, 3 7, 2nd 2 5 1, 8 4, 6 3, 7

3rd 2 1, 5 4, 8 3, 6 7, 4th 1 2 4, 5 3, 8 6, 7 5 th 1 2, 4 3, 5 6, 8 7, 5 1 2 3 4 5 6 7 8

The odd-even transposition sort N steps



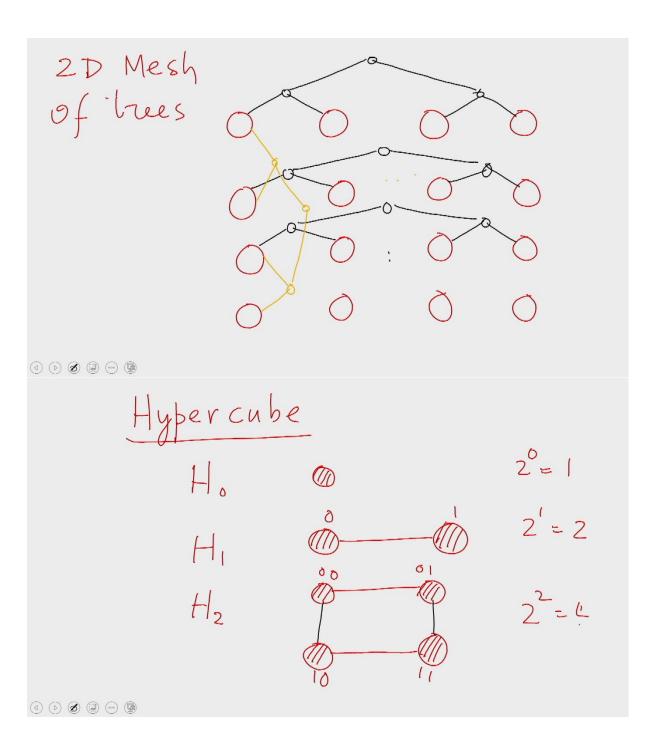
interior: 4 Corner 12 the rest: 3

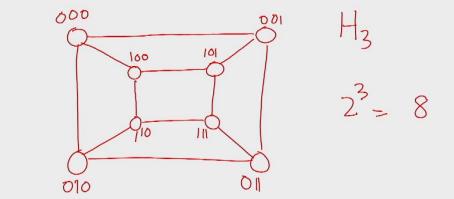


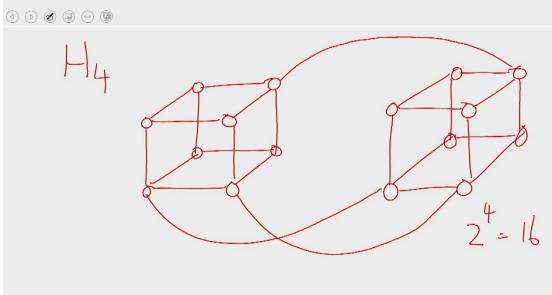
3D-mesh

degree: 6

(4) (b) (8) (B) (0) (B)







(a) (b) **(8)** (c) (c) **(b)**

N- node hyper cube dimension of log_N degree: log_N

Butterfly Cube connected Cycles De Bruijn Graphs Shuffle exchange graphs