

# Cosmic

## A Software Simulated 8-Bit Computer Architecture

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**Abstract**—The Cosmic processor, graphical user interface (GUI), and assembler aim to educate students with little or no knowledge of computer architecture or assembly language. Derived from 8-bit processors of the 1980's, Cosmic was built with simplicity and modularity in mind. The simplicity is in the fundamentally basic and well-documented instruction set, while the modularity stems from the design of the system. The system was designed in a way that anyone could write their own simulation environment, graphics stack, peripherals, drivers, and more to use the Cosmic processor at its core. Written in C++ and Python with minimal dependencies, Cosmic is lightweight and portable, which makes it perfect for running on almost any system, including running headless on systems with low graphical power. While no experimentation on its educational properties has currently been conducted, Cosmic is ready with informative labs for students to engage with. Modern, complex assembly languages will no longer be a starting point for novice programmers.

**Index Terms**—retro-computing, simulation, emulation, micro-processor, 8-bit architecture.

### I. INTRODUCTION

intro and problem definition

### II. BACKGROUND

The largest source of background information is in the machines that Cosmic is designed to resemble. The two primary reference devices are the Apple II and the TRS-80 containing the MOS 6502 and the Zilog Z80, respectively.

There are a few other projects out there that are similar to what we did. Emulators of similar devices were helpful for design inspiration. Unlike emulators, we do not have the same design constraints, which added a sense of freedom when creating the system. Outside of emulators, there are a few projects where developers have created self-defined simulated platforms. One such project is an 8-bit Assembler and Simulator written by Marco "Schweigi" Schweighauser [1]. Another project that influenced the creation of Cosmic is David Murray's Commander X16 project [2]. Unlike the Assembler-Simulator, this is a "home-brew" physical computer made using off-the-shelf parts. This project uses the 6502 and other components to create a retro-feeling machine with more modern parts.

Luckily, devices of the time have been very well documented in the past 40 years. This documentation made reverse-engineering the systems and processors simple and allowed us to figure out how engineers of yesterday solved problems that we encountered while designing our system.

### III. DESIGN CONSTRAINTS

design constraints

### IV. TIMELINE

#### A. Fall Semester

Fall semester was the beginning of Cosmic, so much of the semester involved planning and design. Everything from picking the instructions we wanted to implement, to designing the GUI, to planning the underlying system (memory, graphics, etc.) was done during this phase.

By the end of the fall semester, we had the Cosmic processor mostly developed and tested, which was the most important task during this time. Alongside that, the GUI was developed to a significant working extent and we just scratched the surface in regards to writing the assembler.

#### B. Spring Semester

Spring semester kicked off with a heavy focus on finishing development of the assembler and then transitioned to implementing a video output screen and a plethora of GUI improvements. As the semester progressed, focus on developing Cosmic shifted to begin preparing for SCAD. This included tasks such as recording a video presentation, designing a poster, and writing three labs to showcase our project and its educational value. Luckily, among the chaos, we still had time to implement interfacing hardware with Cosmic such as keyboard inputs and GPIO pins on a Raspberry Pi. In the end, all of our deliverables were met.

### V. BUDGET

At first, we thought since our entire project is in software, that nothing would need to be bought. As development continued, we decided to purchase two 4GB Raspberry Pis to experiment interfacing Cosmic with real-world hardware. For just under \$90 each, these Raspberry Pis greatly outclassed the under-powered ones that were available for use in the

Elizabethtown Computer Engineering lab. Two of them were purchased so that each one of us could experiment with them in our own way on our own time without having to worry about conflicting with each other and the classes that use the already available.

## VI. SOCIAL, ETHICAL, AND ENVIRONMENTAL IMPACTS

### A. Social Impacts

Our project doesn't have any direct social impacts. It's a project that many people will find interesting and that could be useful in an educational environment.

### B. Ethical Impacts

The biggest ethical factor of our project is about the code itself. We decided to keep the project 100% open source. We think this is important for a project like this to allow others to learn from our design. All of the libraries included are also all open source.

### C. Environmental Impacts

Our project will have no impact on the environment, as it will be written entirely in software. Hardware that is interfaced with Cosmic such as Raspberry Pis will be completely reusable afterwards.

## VII. DESIGN

design tables and stuff copy pasta from design choices + system level design

## VIII. IMPLEMENTATION

design philosophy + design implementation

## IX. TESTING METHODOLOGIES

Cosmic was designed to be modular for developers to be able to create their own parts of the system. A byproduct of this also made Cosmic incredibly easy to test. We made a significant effort to automate as much of the testing as possible to verify that the builds we produce are stable on any platform.

While testing Cosmic, we use three primary ways of testing the system, assembler, and any other correlated code.

### A. Unit Testing

Unit testing is a great way to make sure that parts are working exactly as we expect them to as things get updated and features get added. Currently, the only portion of the project that is unit tested is the Cosmic Processor itself. As of the writing of this document, we have 565 assertions over 54 different test cases that are all passing. These tests check each instruction in a variety of different ways to make sure that they are working as intended. Not only does this provide a great way to make sure the processor is working correctly, but while writing these tests, we found numerous bugs throughout the processor. Listing 1 shows an example of one of the many test cases for the SHLX Instruction.

```
1 /* 0x4C-0x4F */
2 TEST_CASE("shlx", "[opcodes]"){
3     cosproc proc = cosproc(MemoryRead, MemoryWrite);
4     //Imm
5     /*
6     0000: 4C 00 01 ...
7     */
8     reset(&proc);
9     memory[0x00] = 0x4C;
10    memory[0x02] = 0x01;
11    proc.r[0] = 0x44;
12    proc.r[1] = 0x22;
13    proc.cycle();
14    REQUIRE(proc.r[0] == 0x88);
15    REQUIRE(proc.r[1] == 0x44);
16 }
```

Listing 1. A unit test for the SHLX instruction.

We set up the processor and memory to properly execute the test case we are trying to replicate. The processor then executes the instructions and the memory, registers, or flags are checked depending on the purpose of the test. For the processor, we used Catch2 to write our unit testing, a multi-paradigm test framework for C++. In the future, the unit tests will be written for the assembler using the pytest framework.

### B. Manual Testing

Since the majority of the system is not thoroughly tested or is difficult to test (ex. GUI), manual testing is done to ensure that everything is working well and doesn't break. This constitutes us going in and trying to do everything from using the project normally, to breaking the GUI, to running edge cases. While this helps us discover bugs, it also encourages us to think of ways to improve the GUI and the overall usability of our project. While our unit tests focus on individual instructions or functions, our manual tests ensure that the entire system as a whole is working accordingly.

### C. Automated Testing

Using Travis CI, every time we push an update or change to our GitHub repository, it kicks off several separate builds of the Cosmic system. Cosmic gets compiled and tested on five different environments; x86, ARM, Linux, Windows, and macOS. These builds compile in a clean environment to ensure that the system can compile and run correctly across each platform, not just the systems we are using to design it. The builds start by cloning the repository, then downloading the necessary packages, then finally compiling and running our unit tests. After the builds are complete, we get notified if something causes them to fail so that we know what to fix.

### D. Testing Results

Using the results of our manual and automated testing, we track bugs and other issues using GitHub. Whenever a bug, undesired behavior, or improvement needs to be made, an issue is opened on GitHub and tracked in our Project tracker. The bug is then cataloged, assigned, and worked on. Once it's verified that the bug is squashed, the issue is then closed.

## X. RESULTS

Three introductory labs were created to teach how to write assembly and utilize Cosmic in an educational environment as well as show off some of the features of Cosmic. The labs are generated from a compact and clean LaTeX template, and are easily editable and reusable. They include information such as the lab objective, a prelab section that asks the students to familiarize themselves with specific instructions or other key aspects of Cosmic beforehand, a during lab section that lays out the problem the students must solve, a grading section that breaks down exactly what is required of the student for the lab, and a helpful links section that directs to Cosmic documentation and other helpful places.

### A. Basic Operations

The first lab asks the student to calculate and the  $n$ th Fibonacci number. Since our processor, is only 8 bits, the following constraint is applied:  $1 \leq n \leq 13$ . This lab teaches the basics of Cosmic, such as using instructions, variables, and loops in the assembler to carry out simple tasks. A sample solution is shown in Listing 2.

```
1 byte fib = 6
2 ;The Fibonacci number we wish to calculate
3 byte counter = 2
4 byte first = 0
5 byte second = 1
6 ;Initialize the first two numbers in the sequence to
   be 1
7 byte final = 0
8 JMP #08
9 end:
10     MOV R0 final
11     HCF
12     ;End of program
13 MOV #1 R0
14 CMP fib
15 JZS #end
16 ;Check to see if fib is 1
17 calculate:
18     MOV first R0
19     ADD second
20     MOV second first
21     MOV R0 second
22     MOV fib R0
23     CMP counter
24     MOV second R0
25     ;Needed since we store R0 in final
26     JZS #end
27     INC counter
28     JMP #11
```

Listing 2. Cosmic assembly to find the  $n$ th Fibonacci number.

Since we are calculating the  $n$ th Fibonacci number, we stored our  $n$ , in this case 6, as fib. A Fibonacci number is calculated by adding together the previous two numbers in the sequence, so we stored the first and second numbers of the sequence accordingly. We defined a counter variable to keep track of which Fibonacci number we are on, and we initialize it to two because if we want the first number in the sequence, we can just return 1 without any calculations. Lastly, we defined a final variable to store our answer in. We then defined an end loop, which is only called after we have generated our answer, to store our final answer in. The MOV,

CMP, and JZS instructions following the end of the end loop act as an if statement to check if fib is equal to 1. If it is not, then we head into the calculate loop. This loop moves our first variable into the accumulator and adds the second to it. Then it moves the second variable to the first, and the accumulator to the second. It then checks to see if we have calculated the  $n$ th Fibonacci number or not. If we need to continue calculating, the counter is incremented and JMP is called to bring us back to the start of the calculate loop. If the answer has been found, the end loop is called and the program ends after it executes. After the program ends, 8, the 6th Fibonacci number, should be stored in the final variable.

After a program like the one found in Listing 2 is written, it is as simple as clicking Assemble in the Cosmic editor to assemble the code into machine code and load it into the memory editor. All the user has to do afterwards is simply click Run to start executing the program.

### B. Basic Graphics

The second lab asks the student to write a program to output a gradient to the Cosmic video screen. Since Cosmic's video output works by reading the video memory for brightness values for each pixel, ranging from 0x00 to 0xFF, the student must loop through the video memory and store either decreasing or increasing values of brightness for the pixels. This lab again reinforces the basics of Cosmic and introduces the usage of the video output. A sample solution is shown in Listing 3.

```
1 byte brightness = FE
2 ;Start at full brightness -1 so that the program
   terminates correctly.
3 word start = 8000
4 ;Starting pixel
5 MOVX #BFFF R0
6 ;Store the end location of the video memory.
7 loop:
8     MOV brightness @start
9     ;Set the brightness of this pixel
10    DEC brightness
11    ;Decrement the brightness by two because the
      video out is 64x64
12    INCX start
13    CMPX start
14    JNZ #loop
15    ;Repeat until the brightness reaches 0
16    HCF
17 ;End of program
```

Listing 3. Cosmic assembly to output a gradient to the video screen.

Since Cosmic's video screen is 64x64, we define our initial brightness to be 0xFE. We also define a word, 16 bits instead of 8 bits, starting location for the beginning of the video memory. We then MOV the end location of the video memory to the 16-bit accumulator as a reference. Next, we defined a loop to MOV the current brightness value to the current pixel in video memory. We then decrement the brightness variable twice, since each line of the video screen is only 64 pixels long, and increment our position in video memory. Lastly, we compare our position in video memory to the accumulator, since we stored the end location there, to determine if we

should continue drawing or end the program. The outcome is a smooth gradient as can be seen in Figure 1.



Fig. 1. The gradient produced in Lab 2.

### C. Real-world Interfacing

The third and final lab asks the student to write a simple program to read input from a button hooked up to a Raspberry Pi and turn on an LED when input is detected. This lab introduces the student to interfacing Cosmic with something physical like a Raspberry Pi. Even though the task in the lab is simple, it opens up a whole new world of opportunities to explore. In Cosmic, Raspberry Pi GPIO pins are mapped to the values in memory from 0xC402 to 0xC41E. The most significant bit in the GPIO bytes signify whether the pin is an input or an output. The remaining 7 bits set it ON or OFF. If the 7 bits equal zero, then the pin is OFF. If they equal something greater than 0, then it is ON. A sample solution is shown in Listing 4.

```

1 word inputLoc = C402
2 word outputLoc = C403
3 start:
4     MOV inputLoc R0
5     AND FF
6     JNZ #turnON
7     JMP #turnOFF
8 turnON:
9     MOV FF outputLoc
10    JMP #start
11 turnOFF:
12    MOV 80 outputLoc
13    JMP #start

```

Listing 4. Cosmic assembly to interface with a Raspberry Pi.

Since the pins are mapped directly to memory in Cosmic, we defined two words to store the input and output locations of the pins we want to use. We then defined a start loop that

determines if the LED should be turned on or off based on ANDing the input value with 0xFF and jumping accordingly. If the LED needs to be turned on, the loop jumps to the turnON label that MOVs 0xFF to the output location and jumps back to the start loop. If the LED needs to be turned off, the loop jumps to the turnOFF label that MOVs 0x80 to the output location and jumps back to the start loop. This allows the student to infinitely poll the input from the Raspberry Pi.

## XI. CONCLUSION

In the end, we could not be happier with the product we delivered. We committed a significant amount of time to Cosmic throughout the last year to get it to where it is now and learned a lot along the way. We hope that Cosmic, or even just the idea of Cosmic, enables and encourages more people to get involved with the retro-computing community and to learn about low level computer design and functionality.

## XII. ACKNOWLEDGMENTS

people who helped on the project + dr Li?

## REFERENCES

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