INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, KALYANI
An Institute of National Importance
(Autonomous Institution under MoE, Govt. of India &
Department of Information Technology & Electronics, Govt. of West Bengal)
C/o WEBEL IT Park, B-14, Opposite of Kalyani Water Treatment Plant
Near Buddha Park, Dist.-Nadia, P.O.-Kalyani, PIN - 741235, West Bengal
Email-office@iiitkalyani.ac.in, website-www.iiitkalyani.ac.in

Advt. No. IIITK/Rectt/MeitY-PA/23-24/39   Dated: 11-08-2023

Project Associates Positions under C2S, MeitY Project

Applications are invited under the under the **MeitY** sponsored project. The details are:

Positions	Project Associate-I, Project Associate-II
Number of Position (s)	01 for Project Associate-I, 01 for Project Associate-II
Title of The Project	VLSI Implementation of Crypto-Hardware targeting Classical and Post-Quantum Cryptography (Code : IIITK/SRIC/MeitY/23-24/2/VLSIQ)
Principal Investigators	**Principal Investigator**: Prof. Santanu Chattopadhyay, Professor & Director, ECE **Co-Principal Investigator**: Dr. Rinky Sha, Assistant Professor, ECE **Other Investigators:** (i) Dr. SK Hafizul Islam, Assistant Professor, CSE (ii) Dr. Dalia Nandi (Das), Assistant Professor, ECE (iii) Dr. Pratik Chakraborty, Assistant Professor, ECE (iv) Dr. Bapi Kar, Assistant Professor, ECE (v) Dr. Amit Ranjan Azad, Assistant Professor, ECE
Tenure of Project	5 years
Job Description	The selected candidate is expected to carry out the research work related to the project and to help ensure the timely and successful completion of the project, in consultation with the PI. S/He is also expected to maintain the CAD tools, write the initial drafts of reports, programs, manuscripts, and patents resulting from the project in consultation with the PI.
Essential Qualifications	One of the following criteria be satisfied: 1. M.E/M. Tech in Electronics and Communication Engineering / Computer Science and Engineering / Electrical Engineering or related discipline 2. B.E/B. Tech in Electronics and Communication Engineering / Computer Science and Engineering / Electrical Engineering or related discipline with GATE score
Desirable Qualifications	Experience in circuit design, VLSI CAD tool handling

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Fellowship	**Project Associate-I** @ 31,000/-+ 27% HRA per Month (as per Institute norms).
	Project Associate-II @35,000/-+ 27% HRA per Month (as per Institute norms).
Last Date & Time	Interested candidates are requested to apply by filling up the form prescribed in this advertisement and send through Post/Courier to **The Deputy Registrar** **Indian Institute of Information Technology Kalyani,** **Webel IT Park Campus, Near Budha Park** **Kalyani - 741235, Nadia, West Bengal.** In addition to the Hard Copy, send the soft copy applications along with the copy of all mark sheets and GATE score card if applicable, in a single pdf, to **recruitment@iiitkalyani.ac.in** by **Last Date:** Hard copy through post/courier and softcopy through mail – by **August 31, 2023 4:00 PM**
Registration for Ph.D.	Selected candidates are expected to register for full time Ph.D. program at Indian Institute of Information Technology Kalyani as per Institute norms.

Note:

(1) Shortlisted candidates will be informed the date of interview by e-mail.

(2) Mere fulfilment of eligibility criteria does not ensure an applicant's right to join in the Project associate position and Ph.D. programme. The decision of IIIT Kalyani will be final in this regard.

(3) The Institute reserves the right not to fill the position if no candidate is found suitable.

(4) This post is purely contractual basis.

(5) All candidates should make their own arrangements for their stay at Kalyani, if required. NoTA/DA will be paid to attend the interview.

For further clarifications, contact rinky@iiitkalyani.ac.in

Dr. Muruganantham Ponnusamy
Deputy Registrar, IIIT Kalyani