

Dept. of Electronics and Electrical Communication Engineering
Indian Institute of Technology Kharagpur

VLSI DESIGN LABORATORY(EC69216)



Title:

TWO-STAGE OP AMP

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Objective:

Design and Analysis of TWO STAGE- Differential Amplifier for the given specifications.

- $V_{DD} = 1.8V$
- $A_v \geq 1000, 60dB$
- Phase Margin $\geq 60^\circ$
- $C_L = 2pF$
- $ICMR (+) = 1.6V$
- $ICMR (-) = 0.8V$
- Slew Rate = $20V/\mu sec$
- Power dissipation $< 0.3 mW$
- $GBW \geq 30MHz$

Introduction:

- A two-stage differential amplifier is a type of electronic amplifier that amplifies the difference between two input signals while suppressing any voltage common to both inputs.
- A two-stage amplifier can provide high gain and high output swing. The first stage is a differential amplifier, an Analog circuit with two inputs (V_{in+} and V_{in-}) and one output (V_{out}), which is proportional to the difference between the two inputs. The second stage is a Common Source Amplifier.

Two-Stage Configuration:

- In a two-stage differential amplifier, two operational amplifiers (op-amps) are used in sequence. The first stage is typically a non-inverting amplifier, and the second stage is a differential amplifier. This configuration increases the gain and input resistance of the amplifier.

First Stage: Non-Inverting Amplifier:

- The first stage amplifies the input signal without inverting it. The gain of this stage is determined by the feedback resistors used in the circuit. The output of the first stage becomes the input for the second stage.

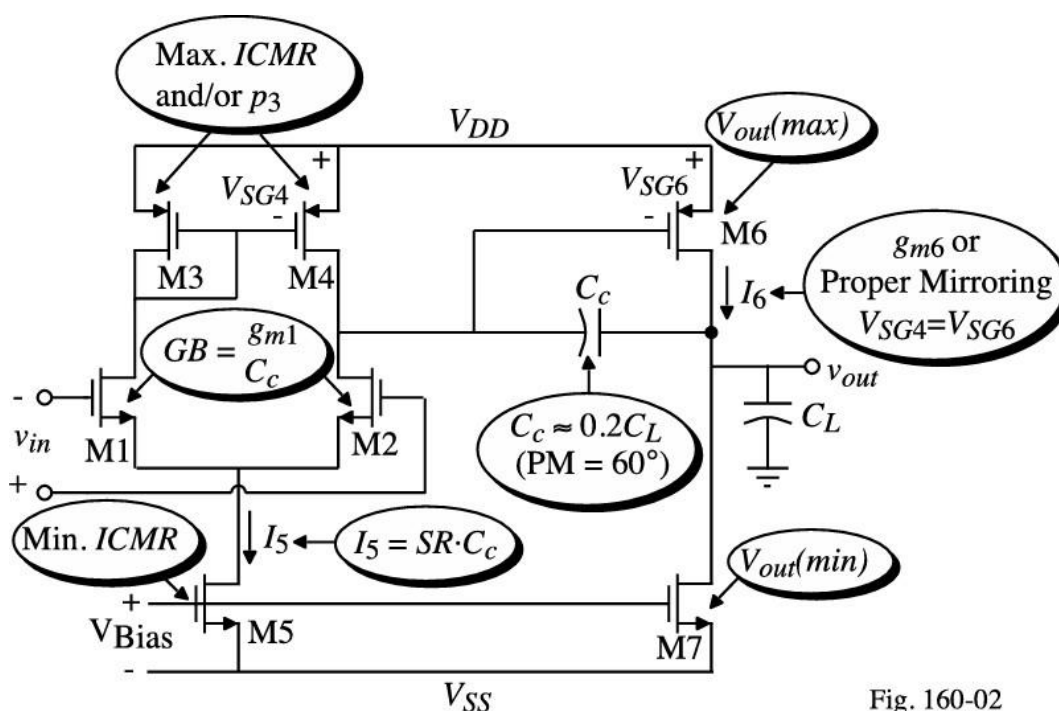


Fig. 160-02

BIAS is done by the current mirror circuit

- Gain of the differential amplifier is the product of transconductance of M1 and Rout
 $A_d = g_{m2} \{ r_{ds2} \parallel r_{ds4} \}$
- While that of CS stage is $A_v = g_{m6} \{ r_{ds6} \parallel r_{ds7} \}$

Circuit Diagram:

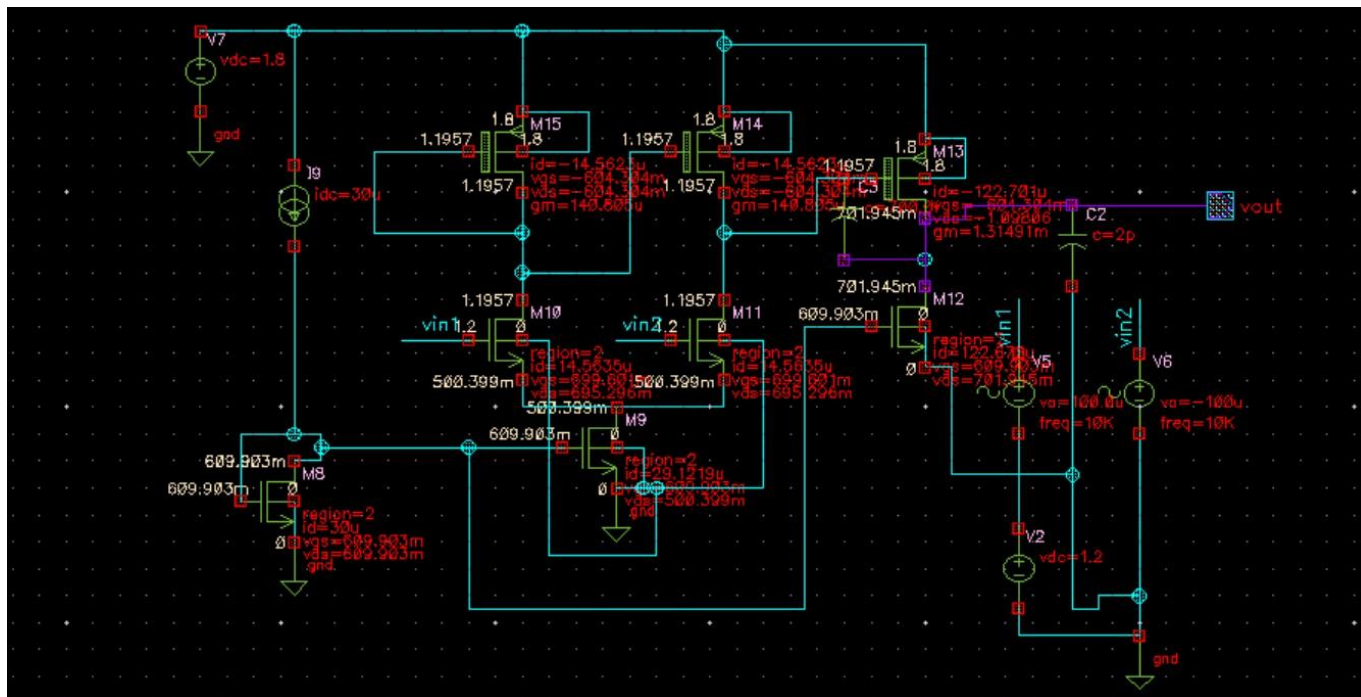


Figure: 2-stage differential amplifier

Plots:

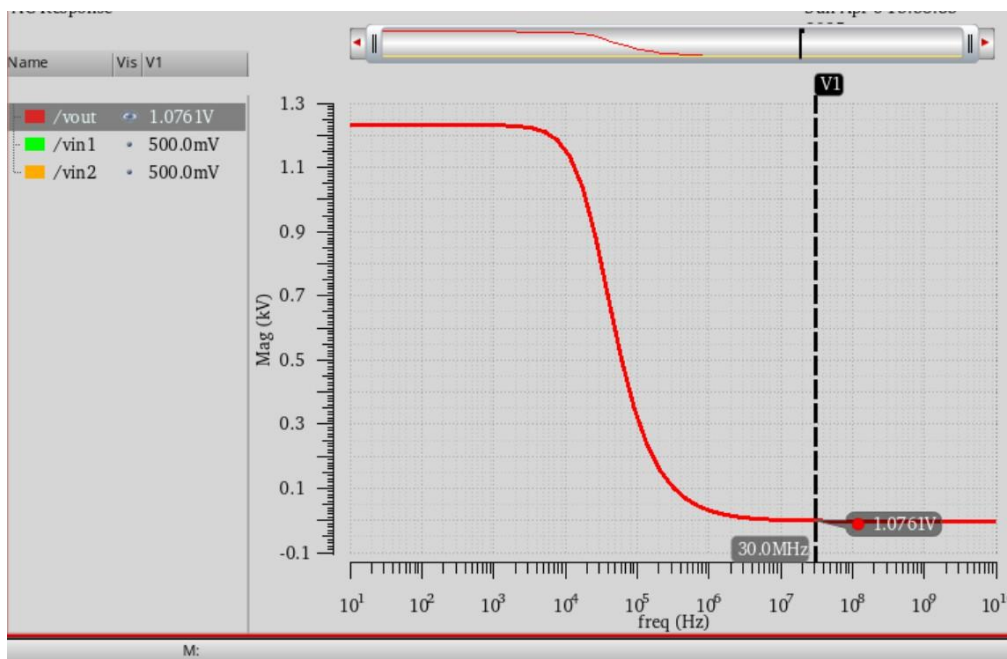


Figure: GAIN PLOT FOR DIFFERENTIAL MODE

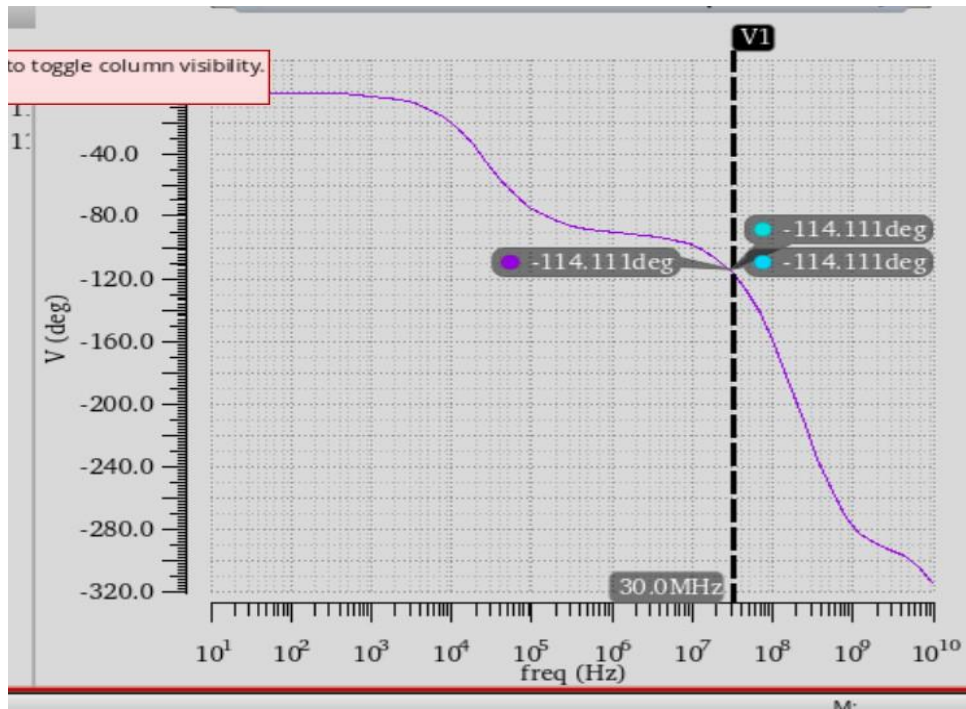


FIGURE: PHASE PLOT FOR DIFFERENTIAL MODE

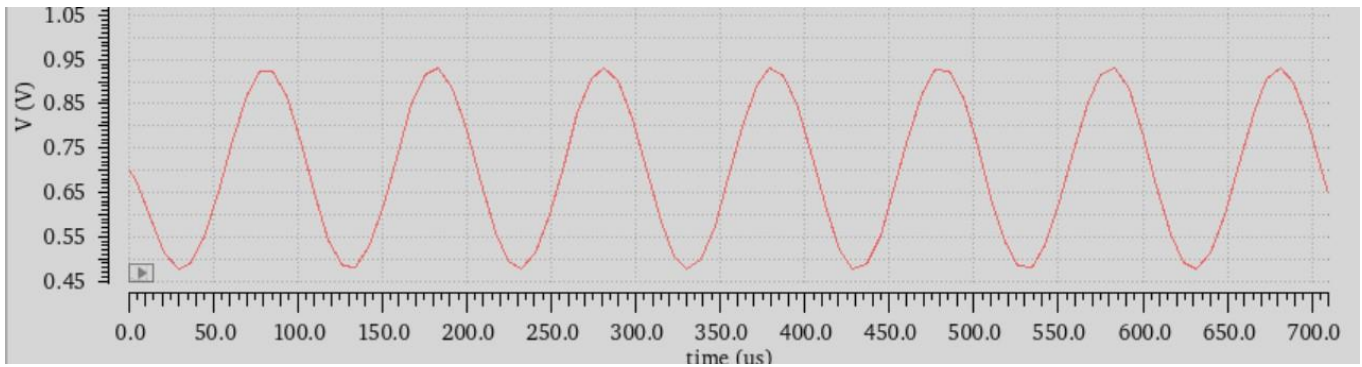


FIGURE:TRANSIENT ANALYSIS

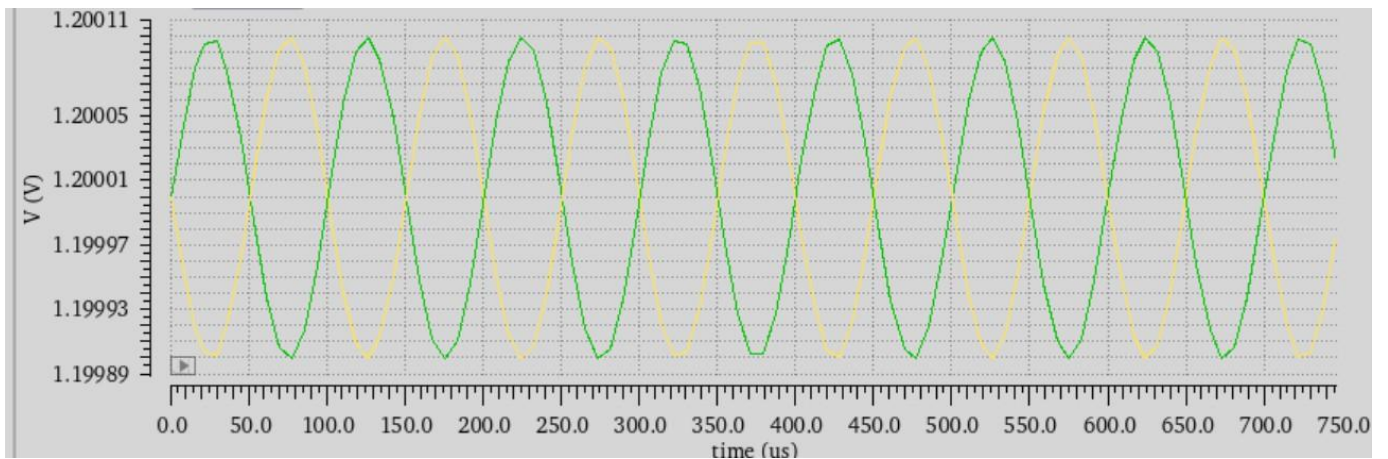


FIGURE:TRANSIENT ANALYSIS FOR VIN AS DIFFERENTIAL MODE

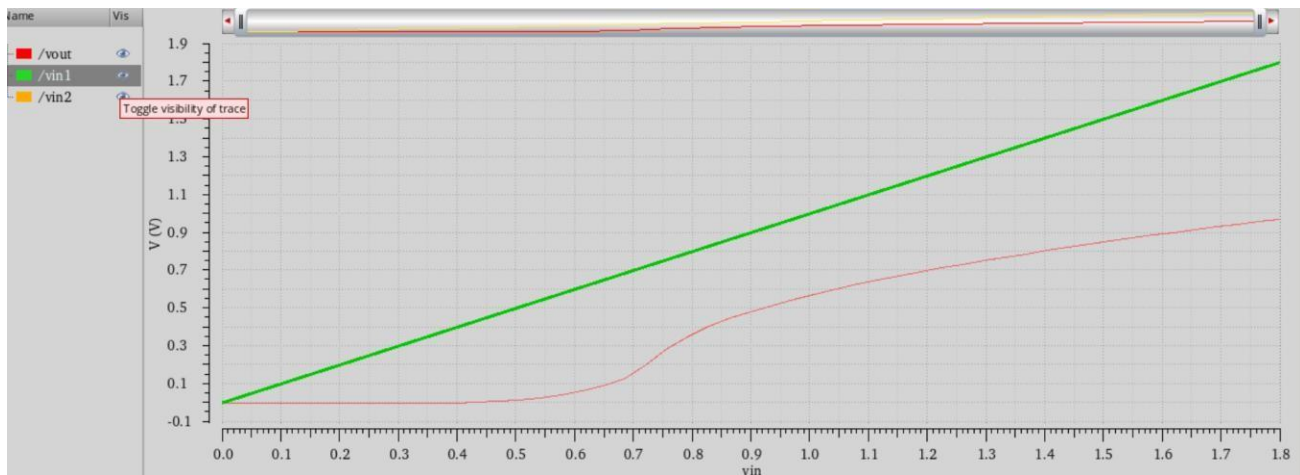


Figure: DC ANALYSIS

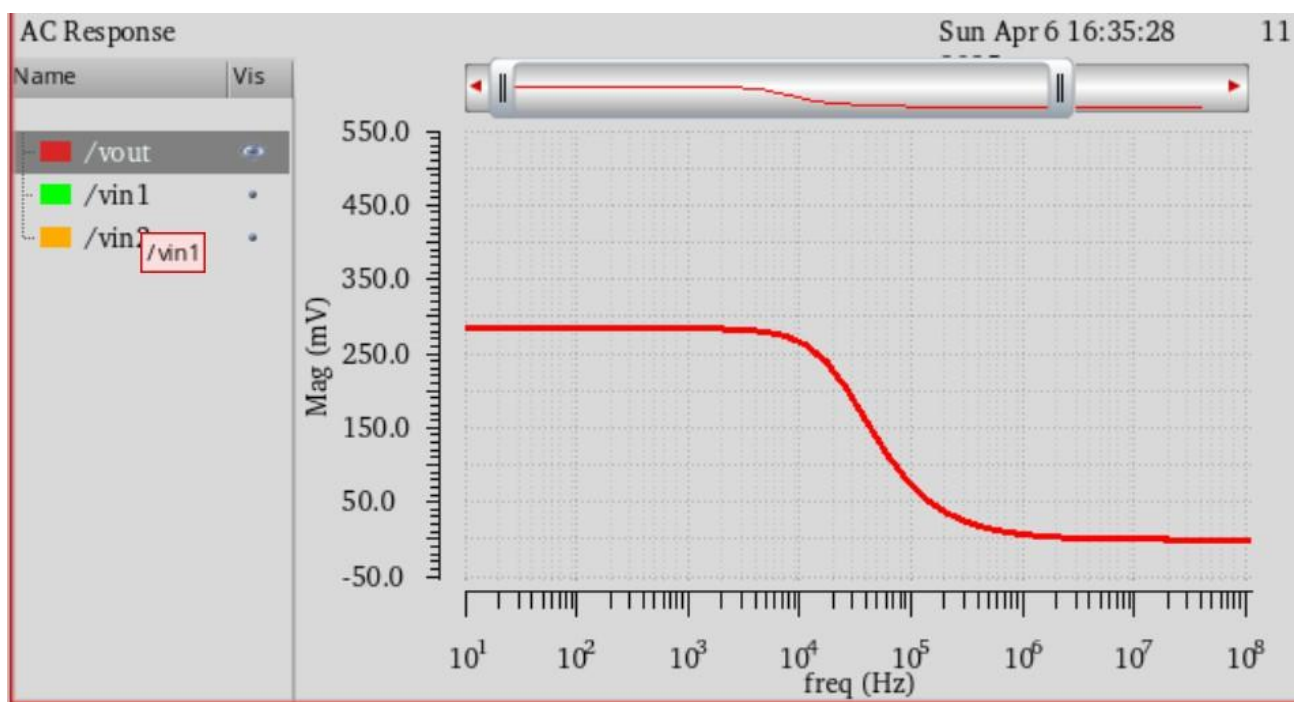


Figure: COMMON MODE GAIN

Results and Calculations:

From the circuit diagram, the theoretical and practical values are

• Parameter	• Theoretical	• Practical
• (W/L) ₉	• 2.1/0.2	• 2.1/0.2
• (W/L) ₈	• 2.1/0.2	• 2.1/0.2
• (W/L) ₁₀	• 1.6/0.2	• 1.8/0.4
• (W/L) ₁₁	• 1.6/0.2	• 1.8/0.4
• (W/L) _{15,14}	• 2.6/0.2	• 6/0.4
• (W/L) ₁₃	• 2.6*8/0.2	• 3*8/0.2
• (W/L) ₁₂	• 2.1*4/0.2	• 2.1*4/0.2
• C _c	• 0.7p F	• 0.7p F
• I _{tail}	• 30u A	• 29.129u A

• Gm1	• 131.85u	• 158.138u
• Gm2	• 1.048m	• 1.32902m

Calculations:

Date: / /

$K_n = 140 \mu A/V^2$, $K_p = 55 \mu A/V^2$, $V_{th,n} = 0.5V$, $V_{th,p} = 0.47V$

$Z = \frac{g_{m2}}{C_c}$ $GBW = DC_{gain} \times P_1$

$A_1 = \frac{1}{g_{m2} R_2 C_c} = \frac{g_{m1} R_1 g_{m2} R_2}{g_{m2} R_2 C_c}$

$P_2 = \frac{g_{m2}}{C_c}$ $GMV = \frac{g_{m1}}{C_c}$

$A_{DC} = g_{m1} R_1 g_{m2} R_2$ $slewrate = \frac{I_o}{C_c}$

Phase Margin: $\angle \geq 10^\circ$ GBW

$\angle V_{out} = -\tan^{-1}\left(\frac{\omega}{2}\right) - \tan^{-1}\left(\frac{\omega}{P_1}\right) - \tan^{-1}\left(\frac{\omega}{P_2}\right)$

$\angle V_{in}$

phase at GBW:

$-180^\circ + PM = -\tan^{-1}\left(\frac{1}{10}\right) - \tan^{-1}(A_{DC}) - \tan^{-1}\left(\frac{\omega}{P_2}\right)$

$PM = 6^\circ \Rightarrow \tan^{-1}(M) = \frac{GBW}{P_2}$

$P_2 \geq 2.2 \text{ GBW}$

$\frac{g_{m2}}{C_c} \geq 2.2 \frac{g_{m1}}{C_c}$ (create zero at 10 GBW)

$C_c \geq 0.22 C_L$ $g_{m2} = 0.5 g_{m1}$

$C_L = 2 \text{ pF}$

$C_c > 0.44 \text{ pF}$

$C_c = 100 \text{ fF}$

$GBW = \frac{g_{m1}}{C_c} \geq 30 \times 10^6 \times 2\pi$

$\frac{g_{m1}}{100 \times 10^{-15}} \geq 30 \times 10^6 \times 2\pi$ $\beta = 100$

$g_{m1} \geq 2.9 \times 10^{-6} \Rightarrow \sqrt{\frac{2 \times 140 \mu A}{1 \times 10^{-6}}} \geq 2.9 \times 10^{-6} \times 2\pi$

power $\Rightarrow I_{total} \times 1.8 < 0.3 \text{ mW}$

slewrate $\Rightarrow I_{total} < 166 \mu A$

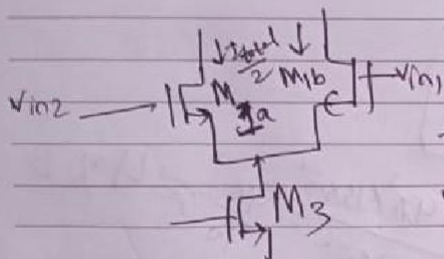
$\frac{I_{total}}{C_c} \geq 20/10$

$$I_{total} \geq 14 \times 10^{-6}$$

$$\therefore I_{total} = 30 \mu A$$

$$ICMR = 0.8 \Rightarrow V_{GS1} - V_{th,n} = \sqrt{\frac{2I_{tot}}{2\beta_{M1}}}$$

$$\beta_{M1} = k_n \left(\frac{W}{L} \right)_{M1} = 140 \times 10^{-6} \frac{W}{L}$$



for ICMR

$$V_g = 0.8, V_s = (V_{dsat})_{M2}$$

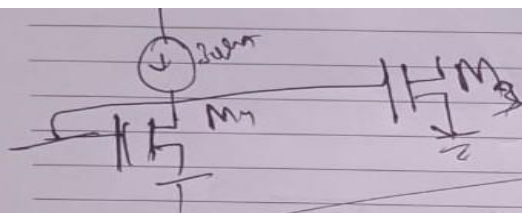
$$\text{from } g_{m1} \Rightarrow \sqrt{\beta_{M1} I_{tot}} = 2.8 \times 10^{-6} \text{ A}$$

$$0.3 - \sqrt{\frac{I_{tot}}{\beta_{M1}}} = (V_{dsat})_{M3}$$

$$(V_{dsat})_{M3} = 0.3 - \frac{2.8 \times 10^{-6}}{3.7 \times 10^{-6} \text{ A}^2} = 0.3 - 0.75 \times 10^{-6} \approx 0.2 \approx \boxed{V_{GS1} - V_{th}}$$

$$\beta_{M1} = \frac{1.5 \times 10^{-12} \text{ A}^2}{3 \times 10^{-6} \text{ A}^2} = 3.7 \times 10^{-6} \text{ A}^2$$

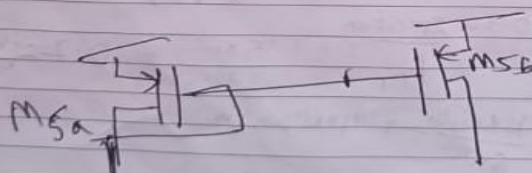
$$10 \times \left(\frac{W}{L} \right)_{M1} \times 10 \times 0.2^2 = 30 \mu A$$



$$\left(\frac{W}{L} \right)_{M1} = \frac{30}{10 \times 0.04} = 10.7 \mu m$$

$$\left(\frac{W}{L} \right)_{M3} = 20.4$$

$$\left(\frac{W}{L} \right)_{M1} = \frac{\beta}{k_n} = \frac{4 \pi^2 \times 1.5 \times 10^{-6}}{150 \times 10^{-6}} = \frac{4 \pi^2}{10} \approx 4 \times 2$$



Considering (CMR⁺) $V_{dm,1} > V_{gm,1} - V_{th}$

$$V_{dsat} + V_{th} = V_{gm,1}$$

$$(V_{in})_{max} = V_{d,1} + V_{th}$$

$$(V_{in})_{max} = V_{ds} - V_{sgs} + V_{th} \Rightarrow \boxed{V_{sgs} = 0.6V}$$

$$I_5 = \frac{\beta_{M5}}{2} (V_{sg} - 0.4V)^2$$

$$15 \times 10^{-6} = \frac{55 \times 10^{-6}}{2} (V_{sg} - 0.4V)^2$$

$$\left(\frac{W}{L}\right)_{M5} = \frac{60/2}{55 \times (0.2)^2} = 19.27$$

Now for Stage 2 \rightarrow Power $10 \times 60 \times 4.8 + 1.8 \times 1 \leq 0.3mW$

$$\boxed{I \leq 190}$$

$$\boxed{I = 100}$$

Stage 2 \rightarrow M_2 \rightarrow M_3 \rightarrow M_4 \rightarrow M_5

$$I_{M2} = \left(\frac{W}{L}\right)_{M2} \times \frac{V_{gs}^2}{2}$$

$$\boxed{\left(\frac{W}{L}\right)_{M2} = \left(\frac{W}{L}\right)_{M3} \times A} \quad \text{because } I = 100$$

