



CERTIFICATE OF COMPLETION

Celebrating
15+YEARS
Of Excellence

MSUID : **MS/KW/2024-0655**

This is to certify that.....

Abisa M

*has participated in **VLSI Design Using Verilog HDL**,*

*an online workshop from **24th to 26th Feb 2024***

A handwritten signature in blue ink.

PROF. VIGILKUMAR V V
HEAD, TALENT BASE BUILDERS, ASAP KERALA



SIVAKUMAR P R
CEO, MAVEN SILICON