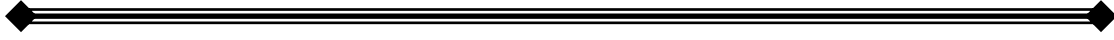


California University of PA
Dept. of Computer Science, Info Systems, and Engineering Technology



CET335 Microprocessor Interfacing

Fall 2021

= Lab Report =

Project: 180° Servo Rangefinder

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Member 2: Charles Krug

Date Submitted: 12/02/2021

Project: 180° Servo Rangefinder

Date Performed: 11/11/2021 – 12/02/2021

I. OBJECTIVES

Main objectives are to use the user manuals and data sheets for the ultrasonic range finder to construct a working 180° rangefinder. Secondary objectives are to properly code, troubleshoot, and operate the code and circuit to complete the main objective.

II. PROCEDURE

First either create a flow chart, pseudo code, or take proper notes on what ports to use for the project layout. Since multiple ports need to be used for the code a proper setup is necessary to help construct the code without too many errors. Once the layout has been set, start by correctly initializing all the used ports and make sure SysTick and PLL is being used. The best way to work through the code is to set the major processes in the code as functions that can be used in blocks to help troubleshoot the code. Start with the 10µs delay for the trigger input, then work through driving the sensor output to the board. Create a function that reads the high trigger output, then set a count until it hits the low trigger output of the sensor's echo. Using count from the echo's output, which is the pulse width, multiply it by a number starting at 0.01. Now the sensor must be calibrated to get the correct centimeter output, get a measuring device, and measure out a flat surface (20cm is a good start) that leads to a large perpendicular wall or screen. Using a `printf` and the UART to output the calculated distance, keep changing the number till the sensor reads the length measured. Once the right measurement is achieved try it with different measured distances to check its accuracy. Next create an FSM that uses the data from the sensor to change its outputs. Once the sensor and code are working, set up the sensor on the servo, make sure to have the servo properly set to 0° before changing the degree. A mount for the servo would be helpful for troubleshooting since the sensor can cause it to be off balance. Set up two different sections of code for the CW and CCW movement, the code will be pretty similar, but the duty be set to different end points either +90° or -90°.

a. Equipment

- breadboard, jumper cables, red, yellow, green, and white LED
- power supply module, 9V 1A Power Supply, HC-SR04 ultrasonic ranging module
- resistors: 220 Ω x4

III. DISCUSSION

When constructing a project with no layout it is very important to create a layout to not cause confusion or issue when it comes to programming. Since there are many ports being used it can get confusing what goes where and what needs to be set to input or output. Another issue when using multiple ports some special functions like pulse width modulation are only used with port A and B pin seven and will not work properly with other pins. Another issue that was found while constructing this project is some ports can only be used for one function or it can cause issues with the function. Such as the ultrasonic ranging module, if other registers are used with the port that do not need to be set for the ultrasonic ranging module it will cause issues with sensor. Sometimes the trigger won't set, or the output won't be ran, only simple input and outputs can be put ran on the same port of the sensor.

IV. CONCLUSION

When constructing standalone projects, it is good to have a plan and a layout set in mind, the use of pseudo code, and flowcharts add the project. Port layouts and notes will also aid the construction of the projects, make sure to research what is necessary for the ports so that there are no compatibility issues such as pulse width modulation.

V. APPENDIX

A. Screen Captures

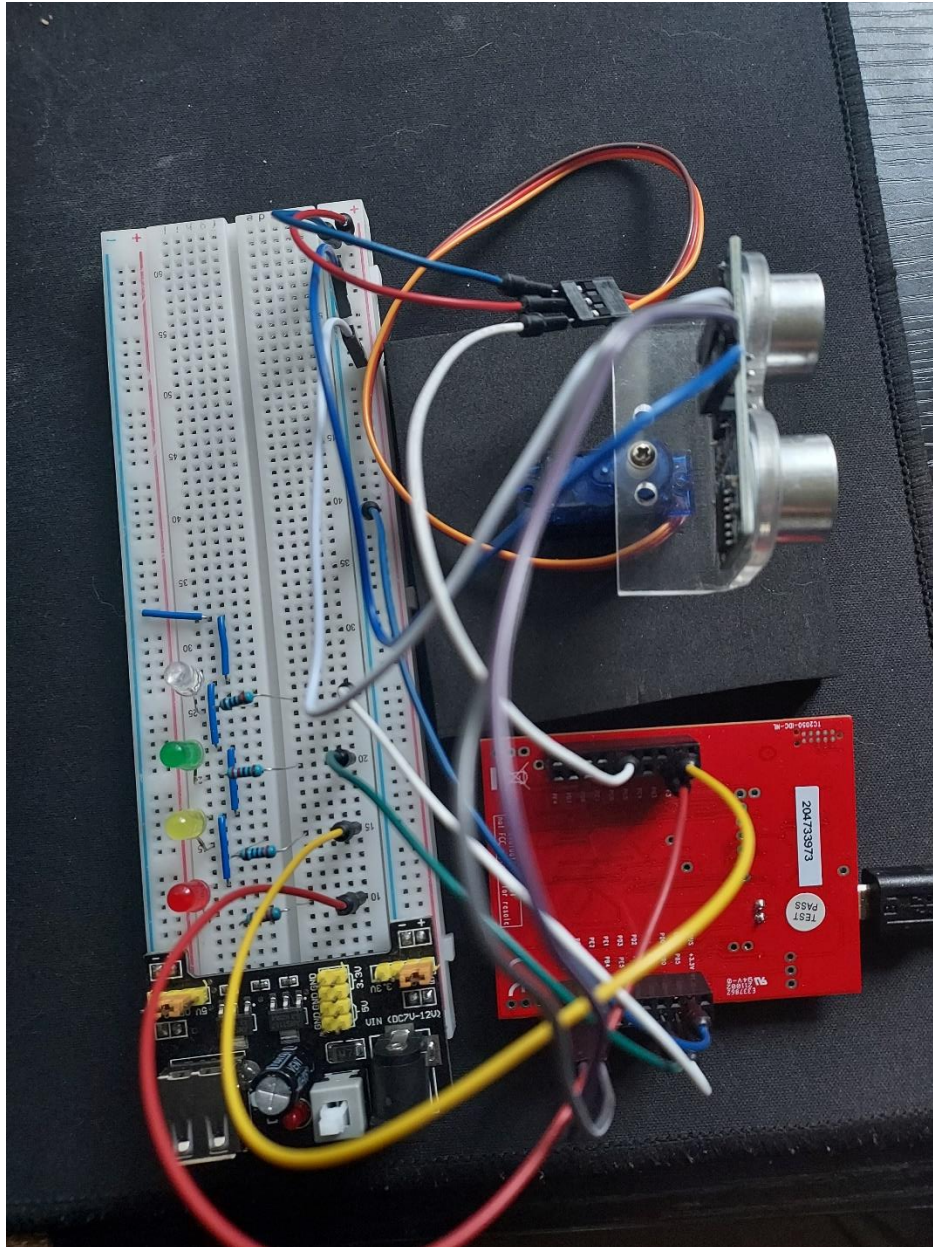


Figure 1: Circuit layout for ultrasonic ranging module, servo, and FSM output.

IN = \emptyset OUT = 2		8421 . 8421		10 11 12 13 14 15 0-9 A B C D E F		
		PA7	PA6	PA5	PA4 . PA3	PA2 PA1 PA \emptyset
I/O		\emptyset	1			
DISCRIPTION.		ECHO	TRIG			
		PULSE	PULSE			
		PB7	PB6	PB5	PB4 . PB3	PB2 PB1 PB \emptyset
I/O		1	1	1	1	1
DISC.	PWMQ/B		CW	CW	R	Y G B
			SW	SW	ERR	>1m 1m-2m <2m
		PC7	PC6	PC5	PC4 . PC3	PC2 PC1 PC \emptyset
I/O						
DISCRIPTION.						
		PF4	PF3	PF2	PF1	PF \emptyset
		\emptyset	1	1	1	\emptyset
		SW2	G	B	R	SW2

Figure 2: Notes on port layouts for input/output

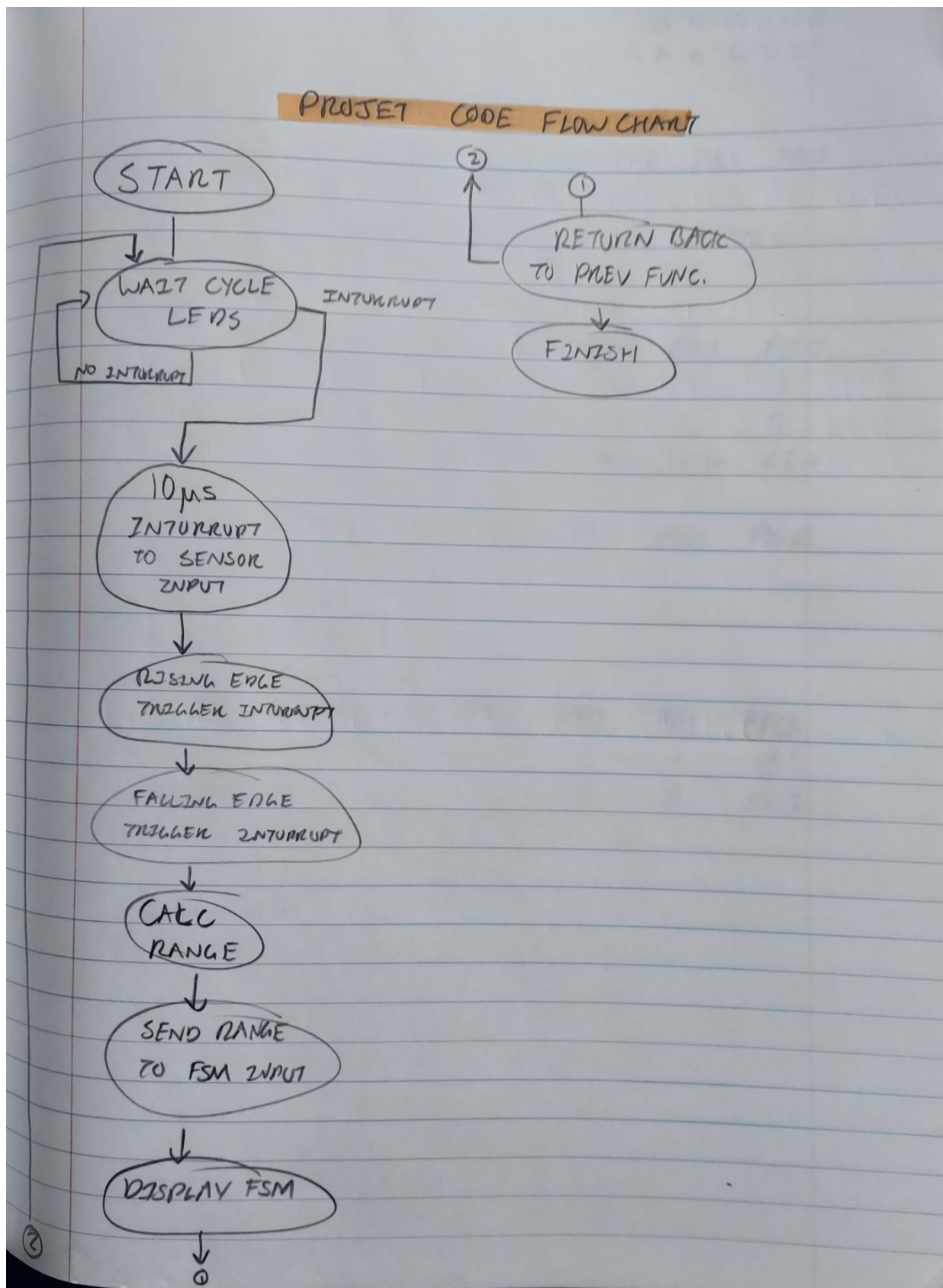


Figure 3: Project Flowchart

```

1 // ***** 0. Documentation Section *****
2 // main.c
3 // Runs on TM4C123
4 // Project: Range Finder using Interrupts/Timers
5 // Authors: Andrew D. Bissell, Charles Krug
6 // Date: November 11/2021 - November 28/2021
7
8 // Off Board hardware:
9 // Breadboard, jumper cables, red led, yellow led, green led, white led
10 // Power Supply Module, 9V 1A Power Supply, HC-SR04 Ultrasonic Ranging Module
11 // Resistors: 220ohm x4
12 // -----
13 // ***** 1. Pre-processor Directives Section *****
14 // -----
15 // #include <stdio.h> // standard C library
16 // #include "uart.h" // Uart for outputting to user
17 #include "tm4c123gh6pm.h" // Magical library of amazing #define's
18
19 #define Trigger ((volatile unsigned long *)0x40004100) // PortA Trigger Output
20 #define Echo ((volatile unsigned long *)0x40004200) // PortA Echo Input
21 #define LED ((volatile unsigned long *)0x40025038) // PortF LED's (on-board)
22 #define SW1 ((volatile unsigned long *)0x40025040) // PortF SW1 (on-board)
23 #define SW2 ((volatile unsigned long *)0x40025004) // PortF SW2 (on-board)
24 #define LIGHT ((volatile unsigned long *)0x4000503C) // PortA for LEDs all outputs
25 // ***** a. STATES *****
26 #define RLED 0 // Red LED if error or out of range
27 #define YLED 1 // Yellow LED if between 1 cm and 50 cm (0.01 m - 0.5 m)
28 #define GLED 2 // Green LED if between 50 cm and 75 cm (1 m - 2 m)
29 #define WLED 3 // White LED if above 75 cm
30 // -----
31 // ***** 2. Declarations Section *****
32 // -----
33 // ***** a. FSM Declarations *****
34 typedef struct StateStructure {
35     unsigned long Out; // 4-bit pattern to output
36     unsigned long Time; // delay in lms units
37     unsigned long Next[4];
38 }State;
39 typedef const struct State STyp;
40
41 State FSM[4] =
42 { // Outp,D, { 00, 01, 10, 11, },
43   {0x08,5, {RLED, YLED, GLED, WLED,}}, // RLED 0
44   {0x04,5, {RLED, YLED, GLED, WLED,}}, // YLED 1
45   {0x02,5, {RLED, YLED, GLED, WLED,}}, // GLED 2
46   {0x01,5, {RLED, YLED, GLED, WLED,}}, // WLED 3
47   // ERR,>50cm,>75cm,<75cm,}},
48 };

```

Figure 4: Documentation, Pre-processor directives, declarations sections

```

49 // ***** b. Function Prototypes *****
50 void PortA_Init(void); // Initializes PortA
51 void PortB_Init(void); // Initializes PortB
52 void PortF_Init(void); // Initializes PortF
53 void PWM0B_Init(unsigned int period, unsigned int duty); // Pulse Width Modulation for the motor
54 void EnableInterrupts(void); // Allow interrupts in the program to occur
55 void SysTick_Init(void); // Initializes SysTick Timer
56 void SysTick_Wait(unsigned long delay); // Initializes SysTick Delay using busy wait
57 // The delay parameter is in units of the core clock.
58 // (units of 20 nsec for 50 MHz clock)
59 void SysTick_Waitlms(unsigned long delay); // Initializes SysTick Delay using busy wait (lms delay)
60 // This assumes 50 MHz system clock.
61 void SysTick_Wait10us(unsigned long delay); // Initializes SysTick Delay using busy wait (1us delay)
62 // This assumes 50 MHz system clock.
63 void PLL_INIT(void); // Initializes the phase lock loop.
64 void PWM0A_Init(unsigned int period, unsigned int duty); // PWM for motor
65 void Timer0A_DelayMicroSec(int time); // Timer with interrupt for a 10 us delay
66 int Timer0A_PeriodCapture(void); // Captures the rising edge then falling edge of a
67 // pulse to get its pulse width.
68 unsigned int PulseWidth(void); // Captures pulse width with software programming
69 unsigned int Distance_Input(unsigned int); // Used set the input based on the measured distance
70 // ***** c. Global Variables *****
71 unsigned long S; // For current state
72 unsigned long Input; // For next state
73 unsigned long j; // Used for moving the motor by user input
74 unsigned int Distance; // Final Calc of distance
75 unsigned int echo_time; // Initial data from sensor
76 unsigned int average; // To take a 3 data average of data
77 unsigned int i; // Loop count

```

Figure 5: Function prototypes and Global variable sections

```

78 // -----
79 // ***** 3. Subroutines Section *****
80 // -----
81 int main(void){
82 // UART_Init(); // Initialize UART for printing
83 SysTick_Init(); // Initialize SysTick
84 PLL_INIT(); // Initialize PLL
85 PortA_Init(); // Initialize PortA
86 PortB_Init(); // Initialize PortB
87 PortF_Init(); // Initialize PortF
88 EnableInterrupts(); // Allow interrupts
89 PWM0B_Init(50000,3150); // Initialize the PWM and send the servo to 0°
90 while(1){
91 LIGHT = FSM[S].Out; // Set output for FSM before new cycle
92 LED = 0x08; // GREEN LED FLAG
93 // ***** a. Clockwise Movement *****
94 if((GPIO_PORTF_DATA_R&0x11)==0x01){
95 SysTick_Waitlms(25);
96 LED = 0x0E;
97 while((GPIO_PORTF_DATA_R&0x11)==0x01){
98 if(PWM0_0_CMPB_R < 5550){
99 while((PWM0_0_CMPB_R < 5550) && ((GPIO_PORTF_DATA_R&0x11)==0x01)){
100 j += 1;
101 SysTick_Waitlms(50);
102 PWM0_0_CMPB_R += j;
103 }
104 }
105 else{
106 while((PWM0_0_CMPB_R > 5550) && ((GPIO_PORTF_DATA_R&0x11)==0x01)){
107 j += 1;
108 SysTick_Waitlms(50);
109 PWM0_0_CMPB_R -= j;
110 }
111 }
112 }
113 }
114 LED = 0x00;

```

Figure 6: Subroutine and clockwise movement sections

```

115 // ***** b. Counter-Clockwise Movement *****
116 if((GPIO_PORTF_DATA_R&0x11)==0x10) && ((GPIO_PORTF_DATA_R&0x11)==0x10){
117     SysTick_Waitlms(25);
118     LED = 0x0C;
119     while((GPIO_PORTF_DATA_R&0x11)==0x10){
120         if(PWM0_0_CMPB_R < 1275){
121             while(PWM0_0_CMPB_R < 1275){
122                 j += 1;
123                 SysTick_Waitlms(50);
124                 PWM0_0_CMPB_R += j;
125             }
126         }
127         else{
128             while((PWM0_0_CMPB_R > 1275) && ((GPIO_PORTF_DATA_R&0x11)==0x10)){
129                 j += 1;
130                 SysTick_Waitlms(50);
131                 PWM0_0_CMPB_R -= j;
132             }
133         }
134     }
135 }
136 LED = 0x00;
137 // ***** c. Toggle Range Finder *****
138 if((GPIO_PORTF_DATA_R&0x11)==0x00){
139     average = 0; // Reset average
140
141     for(i=0;i<3;i++){ // Three samples, gets average to have better data.
142         LED = 0x00; // CLEAR LED FLAG
143         echo time = 0; // Reset echo
144         Trigger |= 0x40; // Set trigger high
145         SysTick_Wait10us(1); // 10 µs delay using systick
146         // Timer0A_DelayMicroSec(1); // 10 µs delay using timer/interrupt
147         Trigger &= ~0x40; // Set trigger low
148         // average += Timer0A_periodCapture(); // Get pulse width from sensor using timer/interrupts.
149         average += PulseWidth(); // Get pulse width from sensor using software programming.
150     }
151     average /= 3; // After three samples take the average
152     LED = 0x02; // RED LED FLAG
153 // ***** d. Calculate Range(cm) *****
154 Distance = average*0.01175; // Average is then multiplied to get correct cm output
155 // ***** e. FSM *****
156 // printf("\n Distance: %d cm\n",Distance); // Output to display of the distance in cm
157 Input = Distance_Input(Distance); // Sets the input using the distance
158 SysTick_Waitlms(500); // Delay
159 S = FSM[S].Next[Input]; // Get delay of the FSM
160 // Gets the next state
161 }
162 }
163 }

```

Figure 7: Counter-Clockwise movement, Rangefinder toggle, calculate range, and FSM sections


```

164 // -----
165 // ***** 4. Port Initializations Functions *****
166 // -----
167 void PortF_Init(void){ volatile unsigned long delay;
168     SYSCCTL_RCGC2_R |= 0x00000020; // 1) F clock
169     delay = SYSCCTL_RCGC2_R; // delay
170     GPIO_PORTF_LOCK_R = 0x4C4F434B; // 2) unlock PortF PF0 (SW2)
171     GPIO_PORTF_CR_R = 0x1F; // allow changes to PF4-0
172     GPIO_PORTF_AMSEL_R = 0x00; // 3) disable analog function
173     GPIO_PORTF_PCTL_R = 0x00000000; // 4) GPIO clear bit PCTL
174     GPIO_PORTF_DIR_R = 0x0E; // 5) Inputs PF4(SW1),PF0(SW2)
175     // Outputs PF3(Green),PF2(White),PF1(Red) LEDs
176     GPIO_PORTF_AFSEL_R = 0x00; // 6) no alternate function
177     GPIO_PORTF_PUR_R = 0x11; // enable pullup resistors on PF4,PF0
178     GPIO_PORTF_DEN_R = 0x1F; // 7) enable digital pins PF4-PF0
179 }
180
181 void PortB_Init(void){ volatile unsigned long delay;
182     SYSCCTL_RCGC2_R |= 0x00000002; // 1) B clock
183     delay = SYSCCTL_RCGC2_R; // delay
184     GPIO_PORTB_LOCK_R = 0x4C4F434B; // 2) unlock PortF PF0 (SW2)
185     GPIO_PORTB_CR_R = 0xFF; // allow changes to PB7 - PB0
186     GPIO_PORTB_AMSEL_R = 0x00; // 3) disable analog function
187     GPIO_PORTB_PCTL_R = 0x00000000; // 4) GPIO clear bit PCTL
188     GPIO_PORTB_DIR_R = 0xFF; // 5) PB7 - PB0 Outputs
189     GPIO_PORTB_AFSEL_R = 0x00; // 6) no alternate function
190     GPIO_PORTB_PUR_R = 0x30; // enable pullup resistors on PB5,PB4
191     GPIO_PORTB_DEN_R = 0xFF; // 7) enable digital pins PB7-PB0
192 }
193
194 void PortA_Init(void){
195     volatile unsigned long delay;
196     SYSCCTL_RCGC2_R |= 0x00000001; // 1) A clock
197     delay = SYSCCTL_RCGC2_R; // delay
198     GPIO_PORTA_DIR_R = 0x7F; // 5) 0 = output, 1 = input
199     // 0 1 1 1 . 1 1 1
200     // PA7 PA6 PA5 PA4 . PA3 PA2 PA1 PA0
201     GPIO_PORTA_DEN_R = 0xFF; // 7) enable digital pins PB7-PB0
202     // ***** a. Unused Registers *****
203     // GPIO_PORTB_LOCK_R = 0x4C4F434B; // 2) unlock PortA
204     // GPIO_PORTB_CR_R = 0xFF; // allow changes to PA7-PA0
205     // GPIO_PORTB_AMSEL_R = 0x00; // 3) disable analog function
206     // GPIO_PORTB_PCTL_R = 0x00000000; // 4) GPIO clear bit PCTL
207     // GPIO_PORTB_AFSEL_R |= 0xC0; // 6) PA6 use alternate function
208     // GPIO_PORTB_PUR_R = 0x00; // disable pullup resistor
209     // GPIO_PORTB_DEN_R = 0xFF; // 7) enable digital pins PA7-PA0
210 }

```

Figure 8: Port init functions

```

211 // -----
212 // ***** 5. SysTick/PLL Functions *****
213 // -----
214 // Initialize SysTick with busy wait running at bus clock.
215 void SysTick_Init(void){
216     NVIC_ST_CTRL_R = 0; // disable SysTick during setup
217     NVIC_ST_RELOAD_R = NVIC_ST_RELOAD_M; // maximum reload value
218     NVIC_ST_CURRENT_R = 0; // any write to current clears it
219     // enable SysTick with core clock
220     NVIC_ST_CTRL_R = NVIC_ST_CTRL_ENABLE+NVIC_ST_CTRL_CLK_SRC;
221 }
222
223 // Time delay using busy wait.
224 // The delay parameter is in units of the core clock. (units of 20 nsec for 50 MHz clock)
225 void SysTick_Wait(unsigned long delay){
226     volatile unsigned long elapsedTime;
227     unsigned long startTime = NVIC_ST_CURRENT_R;
228     do{
229         elapsedTime = (startTime-NVIC_ST_CURRENT_R)&0x00FFFFFF;
230     }
231     while(elapsedTime <= delay);
232 }
233
234 // Time delay using busy wait.
235 // This assumes 50 MHz system clock.
236 void SysTick_Wait10us(unsigned long delay){
237     unsigned long i;
238     for(i=0; i<delay; i++){
239         SysTick_Wait(750); // wait 10us (assumes 50 MHz clock)
240     }
241 }
242
243 // Time delay using busy wait.
244 // This assumes 50 MHz system clock.
245 void SysTick_Wait1ms(unsigned long delay){
246     unsigned long i;
247     for(i=0; i<delay; i++){
248         SysTick_Wait(80000); // wait 1ms (assumes 50 MHz clock)
249     }
250 }
251
252 void PLL_INIT(void){
253     SYSTCL_RCC2_R |= 0x80000000; // 0) Use RCC2, USERCC2
254     SYSTCL_RCC2_R |= 0x00000800; // 1) bypass PLL while initializing, BYPASS2, PLL bypass
255     SYSTCL_RCC2_R = (SYSTCL_RCC2_R &~0x000007C0) // 2) select the crystal value and oscillator source
256     // clear XTAL field, bits 10-6
257     + 0x00000540; // 10101, configure for 16 MHz crystal
258     SYSTCL_RCC2_R &= ~0x00000070; // configure for main oscillator source
259     SYSTCL_RCC2_R &= ~0x00002000; // 3) activate PLL by clearing PWRDN
260     SYSTCL_RCC2_R |= 0x40000000; // 4) set the desired system divider, use 400 MHz PLL
261     SYSTCL_RCC2_R = (SYSTCL_RCC2_R &~ 0x1FC00000) // clear system clock divider
262     + (4<<22); // configure for 80 MHz clock
263     while((SYSTCL_RIS_R&0x00000040)==0){}; // 5) wait for the PLL to lock by polling PLLLRIS, wait for PLLRIS bit
264     SYSTCL_RCC2_R &= ~0x00000800; // 6) enable use of PLL by clearing BYPASS
265 }

```

Figure 9: SysTick and PLL functions

```

266 // -----
267 // ***** 6. Timer/Interrupts *****
268 // -----
269 // Microsecond delay using one-shot mode and prescaler
270 void Timer0A_DelayMicroSec(int time){
271     // int ms = 4000; // Millisecond
272     int us = 4; // Microsecond
273     SYSTCL_RCGCTIMER_R |= 2; // 1. Enable clock to Timer Block 0
274     TIMER1_CTL_R = 0; // 2. Disable Timer before initialization
275     TIMER1_CFG_R = 0x04; // 3. 16-bit option
276     TIMER1_TAMR_R = 0x01; // 4. One-shot mode and down counter
277     TIMER1_TAILR_R = us * time - 1; // 5. Timer A interval load value register
278     TIMER1_TAPR_R = 4 - 1; // Timer A prescaler 16MHz/4 = 4MHz
279     TIMER1_ICR_R = 0x1; // 6. Clear the TimerA timeout flag
280     TIMER1_CTL_R |= 0x01; // 7. Enable Timer A after initialization
281     while((TIMER1_RIS_R & 0x1)==0); // 8. Wait for TimerA timeout flag to set
282 }
283
284 // Initialize Timer0A in edge-time mode to capture rising edges.
285 // Input pin of Timer0A is PB6.
286 void Timer0Capture_Init(void){
287     SYSTCL_RCGCTIMER_R |= 1; // 1. Enable clock to Timer Block 0
288     SYSTCL_RCGC2_R |= 2; // 2. Enable clock to PortB
289     GPIO_PORTB_DIR_R &= ~0x40; // 3. Make PB6 an input pin
290     GPIO_PORTB_DEN_R |= 0x40; // 4. Make PB6 as digital pin
291     GPIO_PORTB_AFSEL_R |= 0x40; // 5. Use PB6 alternate function
292     GPIO_PORTB_PCTL_R &= ~0x0F000000; // 6. Configure PB6 for T0CCP0
293     GPIO_PORTB_PCTL_R |= 0x07000000;
294     TIMER0_CTL_R &= ~1; // 7. Disable Timer0A during setup
295     TIMER0_CFG_R = 4; // 8. 16-bit timer mode
296     TIMER0_TAMR_R = 0x17; // 9. Up-count, edge-time, capture mode
297     TIMER0_CTL_R |= 0x0C; // 10. Capture either edge
298     TIMER0_CTL_R |= 1; // 11. Enable Timer0A
299 }
300
301 // Captures two consecutive rising edges of a periodic signal from Timer Block 0
302 // Timer A and returns the time difference
303 int Timer0A_periodCapture(void){
304     // capture the first rising edge
305     int lastEdge, thisEdge;
306     TIMER0_ICR_R = 4; // Clear Timer0A capture flag
307     while((TIMER0_RIS_R & 4) == 0); // Wait till capture
308     lastEdge = TIMER0_TAR_R; // Save the timestamp
309
310     // capture the second rising edge
311     TIMER0_ICR_R = 4; // Clear Timer0A capture flag
312     while((TIMER0_RIS_R & 4) == 0); // Wait till capture
313     thisEdge = TIMER0_TAR_R; // Save the timestamp
314
315     return(thisEdge - lastEdge) & 0x00FFFFFF; // Return the time difference
316 }

```

Figure 10: Timer and Interrupts functions (UNUSED)

```

317 // -----
318 // ***** 7. Pulse Width Modulation *****
319 // -----
320 void PWM0B_Init(unsigned int period, unsigned int duty){ // Output on PB7/MOPWM1
321     volatile unsigned long delay;
322     SYSTCL_RCGC0_R |= 0x00100000; // 1) Activate PWM0
323     SYSTCL_RCGCGPIO_R |= 0x02; // 2) Activate port B
324     delay = SYSTCL_RCGCGPIO_R; // Allow time to finish activating
325     GPIO_PORTB_AFSEL_R |= 0x80; // 3) Enable alt funtion on PB7
326     GPIO_PORTB_PCTL_R &= ~0xF0000000; // 4) Configure PB7 as MOPWM1
327     GPIO_PORTB_PCTL_R |= 0x40000000;
328     GPIO_PORTB_AMSEL_R &= ~0x80; // Disable analog funtionality on PB7
329     GPIO_PORTB_DEN_R |= 0x80; // Enable digital I/O on PB7
330     SYSTCL_RCC_R &= ~0x000E0000; // 5) Clearing first
331     SYSTCL_RCC_R |= 0x00080000; // The writing in the 19:17 with PWMDIV 0x3 configure for /32 divider
332     SYSTCL_RCC_R |= 0x00100000; // Use PWM divider
333     PWM0_O_CTL_R = 0x00; // 6) Re-loading down-counting mode
334     PWM0_O_GENB_R = 0x000000C0|0x00000008; // 0xC8 in lesson // PB7 goes low on LOAD, PB7 goes high on CMPB down
335     PWM0_O_LOAD_R = period - 1; // 7) Cycles needed to count down to 0
336     PWM0_O_CMPB_R = duty - 1; // 9) Count value when output rises
337     PWM0_O_CTL_R |= 0x00000001; // 10) Start PWM0
338     PWM0_ENABLE_R |= 0x00000002; // 11) Enable PB7/MOPWM1
339 }
340 // -----
341 // ***** 8. Main Program Functions *****
342 // -----
343 // Gets the pulse width by waiting on the L2H/H2L transitions of the input port.
344 // Returns the time it takes for the entire L2H/H2L transtition.
345 unsigned int PulseWidth(void){
346     while((GPIO_PORTA_DATA_R&0x80)==0); // Search for high transition
347     while((GPIO_PORTA_DATA_R&0x80)!=0x80){ // Search for low transition
348         SysTick_Wait(3);
349         echo_time++; // Count echo_time
350     }
351     return echo_time;
352 }
353
354 // Sets the right output for the FSM using the distance from the sensor.
355 // Takes in the Distance and returns the state the FSM needs to be.
356 unsigned int Distance_Input(unsigned int Distance){
357     if((Distance > 400) || (Distance < 1)){ // Red LED
358         return 0;
359     }
360     else if(Distance <= 50){ // Yellow LED
361         return 1;
362     }
363     else if(Distance <= 75){ // Green LED
364         return 2;
365     }
366     else if(Distance > 75){ // White LED
367         return 3;
368     }
369     return 0;
370 }
371

```

Figure 11: Pulse Width Modulation and other functions used in the main program