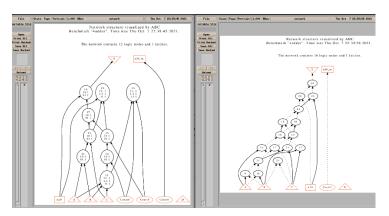
LSV Hw1

1.(b)

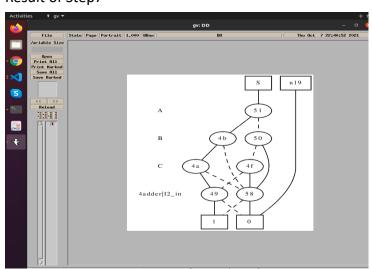
```
Activities Terminal * + 7 22:41

tryugtryu-VivoBook-ASUSLaptop-X435EA-5435EA:-5 cd Desktop
tryugtryu-VivoBook-ASUSLaptop-X435EA-5435EA:-6 cd Desktop
tryugtryu-VivoBook-ASUSLaptop-X435EA-5435EA:-9 ceaktop5 cd LSV/LSV-PA
tryugtryu-VivoBook-ASUSLaptop-X435EA-5435EA:-9 ceaktop5
abc c9 - seaktop6 control tryugtryu-VivoBook-ASUSLaptop-X435EA-5435EA:-9 ceaktop5 cd LSV/LSV-PA
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abc c9 - seaktop6 control tryugtryu-VivoBook-ASUSLaptop-X435EA-5435EA:-9
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abc c9 - seaktop6 cander control tryugtryu-VivoBook-ASUSLaptop-X435EA:-9
tryugtryu-VivoBook-ASUSLaptop-X435EA:-9 cd LSV/LSV-PA
tryugtryu-VivoBook-ASUSLaptop-X435EA:-9 cd LSV/LSV-PA
abc c9 - seaktop6 cander control tryugtryu-VivoBook-ASUSLaptop-X435EA:-9 cd LSV/LSV-PA
abc c9 - seaktop6 control tryugtryu-VivoBook-ASUSLaptop7
abc c9 - seaktop6 control tryugtryu-VivoBook-ASUSLa
```

Result of Step3,5



Result of Step7

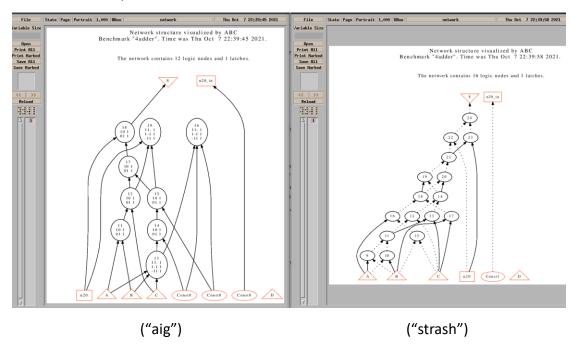


2(a).

(1).

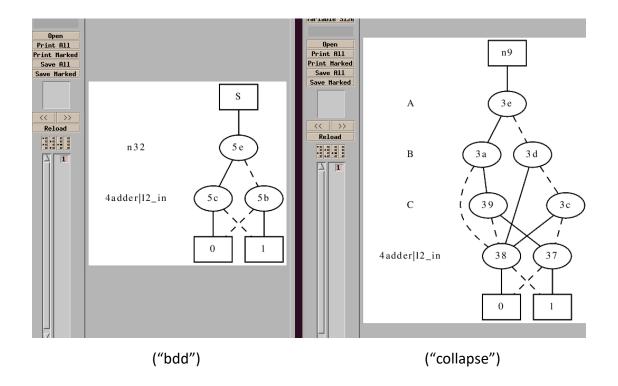
Command "aig": Unsorted. Converts local functions of the nodes to AIGs **Command "strash"**: Transforms the current network into an AIG by one-level structural hashing. The resulting AIG is a logic network composed of two-input AND gates and inverters represented as complemented attributes on the edges.

Structural hashing is a purely combinational transformation, which does not modify the number and positions of latches.



(2).

Command "bdd": Unsorted. Converts local functions of the nodes to BDDs. **Command "collapse":** Recursively composes the fanin nodes into the fanout nodes resulting in a network, in which each CO is produced by a node, whose fanins are Cls. Collapsing is performed by building global functions using BDDs and is, therefore, limited to relatively small circuits. After collapsing, the node functions are represented using BDDs.



2(b).

By using command "logic", we can convert a structurally hashed AIG to a logic network with node function expressed in SOP.