

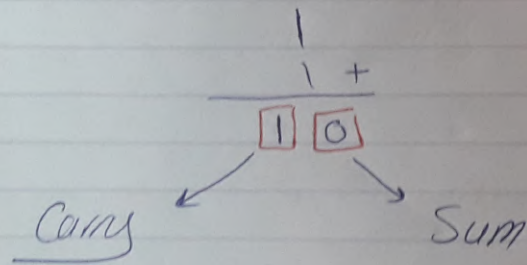
## Binary Adders

It is a Combinational logic Gates that use to add two Binary numbers.

Ex:

$$\begin{array}{r} X = 0101 \\ y = 1010 \\ \hline 1111 \end{array}$$

$$\begin{array}{r} 0 \quad 0 \quad 1 \quad 1 \\ 0+ \quad 1+ \quad 1+ \quad 0+ \\ \hline 0 \quad 1 \quad 10 \quad 1 \end{array}$$



## Adders

Half Adder

Full Adder

### 1. Half Adder:

\* Truth Table:

A	B	S	C
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

(Sum)  $S = A'B + AB' = A \oplus B$

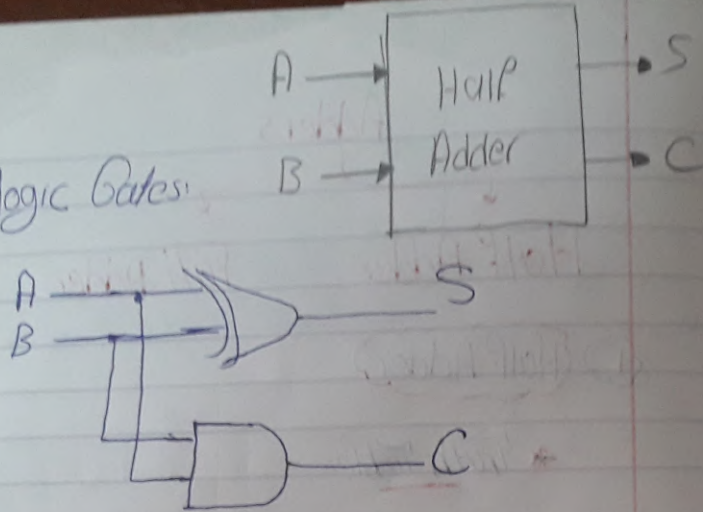
(Carry)  $C = A \cdot B$

\* Boolean Expression:

$$S = A \oplus B \quad C = AB$$



★ Logic Gates:



### Half adder

↳ Combinational logic Gates That Add Two Binary Numbers (Two Digits) & produce two output Sum & Carry

X = Two Bits →  
 ⊕ Y = Two Bits →

Z = Two Bits

Carry ↙ Sum ↘

### ★ Full Adder

↳ Combinational logic Gates that Add Two Binary numbers (Two Bits) <sup>in (1 Bit)</sup>

X = Two Bits

⊕ Y = Two Bits

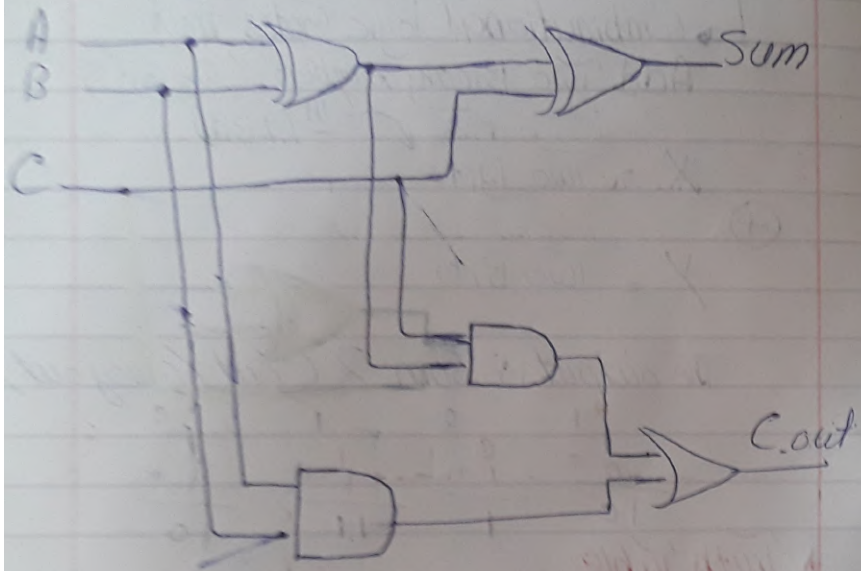
The output is Sum & Cout (carry out)

$$\begin{array}{r} 0^1 + 0^0 = 0 \\ 0^1 + 1^0 = 1 \\ 1^1 + 0^0 = 1 \\ 1^1 + 1^0 = 10 \end{array}$$

### ★ Truth Table:

A	B	C <sub>in</sub>	S	C <sub>out</sub>
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

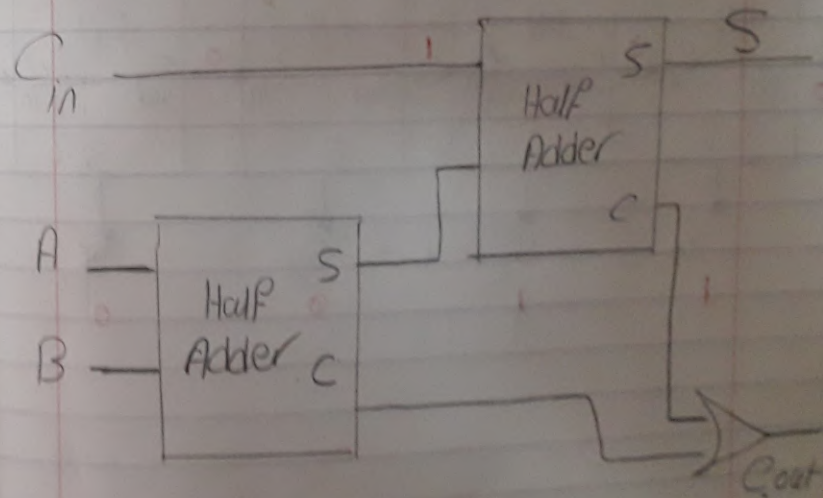
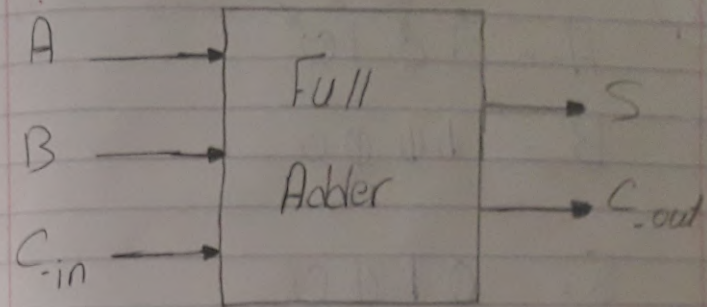
\*logic Gates:



Boolean Expression:

$$S = (A \oplus B) \oplus C_{in}$$

$$C_{out} = SC_{in} + AB$$





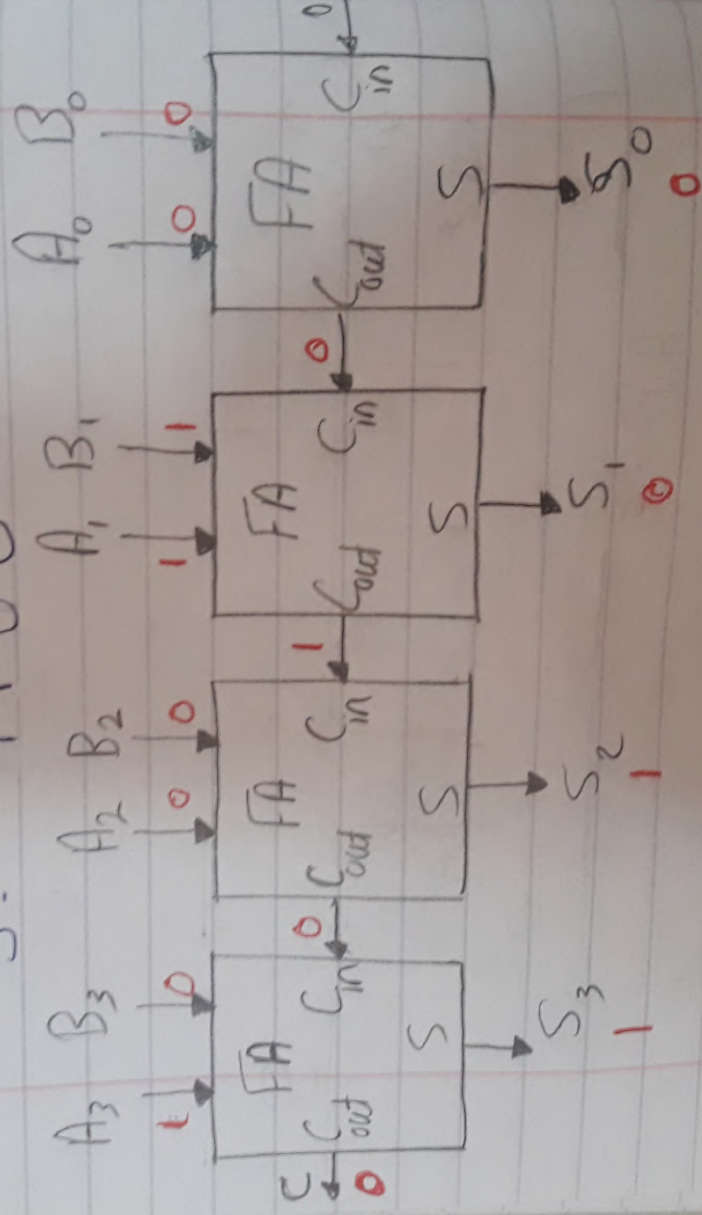
# 4. Bit Binary Adder

Ex

A = 1010

B = 0010

S = 1100



# QUESTIONS



4.11 Using Four Half Adder.

a) Design a four Bit Combinational Circuit Incrementer (a circuit that adds 1 to a four binary number)

~~b) Design a four Bit Combinational Circuit decrementer (a circuit that subtracts 1 from a four bit binary number).~~

Ex.

$$\begin{array}{r} A = 1010 \\ \quad \quad \quad 1 + \\ \hline 1011 \end{array}$$

