y(k.Ti) = x ((mk Mk). Ts) data -ADC clock interpolator incoming Band ! Clock 2+ samples/symbol Limited Data Mk determines the time delay Tk = Mk · Ts resolution of the "fractional time axis. delay " (K+3) Ti (K+1)Ti (k+2) Ti τ_{krs} > Tkizk (mk-1+1)Ts means "extra" sample (\$5,5) $\times (mTs)$ (Duration)
L'sample from ADC Rodo