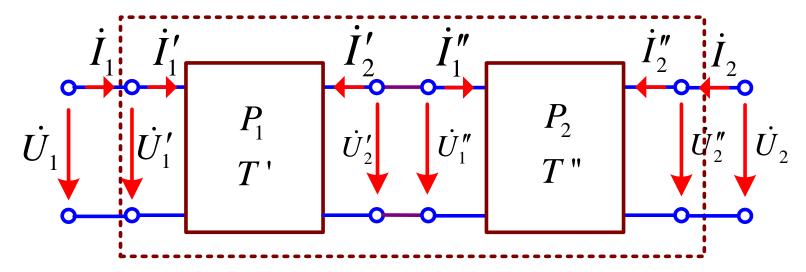
16.5 二端口的连接



一个复杂二端口网络可以看作是由若干简单的二端口按某种方式联接而成,使电路分析得到简化;

1. 级联(链联)



T=TT"

$$\mathbf{T}' = \begin{bmatrix} A' & B' \\ C' & D' \end{bmatrix} \qquad [\mathbf{T}''] = \begin{bmatrix} A'' & B'' \\ C'' & D'' \end{bmatrix}$$

$$[\mathbf{T}''] = \begin{bmatrix} A'' & B'' \\ C'' & D'' \end{bmatrix}$$

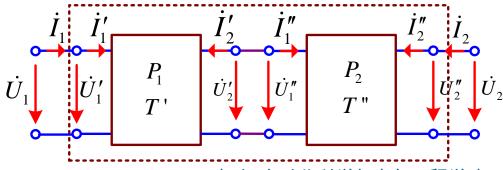


$$\begin{bmatrix}
\dot{U}_1' \\
\dot{I}_1'
\end{bmatrix} = \begin{bmatrix}
A' & B' \\
C' & D'
\end{bmatrix} \begin{bmatrix}
\dot{U}_2' \\
-\dot{I}_2'
\end{bmatrix} \begin{bmatrix}
\dot{U}_1'' \\
\dot{I}_1''
\end{bmatrix} = \begin{bmatrix}
A'' & B'' \\
C'' & D''
\end{bmatrix} \begin{bmatrix}
\dot{U}_2'' \\
-\dot{I}_2''
\end{bmatrix}$$

级联后
$$\begin{bmatrix} \dot{U}_1 \\ \dot{I}_1 \end{bmatrix} = \begin{bmatrix} \dot{U}_1' \\ \dot{I}_1' \end{bmatrix} \quad \begin{bmatrix} \dot{U}_2' \\ -\dot{I}_2' \end{bmatrix} = \begin{bmatrix} \dot{U}_1'' \\ \dot{I}_1'' \end{bmatrix} \quad \begin{bmatrix} \dot{U}_2'' \\ -\dot{I}_2'' \end{bmatrix} = \begin{bmatrix} \dot{U}_2'' \\ -\dot{I}_2' \end{bmatrix}$$

$$\begin{bmatrix} \dot{U}_1 \\ \dot{I}_1 \end{bmatrix} = \begin{bmatrix} \dot{U}_1' \\ \dot{I}_1' \end{bmatrix} = \begin{bmatrix} A' & B' \\ C' & D' \end{bmatrix} \begin{bmatrix} \dot{U}_2' \\ -\dot{I}_2' \end{bmatrix}$$

$$= \begin{bmatrix} A' & B' \\ C' & D' \end{bmatrix} \begin{bmatrix} A'' & B'' \\ C'' & D'' \end{bmatrix} \begin{bmatrix} \dot{U}_2 \\ -\dot{I}_2 \end{bmatrix} = \begin{bmatrix} A & B \\ C & B \end{bmatrix} \begin{bmatrix} \dot{U}_2 \\ -\dot{I}_2 \end{bmatrix}$$



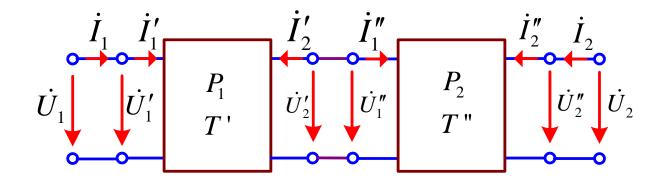
$$\boxed{ \begin{bmatrix} A & B \\ C & D \end{bmatrix} = \begin{bmatrix} A' & B' \\ C' & D' \end{bmatrix} \begin{bmatrix} A'' & B'' \\ C'' & D'' \end{bmatrix} }$$



$$= \begin{bmatrix} A'A'' + B'C'' & A'B'' + B'D'' \\ C'A'' + D'C'' & C'B'' + D'D'' \end{bmatrix}$$

 $\mathbb{E}[\mathbf{T}] = [\mathbf{T}'][\mathbf{T}'']$

结论 级联后所得复合二端口T 参数矩阵等于级联的二端口T 参数矩阵相乘。 可推广到n个二端口级联的关系。



注意



(1) 级联时 *T* 参数是矩阵相乘的关系,不是对应元素相乘。

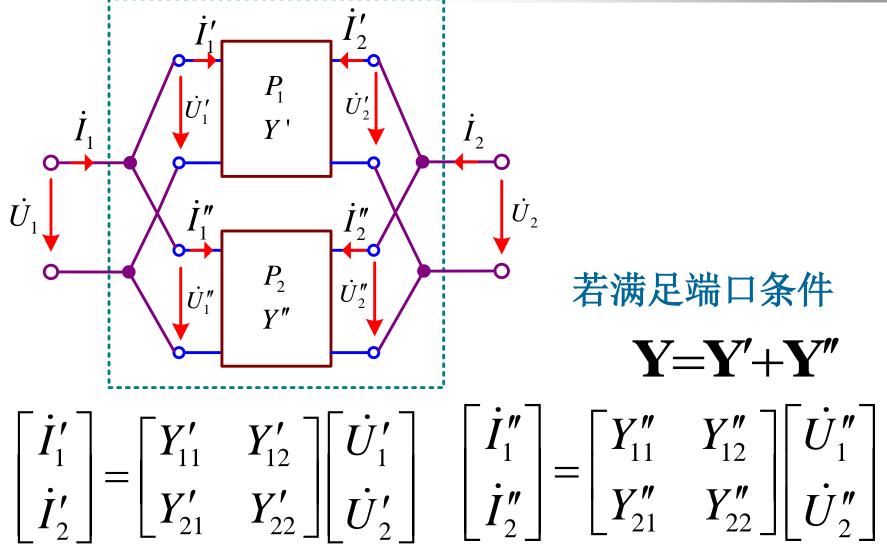
$$\begin{bmatrix} A & B \\ C & D \end{bmatrix} = \begin{bmatrix} A' & B' \\ C' & D' \end{bmatrix} \begin{bmatrix} A'' & B'' \\ C'' & D'' \end{bmatrix}$$
$$= \begin{bmatrix} A'A'' + B'C'' & A'B'' + B'D'' \\ C'A'' + D'C'' & C'B'' + D'D'' \end{bmatrix}$$

显然 $A = A'A'' + B'C'' \neq A'A''$

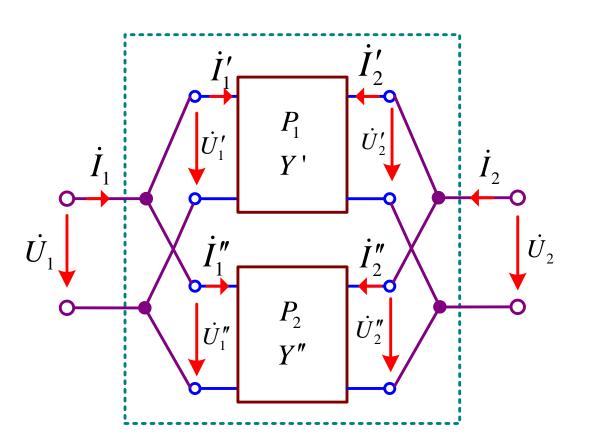
(2) 级联时各二端口的端口条件不会被破坏。

2. 并联 并联采用 / 参数方便。









并联后

$$\begin{bmatrix} \dot{U}_1 \\ \dot{U}_2 \end{bmatrix} = \begin{bmatrix} \dot{U}_1' \\ \dot{U}_2' \end{bmatrix} = \begin{bmatrix} \dot{U}_1'' \\ \dot{U}_2'' \end{bmatrix}$$

$$\begin{bmatrix} \dot{I}_1 \\ \dot{I}_2 \end{bmatrix} = \begin{bmatrix} \dot{I}_1' \\ \dot{I}_2' \end{bmatrix} + \begin{bmatrix} \dot{I}_1'' \\ \dot{I}_2'' \end{bmatrix}$$

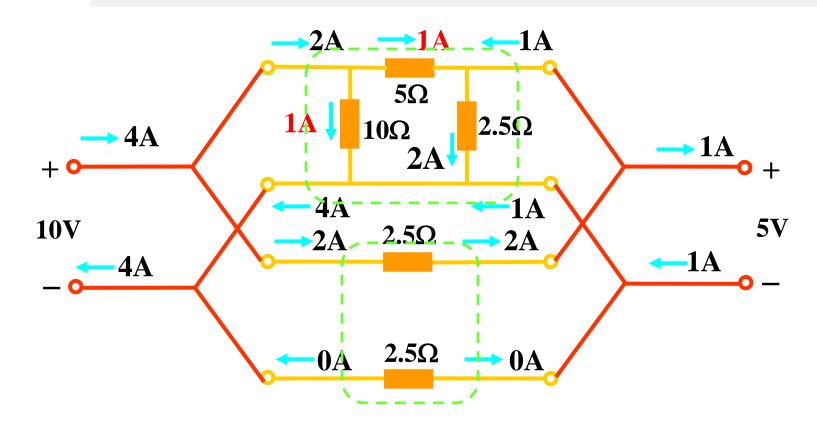
$$\begin{bmatrix} \dot{I}_{1} \\ \dot{I}_{2} \end{bmatrix} = \begin{bmatrix} \dot{I}'_{1} \\ \dot{I}'_{2} \end{bmatrix} + \begin{bmatrix} \dot{I}''_{1} \\ \dot{I}''_{2} \end{bmatrix} = \begin{bmatrix} Y'_{11} & Y'_{12} \\ Y'_{21} & Y'_{22} \end{bmatrix} \begin{bmatrix} \dot{U}'_{1} \\ \dot{U}'_{2} \end{bmatrix} + \begin{bmatrix} Y''_{11} & Y''_{12} \\ Y''_{21} & Y''_{22} \end{bmatrix} \begin{bmatrix} \dot{U}''_{1} \\ \dot{U}''_{2} \end{bmatrix}$$

$$= \left\{ \begin{bmatrix} Y'_{11} & Y'_{12} \\ Y'_{21} & Y'_{22} \end{bmatrix} + \begin{bmatrix} Y''_{11} & Y''_{12} \\ Y''_{21} & Y''_{22} \end{bmatrix} \right\} \begin{bmatrix} \dot{U}_1 \\ \dot{U}_2 \end{bmatrix}$$

$$= \begin{bmatrix} Y'_{11} + Y''_{11} & Y'_{12} + Y''_{12} \\ Y'_{21} + Y''_{21} & Y'_{22} + Y''_{22} \end{bmatrix} \begin{bmatrix} \dot{U}_1 \\ \dot{U}_2 \end{bmatrix} = \begin{bmatrix} \mathbf{Y} \end{bmatrix} \begin{bmatrix} \dot{U}_1 \\ \dot{U}_2 \end{bmatrix}$$

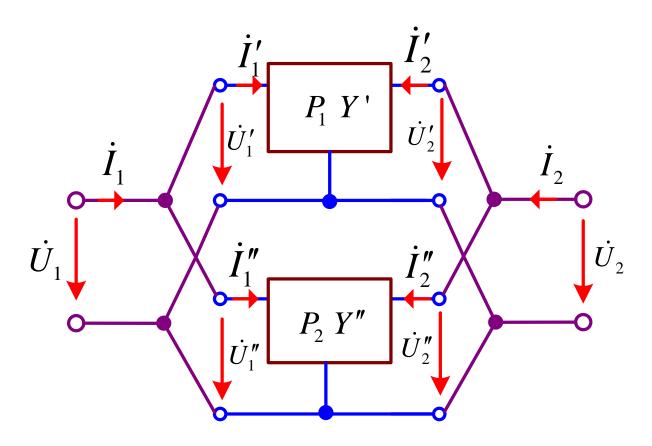
二端口并联所得复合二端口的Y 参数矩 阵等于两个二端口Y参数矩阵相加。

注意: (1) 两个二端口并联时,其端口条件可能被 破坏此时上述关系式就不成立。

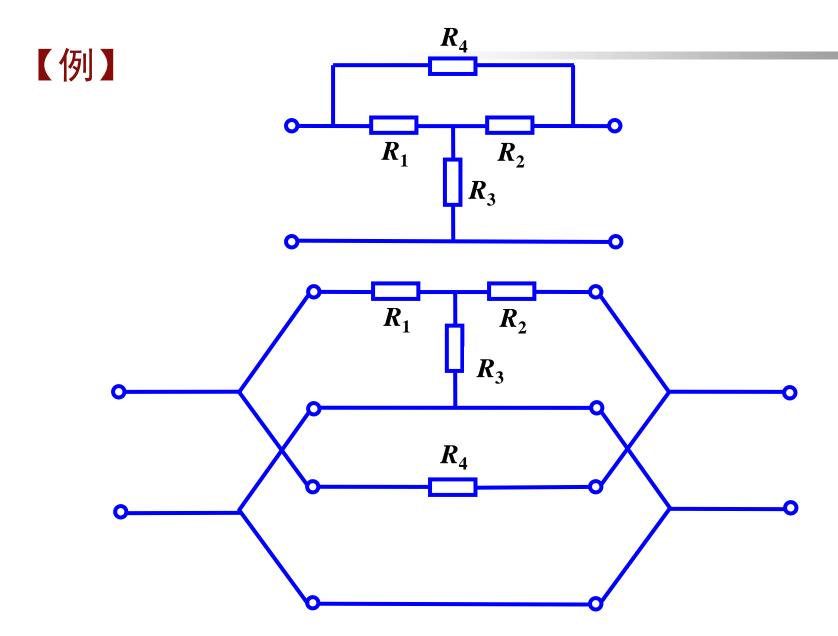


并联后端口条件破坏。

(2) 具有公共端的二端口(三端网络形成的二端口),将公共端并在一起将不会破坏端口条件。

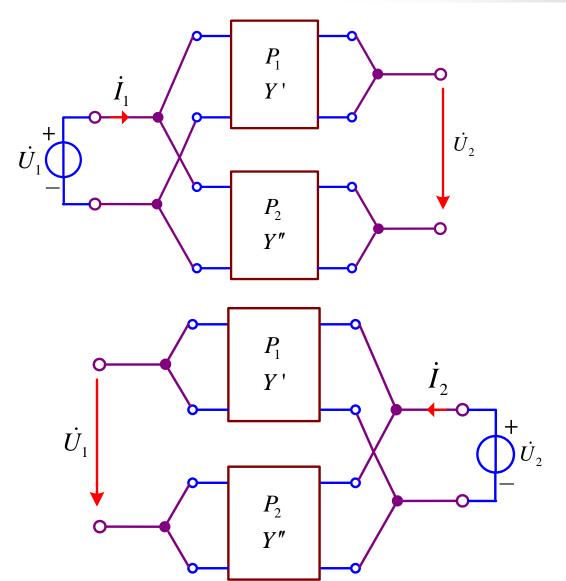






(3) 检查是否满足并联端口条件的方法:



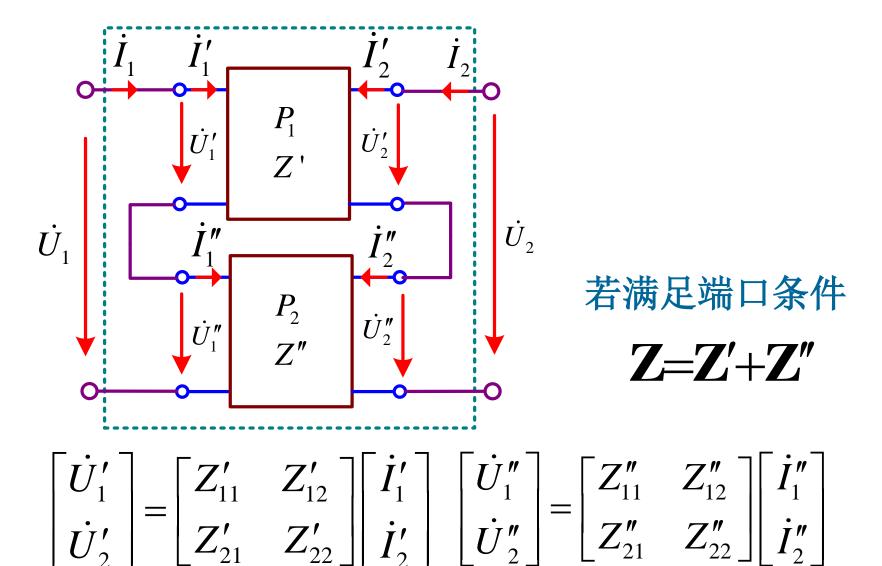


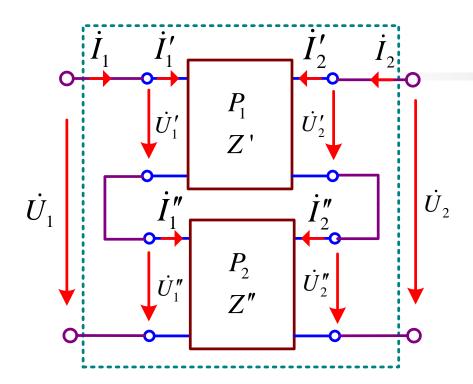
 \dot{U}_{2} ?=0 输入端是否满 足端口条件。

 U_1 ?=0输出端是否满足端口条件。

3. 串联 采用 Z 参数方便。







$$\begin{bmatrix} \dot{I}_1 \\ \dot{I}_2 \end{bmatrix} = \begin{bmatrix} \dot{I}_1' \\ \dot{I}_2' \end{bmatrix} = \begin{bmatrix} \dot{I}_1'' \\ \dot{I}_2'' \end{bmatrix}$$

$$\begin{bmatrix} \dot{U}_1 \\ \dot{U}_2 \end{bmatrix} = \begin{bmatrix} \dot{U}_1' \\ \dot{U}_2' \end{bmatrix} + \begin{bmatrix} \dot{U}_1'' \\ \dot{U}_2'' \end{bmatrix}$$

$$\begin{bmatrix} \dot{U}_1 \\ \dot{U}_2 \end{bmatrix} = \begin{bmatrix} \dot{U}_1' \\ \dot{U}_2' \end{bmatrix} + \begin{bmatrix} \dot{U}_1'' \\ \dot{U}_2'' \end{bmatrix} = \begin{bmatrix} Z' \end{bmatrix} \begin{bmatrix} \dot{I}_1' \\ \dot{I}_2' \end{bmatrix} + \begin{bmatrix} Z'' \end{bmatrix} \begin{bmatrix} \dot{I}_1'' \\ \dot{I}_2' \end{bmatrix}$$

$$=\{[Z']+[Z'']\}\begin{bmatrix} \dot{I}_1\\ \dot{I}_2\end{bmatrix}=[\mathbf{Z}]\begin{bmatrix} \dot{I}_1\\ \dot{I}_2\end{bmatrix}$$



$$[Z] = [Z'] + [Z'']$$

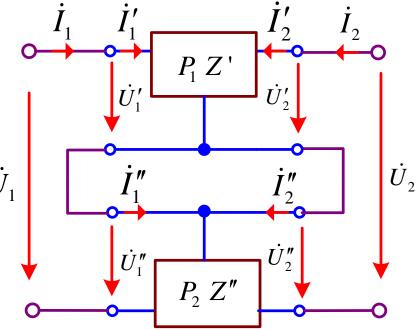
结论: 串联后复合二端口Z 参数矩阵等于原二端口

Z参数矩阵相加。

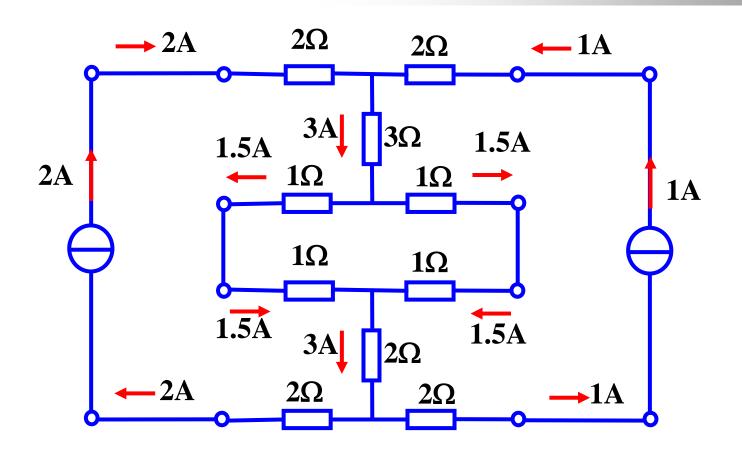
可推广到n端口串联。

注意: (1) 串联后端口条件可能被破坏。需检查端口条件。 $i_1 \quad i_2 \quad \dots \quad i_n \quad$

(2) 具有公共端的二端口,将公共端串联时将不会破坏端口条件。

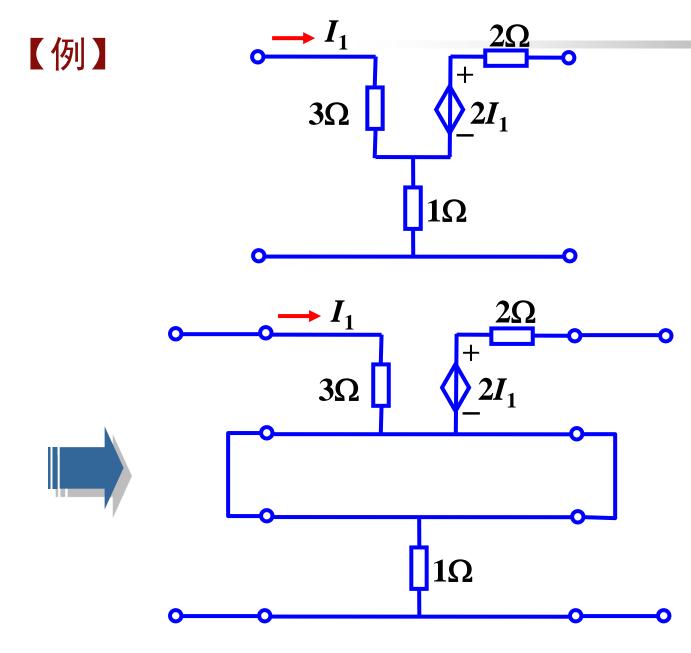






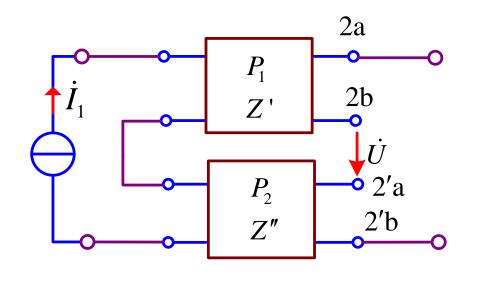
端口条件破坏!



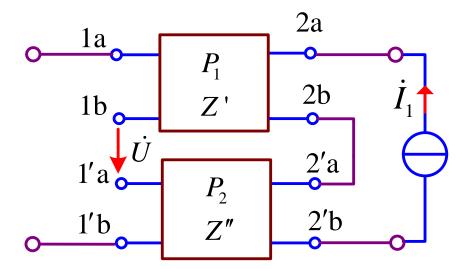








 \dot{U} ?=0输入端是否满 足端口条件。



U?=0输出端是否满足端口条件。

【例】

已知
$$\mathbf{Y}' = \begin{pmatrix} Y'_{11} & Y'_{12} \\ Y'_{21} & Y'_{22} \end{pmatrix}$$
求Y矩阵。

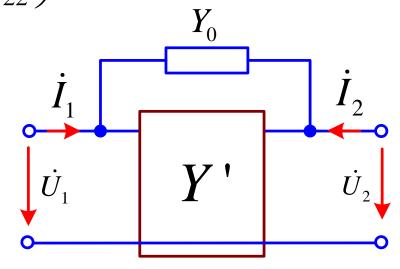


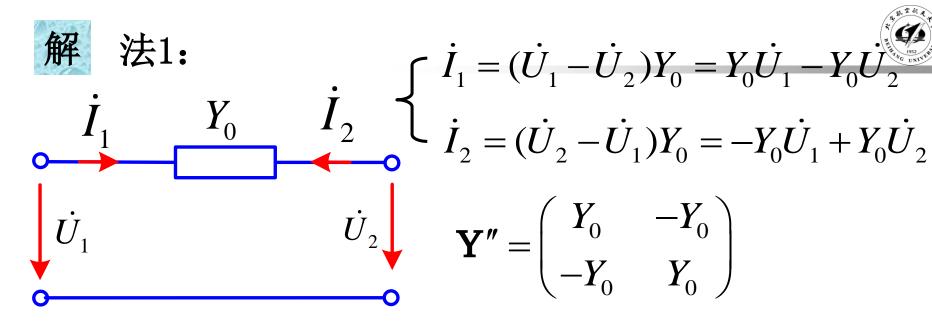
解

法1:

二端口并联 具有公共端,公共端并在一 起,不会破坏端口条件。

$$[\mathbf{Y}] = [\mathbf{Y}'] + [\mathbf{Y}'']$$





$$\mathbf{Y} = \mathbf{Y'} + \mathbf{Y''} = \begin{pmatrix} Y_{11}' + Y_0 & Y_{12}' - Y_0 \\ Y_{21} - Y_0 & Y_{22}' + Y_0 \end{pmatrix}$$

法2:
$$\begin{cases} \dot{I}'_1 = Y'_{11}\dot{U}_1 + Y'_{12}\dot{U}_2 \\ \dot{I}'_2 = Y'_{21}\dot{U}_1 + Y'_{22}\dot{U}_2 \\ \dot{I}_1 = \dot{I}'_1 + (\dot{U}_1 - \dot{U}_2)Y_0 \\ \dot{I}_2 = \dot{I}'_2 + (\dot{U}_2 - \dot{U}_1)Y_0 \end{cases} \qquad \qquad Y$$

$$\begin{cases} \dot{I}_1 = Y'_{11}\dot{U}_1 + Y'_{12}\dot{U}_2 + Y_0\dot{U}_1 - Y_0\dot{U}_2 \\ \dot{I}_2 = Y'_{21}\dot{U}_1 + Y'_{22}\dot{U}_2 - Y_0\dot{U}_1 + Y_0\dot{U}_2 \end{cases}$$

 $\begin{cases} \dot{I}_{1} = (Y'_{11} + Y_{0})\dot{U}_{1} + (Y'_{12} - Y_{0})\dot{U}_{2} \\ \dot{I}_{2} = (Y'_{21} - Y_{0})\dot{U}_{1} + (Y'_{22} + Y_{0})\dot{U}_{2} \end{cases} \quad \mathbf{Y} = \begin{pmatrix} Y'_{11} + Y_{0} & Y'_{12} - Y_{0} \\ Y'_{21} - Y_{0} & Y'_{22} + Y_{0} \end{pmatrix}$

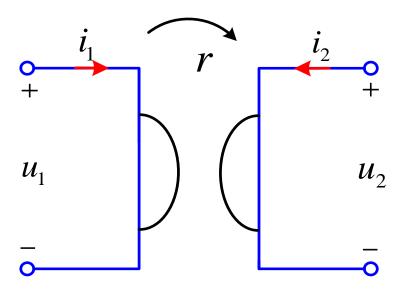
16.6 回转器和负阻抗变换器



1. 回转器

电路符号、参数、VCR、功能关系

电路符号:



方程:

$$u_1 = -ri_2 \quad \text{if } i_1 = gu_2$$

$$u_2 = ri_1 \qquad i_2 = -gu_1$$

r ——回转电阻 g ——回转电导

把一个端口的电流回转为另 一个端口的电压或逆过程

特点1: *不满足互易定理

$$u_1 = -ri_2$$

$$i_1 = gu_2$$



$$u_2 = ri_1$$

$$i_2 = -gu_1$$

$$\begin{bmatrix} u_1 \\ u_2 \end{bmatrix} = \begin{bmatrix} 0 & -r \\ r & o \end{bmatrix} \begin{bmatrix} i_1 \\ i_2 \end{bmatrix}$$

$$\begin{bmatrix} u_1 \\ u_2 \end{bmatrix} = \begin{bmatrix} 0 & -r \\ r & o \end{bmatrix} \begin{bmatrix} i_1 \\ i_2 \end{bmatrix} \quad \begin{bmatrix} i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} 0 & g \\ -g & 0 \end{bmatrix} \begin{bmatrix} u_1 \\ u_2 \end{bmatrix}$$

$$\mathbf{Z} = \begin{bmatrix} 0 & -r \\ r & 0 \end{bmatrix}$$

$$\mathbf{Y} = \begin{vmatrix} 0 & g \\ -g & 0 \end{vmatrix}$$

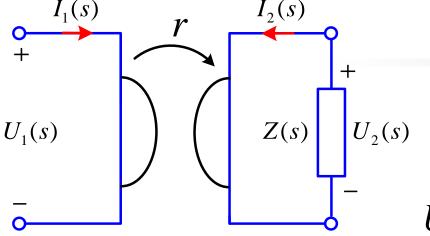
$$u_1 i_1 + u_2 i_2 = -r i_2 i_1 + r i_1 i_2 = 0$$

$$u_1 i_1 + u_2 i_2 = g u_2 u_1 - g u_2 u_1 = 0$$

特点2: 回转器是线性无源元件,不发出也不消耗功率。









$$Z_i(s) = r^2 \frac{1}{Z(s)}$$

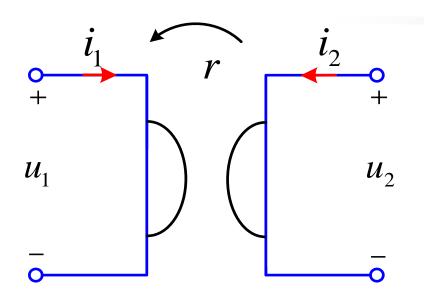
$$Z_{i}(s) = \frac{U_{1}(s)}{I_{1}(s)} = \frac{-rI_{2}(s)}{I_{1}(s)} = \frac{r\frac{U_{2}(s)}{Z(s)}}{I_{1}(s)} = \frac{\frac{r^{2}}{Z(s)}I_{1}(s)}{I_{1}(s)} = r^{2}\frac{1}{Z(s)}$$

若
$$Z(s) = \frac{1}{sC}$$
 $Z_i(s) = r^2 sC$ $L_e = r^2 C$ $Z(s) = sL$ $Z_i(s) = r^2 \frac{1}{sL}$ $C_e = \frac{L}{r^2}$

$$Z(s) = 0$$
(短路) $Z_i(s) = \infty$ (开路)

$$Z(s) = \infty$$
(开路) $Z_i(s) = 0$ (短路)





方程:

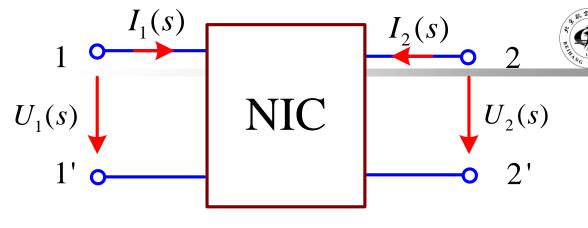
$$u_1 = ri_2$$
 或 $i_1 = -gu_2$
 $u_2 = -ri_1$ $i_2 = gu_1$



$$Z_i(s) = r^2 \frac{1}{Z(s)}$$

2. 负阻抗变换器





Negative Impedance Converter

电流反向型

$$U_1(s) = U_2(s)$$

$$I_1(s) = kI_2(s)$$

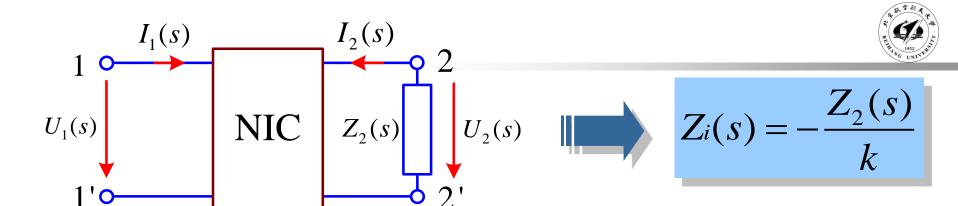
$$\begin{bmatrix} U_1(s) \\ I_1(s) \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & -k \end{bmatrix} \begin{bmatrix} U_2(s) \\ -I_2(s) \end{bmatrix}$$

电压反向型

$$U_1(s) = -kU_2(s)$$

$$I_1(s) = -I_2(s)$$

$$\begin{bmatrix} U_1(s) \\ I_1(s) \end{bmatrix} = \begin{bmatrix} -k & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} U_2(s) \\ -I_2(s) \end{bmatrix}$$



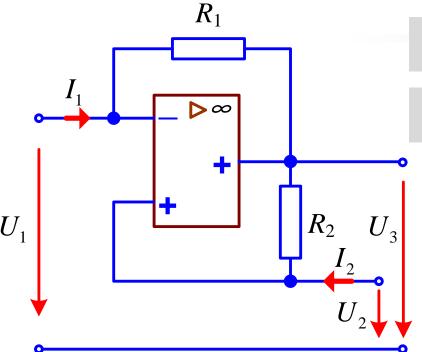
$$Z_{i}(s) = \frac{U_{1}(s)}{I_{1}(s)} = \frac{U_{2}(s)}{kI_{2}(s)} = -\frac{Z_{2}(s)}{k}$$

端口2-2′接
$$R$$
 端口1-1′为- $\frac{1}{k}R$

端口2-2'接
$$L$$
 端口1-1'为- $\frac{1}{k}$ L

端口
$$2-2'$$
接 C 端口 $1-1'$ 为 $-kC$





由虚短:
$$U_1 = U_2$$

由虚断:

$$=\frac{U_1-U_3}{R_1}$$

$$I_2 = \frac{U_2 - U_3}{R_2}$$

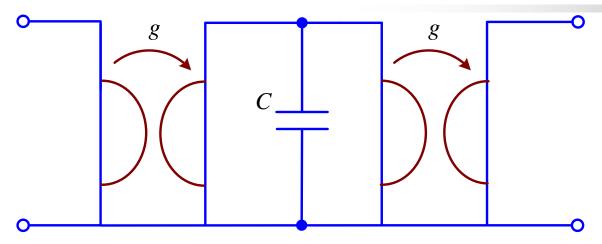
$$I_1 = \frac{R_2}{R_1} I_2$$

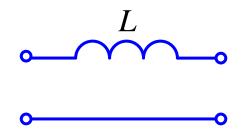
$$k = \frac{R_2}{R_1}$$

电流反向型负阻抗变换器

求两电路等效的条件。



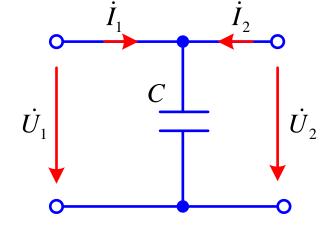




$$u_1 = -ri_2$$

$$u_2 = ri_1$$

$$\mathbf{T}_1 = \left(\begin{array}{cc} 0 & \frac{1}{g} \\ g & 0 \end{array} \right)$$



$$\dot{U}_1 = \dot{U}_2$$

$$\dot{U}_1 = \dot{U}_2$$
 \dot{U}_2
 $\dot{I}_1 = j\omega C\dot{U}_2 - \dot{I}_2$

$$\mathbf{T}_2 = \begin{pmatrix} 1 & 0 \\ j\omega C & 1 \end{pmatrix}$$

$$\mathbf{T} = \begin{pmatrix} 0 & \frac{1}{g} \\ g & 0 \end{pmatrix} \begin{pmatrix} 1 & 0 \\ j\omega C & 1 \end{pmatrix} \begin{pmatrix} 0 & \frac{1}{g} \\ g & 0 \end{pmatrix} = \begin{pmatrix} 1 & j\frac{\omega C}{g^2} \\ 0 & 1 \end{pmatrix}$$

$$\dot{I}_1$$
 \dot{U}_2
 \dot{U}_1
 \dot{U}_2

$$\dot{U}_1 = \dot{U}_2 - j\omega L \dot{I}_2$$

$$\dot{I}_1 = -\dot{I}_2$$

$$\mathbf{T'} = \begin{pmatrix} 1 & j\omega L \\ 0 & 1 \end{pmatrix}$$

$$L = \frac{C}{g^2}$$

回转器与负阻抗变换器



相同点 四端元件

有源元件组成

不同点

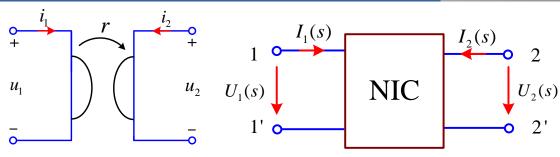
$$u_1 = -ri_2 \quad i_1 = gu_2$$

$$u_2 = ri_1 \qquad i_2 = -gu_1$$

阻抗的倒数

$$Z_i(s) = r^2 \frac{1}{Z(s)}$$

无源元件



$$U_1(s) = U_2(s)$$

$$I_1(s) = kI_2(s)$$

$$U_1(s) = -kU_2(s)$$

$$I_1(s) = -I_2(s)$$

阻抗为负数

$$Z_i(s) = -\frac{Z_2(s)}{k}$$

不是无源元件

作业



【T型并联】

[T1, T2]

【回转器】