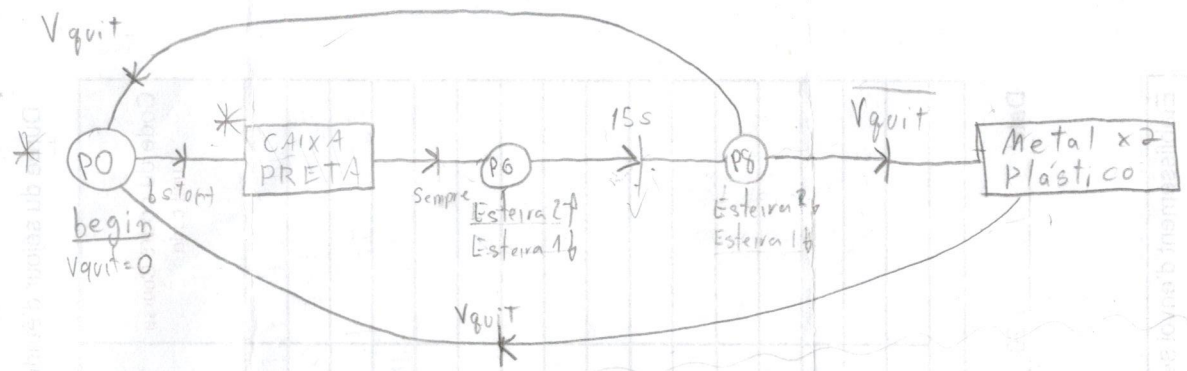
Malha geral



Esteira 1 para direito
2 " esquerd

*1 : ((blocos==0 + blocos==1).ind.cima)
D
(blocos==2).ind.cima)

$\overline{CAB + D\bar{A}\bar{B}} = \overline{(CAB) + D\bar{A}\bar{B}}$

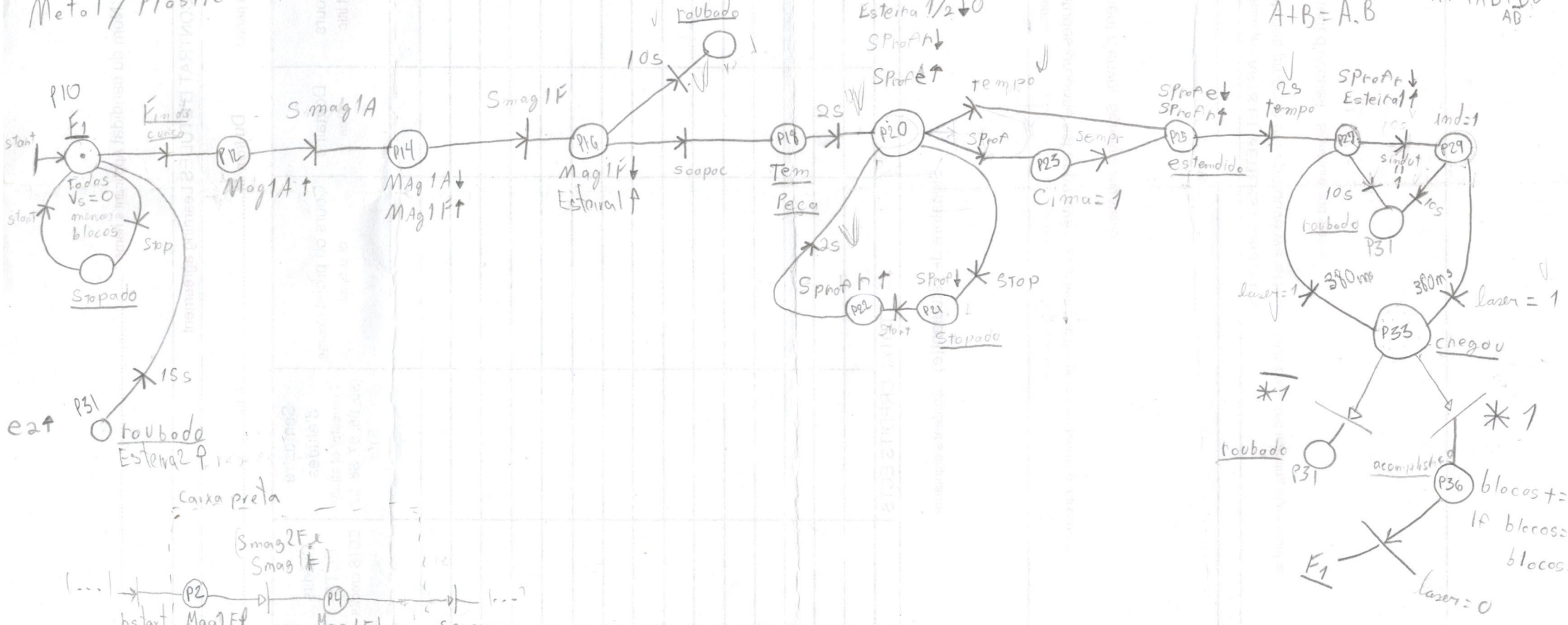
$d_1 = \overline{CAB} \cdot \overline{(D\bar{A}\bar{B})}$

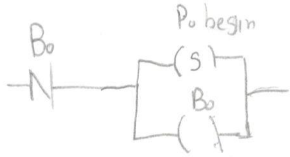
$(\bar{C} + \bar{A} + \bar{B}) \cdot (\bar{D} + A + B)$

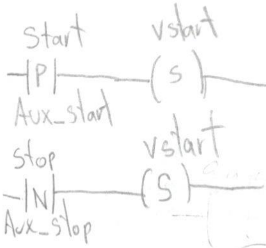
$\bar{A} + B = \bar{A} \cdot \bar{B}$

$\bar{C}\bar{D} + A\bar{C} + B\bar{C}$ ✓
$+ \bar{A}\bar{D} + \bar{A}B + \bar{B}\bar{D}$ ✓
AB

Metal/Plástico



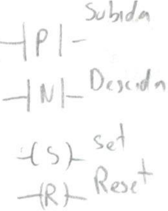




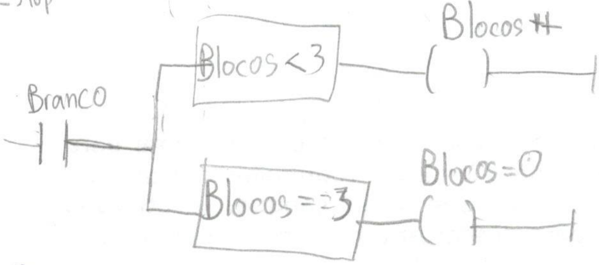
Metal Mag 1
Plástico Mag 2
Metal → Blocos < 2
Plástico → Blocos == 2
10 contadores.

Vquit no ladder

Branco S6



esteira 12s
FC 375ms
Cap/Pneum 1,7s



Falta
- Timers (Retentivos) → colocar vstop nos timers RES resetar os ACC quando vquit ou troca de estágio
- ~~Rever lógicas de blocos~~
- Alterar valor de blocos ✓
- Lógica dos stops e quits ✓ luzes

Sinais
~~Mag 1A~~
~~Mag 2A~~

√ Esteira 1↑ - (P16 + P18 + P27 + P29) $\overline{vstop}$ ✓
√ Esteira 1↓ - P6 + P31 + P8 + P10 + P0 + P33 + P37 + P20 + vstop (+375ms)
√ Esteira 2↑ - (P6 + P31) $\overline{vstop}$
√ Esteira 2↓ - vstop + P8 + P10 + P0 + P37
√ Prof E↑ - (P20) $\overline{vstop}$
√ Prof E↓ - P25 + P21 + vstop
√ Prof R↑ - (P22 + P25) $\overline{vstop}$
√ Prof R↓ - P20 + P27 + vstop
√ Mag 1 F↑ - (P2 + (P14 . Blocos < 2)) $\overline{vstop}$
√ Mag 1 A↑ - (P12 . Blocos < 2) $\overline{vstop}$
√ Mag 2 F↑ - (P2 + (P14 . Blocos == 2)) $\overline{vstop}$
√ Mag 2 A↑ (P12 . Blocos == 2) $\overline{vstop}$
√ Mag 1 F↓ - (P4 + P16 . Blocos < 2 + vstop
√ Mag 1 A↓ P2 + P14 . Bloco < 2 + vstop
√ Mag 2 F↓ P4 + P16 Blocos == 2 + vstop
√ Mag 2 A↓ P2 + P14 Blocos == 2 + vstop

√ Azul - $\overline{P0}$. $\overline{P37}$. $\overline{P2}$
√ VERDE - P0 + vstop + 21
√ VERMELHO - $\overline{P0}$. $\overline{P37}$. $\overline{P2}$
√ ind↑ - P29
√ ind↓ - P10 + P4
√ cima↑ - P23
√ cima↓ - P10 + P4
√ vquit↓ - P0

timers reset
Contadores FC ✓ + P0 + P31 vquit
Esteira esq - P8 + P10 + P0 vquit
P22 to p20 - P20 + P0 vquit
18 - 20 - P20 + P0 vquit
Prof Estend P25 P27 + P0 vquit
P16 - P27 - P29 - P31 + P0 vquit