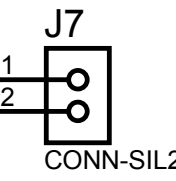
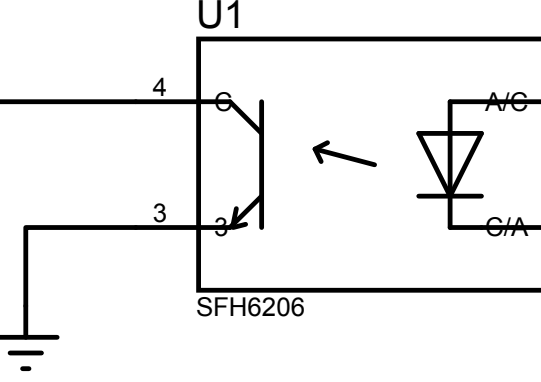
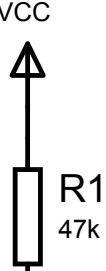
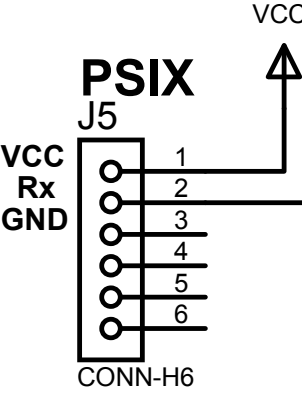
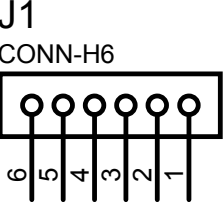
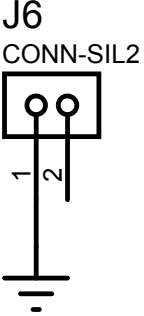
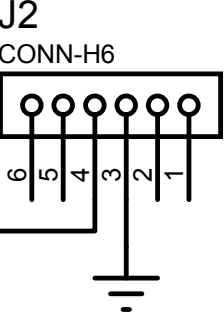
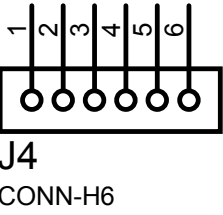
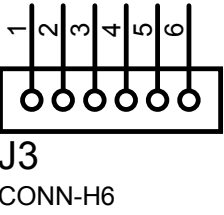


J1,J2,J3,J4 :
1 :PWR (BATT)
2 : DIO - SDA
3 : GND
4 : VCC (+3.3V)
5 : AIO - SCL
6 : IRQ



Vers téléinfo



FILE NAME: JEENODE_TELEINFO.DSN	DATE: 12/02/2011
DESIGN TITLE: Piggy pour Jeenode Teleinfo	PAGE: 1
PATH: C:\Projets_Electroniques\DOMINI\trunk\ARCHIV\COMPTEUR_EDF	TIME: 23:17:40
BY: RzBo	REV: 1