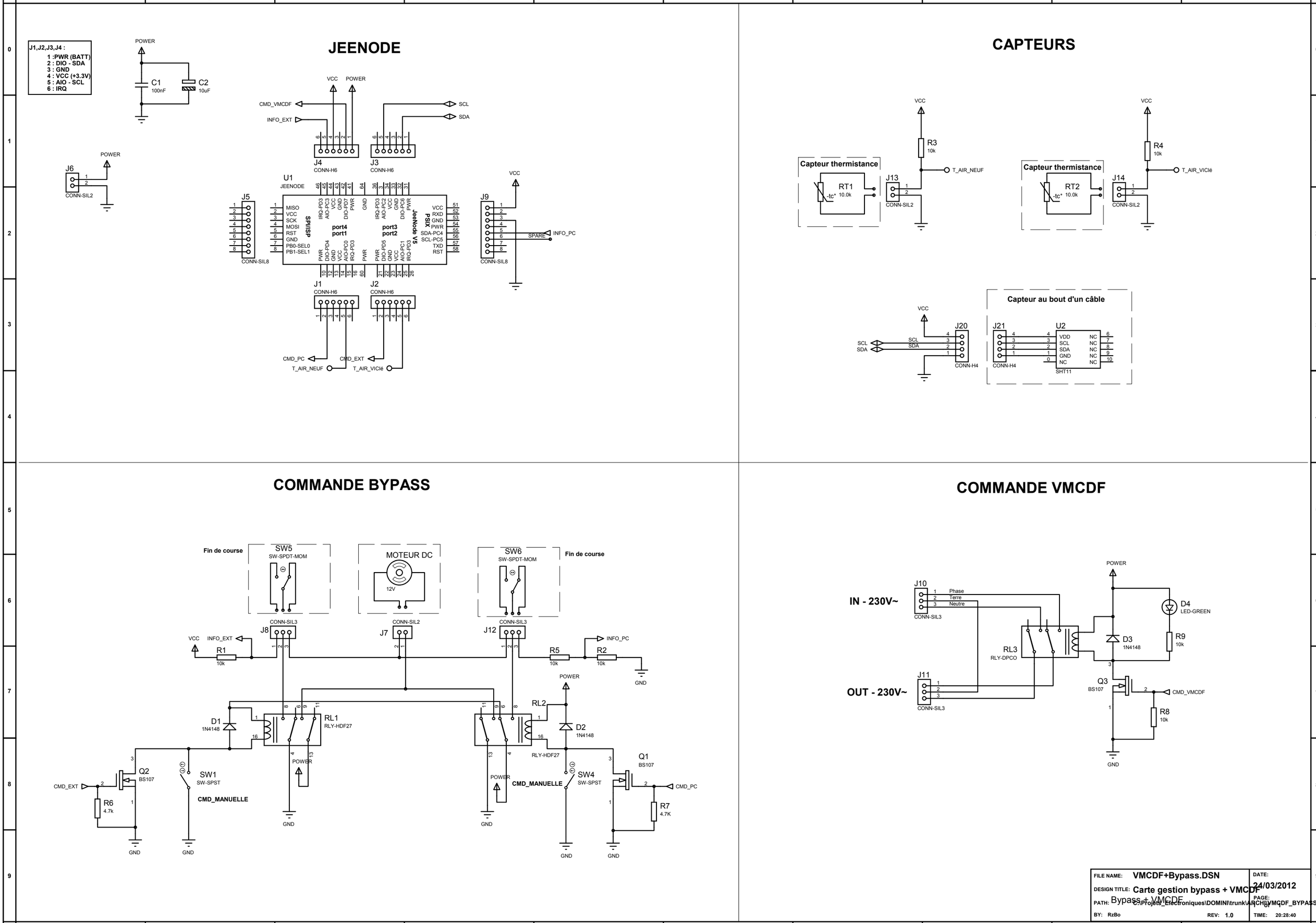
JEENODE
J1,J2,J3,J4 :
1 :PWR (BATT)
2 : DIO - SDA
3 : GND
4 : VCC (+3.3V)
5 : AIO - SCL
6 : IRQ
POWER
C1
100nF
C2
10uF
J6
CONN-SIL2
VCC
POWER
CMD_VMCDF
INFO_EXT
SCL
SDA
J4
CONN-H6
J3
CONN-H6
U1
JEENODE
J5
CONN-SIL8
MISO
VCC
SCK
MOSI
RST
GND
PB0-SEL0
PB1-SEL1
SPI/ISP
port4
port1
port3
port2
Jeenode V5
PS/X
VCC
RXD
GND
PWR
SDA-PC4
SCL-PC5
TXD
RST
J9
CONN-SIL8
SPARE
INFO_PC
J1
CONN-H6
J2
CONN-H6
CMD_PC
CMD_EXT
T_AIR_NEUF
T_AIR_VICIé
CAPTEURS
Capteur thermistance
RT1
10.0k
-tc*
R3
10k
T_AIR_NEUF
J13
CONN-SIL2
Capteur thermistance
RT2
10.0k
-tc*
R4
10k
T_AIR_VICIé
J14
CONN-SIL2
Capteur au bout d'un câble
J20
CONN-H4
J21
CONN-H4
U2
SHT11
VDD
SCL
SDA
GND
NC
COMMANDE BYPASS
Fin de course
SW5
SW-SPDT-MOM
MOTEUR DC
12V
SW6
SW-SPDT-MOM
Fin de course
J8
CONN-SIL3
J7
CONN-SIL2
J12
CONN-SIL3
INFO_EXT
R1
10k
R5
10k
R2
10k
INFO_PC
GND
RL1
RLY-HDF27
RL2
RLY-HDF27
D1
1N4148
D2
1N4148
Q2
BS107
Q1
BS107
SW1
SW-SPST
CMD_MANUELLE
SW4
SW-SPST
CMD_MANUELLE
CMD_EXT
CMD_PC
R6
4.7k
R7
4.7K
COMMANDE VMCDF
IN - 230V~
OUT - 230V~
J10
CONN-SIL3
Phase
Terre
Neutre
J11
CONN-SIL3
RL3
RLY-DPCO
D4
LED-GREEN
D3
1N4148
R9
10k
Q3
BS107
CMD_VMCDF
R8
10k
FILE NAME: VMCDF+Bypass.DSN
DESIGN TITLE: Carte gestion bypass + VMCDF
PATH: C:\Projets_Electroniques\DOMINI\trunk\ARCH\VMCDF_BYPASS\VM
BY: RzBo
REV: 1.0
DATE: 24/03/2012
PAGE: 1 of 1
TIME: 20:28:40
Bypass + VMCDF