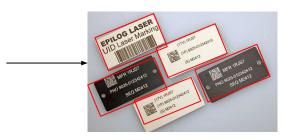
What are our next plans?

Ke, Wang, Wang, Gu, Sun



Workflow: (4 weeks left)





Ewaste dataset [still waiting] because microsoft denies our access

StageI. Correctly labeling bounding boxes - using Yolo

Image processing

Make sure boxes are in the correct places (by the end of next week)



Stage II. Text extracting using Qwen [Jerry's Demo]



Stage III. LLM process of Qwen's output, to "edit and polish the output" (by the end of

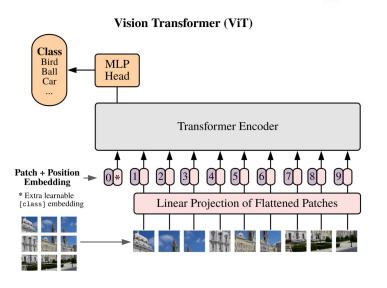


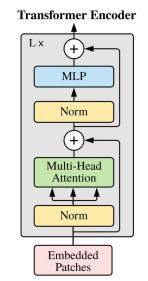
The weapon that we need...



Bluehive Appointment booked with Prof. Mort. [awaiting responses]

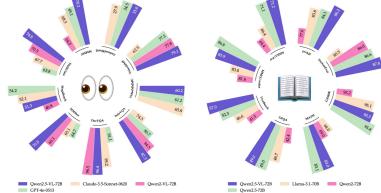
Qwen2.5-VL



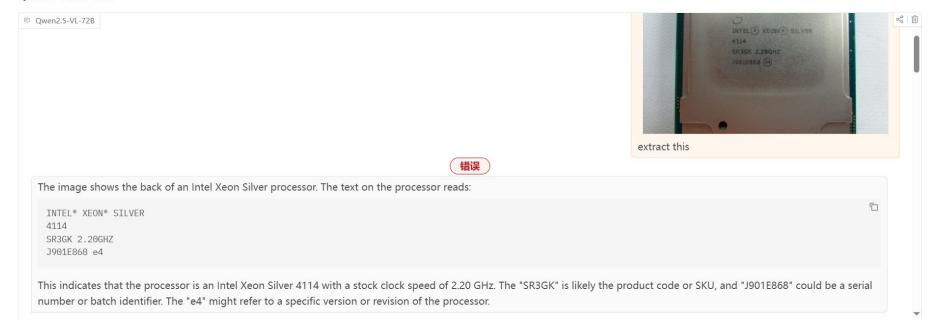


Bai, S., Chen, K., Liu, X., Wang, J., Ge, W., Song, S., Dang, K., Wang, P., Wang, S., Tang, J., Zhong, H., Zhu, Y., Yang, M., Li, Z., Wan, J., Wang, P., Ding, W., Fu, Z., Xu, Y., ... Lin, J. (2025, February 19). QWEN2.5-VL Technical Report. arXiv.org. https://arxiv.org/abs/2502.13923

- redesigned Vision Transformer (ViT) architecture
- "Surpassing the previous open-source models and achieving performance comparable to GPT-40 and Claude 3.5"



Qwen2.5-VL-72B Demo





The image shows an Intel Xeon E-2136 processor. The text on the processor reads:

INTEL® XEON® E-2136

SR3WW 3.30GHZ

L849E772 e4

INTEL® XEON® SILVER 4112 SR3GN 2.60GHZ J737C647 e4

1. IMC12

- Top-left: (370, 230)
- Bottom-right: (470, 270)

2. INTEL® XEON® E5-2643V3

- o Top-left: (340, 290)
- Bottom-right: (810, 320)

3. SR204 3.40GHZ

- o Top-left: (340, 340)
- Bottom-right: (580, 370)

4. MALAY

- o Top-left: (340, 390)
- Bottom-right: (430, 420)

5. L602F579 e4

- o Top-left: (340, 440)
- Bottom-right: (540, 470)



More Checks to Do



