Submission Date, 2016

***Proposal for the development of Project Name***

Prepared by Student Name   
*Computer Engineering Technology Student*Username.github.io

**Executive Summary**

As a student in the Computer Engineering Technology program, I will be integrating the knowledge and skills I have learned from our program into this Internet of Things themed capstone project. This proposal requests the approval to build the hardware portion that will connect to a database as well as to a mobile device application. The internet connected hardware will include a custom PCB with sensors and actuators for \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. The database will store \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. The mobile device functionality will include \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ and will be further detailed in the mobile application proposal. I will be collaborating with the following company/department \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. In the winter semester I plan to form a group with the following students, who are also building similar hardware this term and working on the mobile application with me \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. The hardware will be completed in CENG 317 Hardware Production Techniques independently and the application will be completed in CENG 319 Software Project. These will be integrated together in the subsequent term in CENG 355 Computer Systems Project as a member of a 2 or 3 student group.

**Background**

The problem solved by project is \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_50 word problem statement\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_100 words of background\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

I have searched for prior art via Humber’s IEEE subscription selecting “My Subscribed Content”[1] and have found and read [2] which provides insight into similar efforts.

In the Computer Engineering Technology program we have learned about the following topics from the respective relevant courses:

* Java Docs from CENG 212 Programming Techniques In Java,
* Construction of circuits from CENG 215 Digital And Interfacing Systems,
* Rapid application development and Gantt charts from CENG 216 Intro to Software Engineering,
* Micro computing from CENG 252 Embedded Systems,
* SQL from CENG 254 Database With Java,
* Web access of databases from CENG 256 Internet Scripting; and,
* Wireless protocols such as 802.11 from TECH152 Telecom Networks.

This knowledge and skill set will enable me to build the subsystems and integrate them together as my capstone project.

**Methodology**

This proposal is assigned in the first week of class and is due at the beginning of class in the second week of the fall semester. My coursework will focus on the first two of the 3 phases of this project:  
 Phase 1 Hardware build.  
 Phase 2 System integration.  
 Phase 3 Demonstration to future employers.

*Phase 1 Hardware build*

The hardware build will be completed in the fall term. It will fit within the CENG Project maximum dimensions of 12 13/16" x 6" x 2 7/8" (32.5cm x 15.25cm x 7.25cm) which represents the space below the tray in the parts kit. The highest AC voltage that will be used is 16Vrms from a wall adaptor from which +/- 15V or as high as 45 VDC can be obtained. Maximum power consumption will be 20 Watts.

*Phase 2 System integration*

The system integration will be completed in the fall term.

*Phase 3 Demonstration to future employers*

This project will showcase the knowledge and skills that I have learned to potential employers.

The tables below provide rough effort and non-labour estimates respectively for each phase. A Gantt chart will be added by week 3 to provide more project schedule details and a more complete budget will be added by week 4. It is important to start tasks as soon as possible to be able to meet deadlines.

|  |  |  |
| --- | --- | --- |
| **Labour Estimates** | **Hrs** | **Notes** |
| **Phase 1** |  |  |
| 1. Writing proposal. | 9 | Tech identification quiz. |
| 1. Creating project schedule. Initial project team meeting. | 9 | Proposal due. |
| 1. Creating budget. Status Meeting. | 9 | Project Schedule due. |
| 1. Acquiring components and writing progress report. | 9 | Budget due. |
| 1. Mechanical assembly and writing progress report. Status Meeting. | 9 | Progress Report due (components acquired milestone). |
| 1. PCB fabrication. | 9 | Progress Report due (Mechanical Assembly milestone). |
| 1. Interface wiring, Placard design, Status Meeting. | 9 | PCB Due (power up milestone). |
| 1. Preparing for demonstration. | 9 | Placard due. |
| 1. Writing progress report and demonstrating project. | 9 | Progress Report due (Demonstrations at Open House Saturday, November \_\_, 2016 from 10 a.m. - 2 p.m.). |
| 1. Editing build video. | 9 | Peer grading of demonstrations due. |
| 1. Incorporation of feedback from demonstration and writing progress report. Status Meeting. | 9 | 30 second build video due. |
| 1. Practice presentations | 9 | Progress Report due. |
| 1. 1st round of Presentations, Collaborators present. | 9 | Presentation PowerPoint file due. |
| 1. 2nd round of Presentations | 9 | Build instructions up due. |
| 1. Project videos, Status Meeting. | 9 | 30 second script due. |
| **Phase 1 Total** | **135** |  |
| **Phase 2** |  |  |
| 1. Meet with collaborators | 9 | Status Meeting |
| 1. Initial integration. | 9 | Progress Report |
| 1. Meet with collaborators | 9 | Status Meeting |
| 1. Testing. | 9 | Progress Report |
| 1. Meet with collaborators | 9 | Status Meeting |
| 1. Meet with collaborators | 9 | Status Meeting |
| 1. Incorporation of feedback. | 9 | Progress Report |
| 1. Meet with collaborators | 9 | Status Meeting |
| 1. Testing. | 9 | Progress Report |
| 1. Meet with collaborators | 9 | Status Meeting |
| 1. Prepare for demonstration. | 9 | Progress Report |
| 1. Complete presentation. | 9 | Demonstration at Open House  Saturday, April \_\_, 2017  10 a.m. to 2 p.m. |
| 1. Complete final report. 1st round of Presentations. | 9 | Presentation PowerPoint file due. |
| 1. Write video script. 2nd round of Presentations, delivery of project. | 9 | Final written report including final budget and record of expenditures, covering both this semester and the previous semester. |
| 1. Project videos. | 9 | Video script due |
| **Phase 2 Total** | **135** |  |
| **Phase 3** |  |  |
| 1. Interviews | TBD |  |
| **Phase 3 Total** | **TBD** |  |

|  |  |  |
| --- | --- | --- |
| **Material Estimates** | **Cost** | **Notes** |
| **Phase 1** |  |  |
| 1. A microcomputer composed of a quad-core Windows 10 IoT core compatible Broadcom BCM2836 SoC with a 900MHz Application ARM Cortex-A7 32 bit RISC v7-A processor core stacked under 1GB of 450MHz SDRAM, 10/100 Mbit/s Ethernet, GPIO, UART, I²C bus, SPI bus, 8 GB of Secure Digital storage, a power supply, and a USB Wi-Fi adaptor. | >$80.00 | An example of a retailer: [3]. |
| 1. Peripherals with cables |  |  |
| 1. Sensors |  |  |
| 1. Actuators |  |  |
| 1. Hardware, etc. |  |  |
| **Phase 1 Total** | **>$200.00** |  |
| **Phase 2** |  |  |
| 1. Materials to improve functionality, fit, and finish of project. |  |  |
| **Phase 2 Total** | **TBD** |  |
| **Phase 3** |  |  |
| 1. Off campus colocation | <$100.00 | An example: [4]. |
| *Shipping* | *TBD* |  |
| *Tax* | *TBD* |  |
| *Duty* | *TBD* |  |
| **Phase 3 Total** | **TBD** |  |

**Concluding remarks**

This proposal presents a plan for providing an IoT solution for \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. This is an opportunity to integrate the knowledge and skills developed in our program to create a collaborative IoT capstone project demonstrating my ability to learn how to support projects such as the initiative described by [2]. I request approval of this project.

**References**

[1] Institute of Electrical and Electronics Engineers. (2015, August 28). IEEE Xplore Digital Library [Online]. Available: https://ieeexplore.ieee.org/search/advsearch.jsp

[2] Segura-Garcia, J.; Felici-Castell, S.; Perez-Solano, J.J.; Cobos, M.; Navarro, J.M., "Low-Cost Alternatives for Urban Noise Nuisance Monitoring Using Wireless Sensor Networks," *Sensors Journal, IEEE*, vol.15, no.2, pp.836,844, Feb. 2015 doi: 10.1109/JSEN.2014.2356342

[3] Creatron. (2015, August 28). Part Number: RASPI-004499 [Online]. Available: https://www.creatroninc.com/product/raspberry-pi-2-media-starter-kit/

[4] Upton, Liz. (2015, August 28). Raspberry Pi colocation [Online]. Available: http://raspberrycolocation.com/