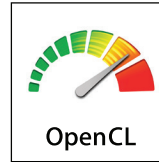


OpenCL (Open Computing Language) is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs, and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems, and handheld devices.

Specification documents and online reference are available at www.khronos.org/opencvl.



[n.n.n] and purple text: sections and text in the OpenCL API 2.1 Spec.
[n.n.n] and green text: sections and text in the OpenCL C 2.0 Spec.
[n.n.n] and blue text: sections and text in the OpenCL Extension 2.1 Spec.

OpenCL API Reference

Section and table references are to the OpenCL API 2.1 specification.

The OpenCL Platform Layer

The OpenCL platform layer implements platform-specific features that allow applications to query OpenCL devices, device configuration information, and to create OpenCL contexts using one or more devices. **Items in blue apply when the appropriate extension is supported.**

Querying Platform Info & Devices [4.1-2] [9.16.9]

`cl_int clGetPlatformIDs (cl_uint num_entries,
cl_platform_id *platforms, cl_uint *num_platforms)`

`cl_int clGetPlatformIDsKHR (cl_uint num_entries,
cl_platform_id *platforms, cl_uint *num_platforms)`

`cl_int clGetPlatformInfo (cl_platform_id platform,
cl_platform_info param_name,
size_t param_value_size, void *param_value,
size_t *param_value_size_ret)`

param_name: CL_PLATFORM_{PROFILE, VERSION},
CL_PLATFORM_{NAME, VENDOR, EXTENSIONS},
CL_PLATFORM_HOST_TIMER_RESOLUTION,
CL_PLATFORM_ICD_SUFFIX_KHR [Table 4.1]

`cl_int clGetDeviceIDs (cl_platform_id platform,
cl_device_type device_type, cl_uint num_entries,
cl_device_id *devices, cl_uint *num_devices)`

device_type: [Table 4.2]
CL_DEVICE_TYPE_{ACCELERATOR, ALL, CPU},
CL_DEVICE_TYPE_{CUSTOM, DEFAULT, GPU}

`cl_int clGetDeviceInfo (cl_device_id device,
cl_device_info param_name,
size_t param_value_size, void *param_value,
size_t *param_value_size_ret)`

param_name: [Table 4.3]
CL_DEVICE_ADDRESS_BITS, CL_DEVICE_AVAILABLE,
CL_DEVICE_BUILT_IN_KERNELS,
CL_DEVICE_COMPILER_AVAILABLE,
CL_DEVICE_{DOUBLE, HALF, SINGLE} FP_CONFIG,
CL_DEVICE_ENDIAN_LITTLE, CL_DEVICE_EXTENSIONS,
CL_DEVICE_ERROR_CORRECTION_SUPPORT,
CL_DEVICE_EXECUTION_CAPABILITIES,
CL_DEVICE_GLOBAL_MEM_CACHE_{SIZE, TYPE},
CL_DEVICE_GLOBAL_MEM_{CACHELINE_SIZE, SIZE},
CL_DEVICE_GLOBAL_VARIABLE_PREFERRED_TOTAL_SIZE,
CL_DEVICE_IL_VERSION,
CL_DEVICE_IMAGE_MAX_{ARRAY, BUFFER}_SIZE,
CL_DEVICE_IMAGE_SUPPORT,
CL_DEVICE_IMAGE2D_MAX_{WIDTH, HEIGHT},
CL_DEVICE_IMAGE3D_MAX_{WIDTH, HEIGHT, DEPTH},
CL_DEVICE_IMAGE_BASE_ADDRESS_ALIGNMENT,
CL_DEVICE_IMAGE_PITCH_ALIGNMENT,
CL_DEVICE_LINKER_AVAILABLE,
CL_DEVICE_LOCAL_MEM_{TYPE, SIZE},
CL_DEVICE_MAX_{CLOCK_FREQUENCY, PIPE_ARGS},
CL_DEVICE_MAX_{COMPUTE_UNITS, SAMPLERS},
CL_DEVICE_MAX_CONSTANT_{ARGS, BUFFER_SIZE},
CL_DEVICE_MAX_GLOBAL_VARIABLE_SIZE,
CL_DEVICE_MAX_{MEM_ALLOC, PARAMETER}_SIZE,
CL_DEVICE_MAX_NUM_SUB_GROUPS,
CL_DEVICE_MAX_ON_DEVICE_{QUEUES, EVENTS},
CL_DEVICE_MAX_{READ, WRITE}_IMAGE_ARGS,
CL_DEVICE_MAX_READ_WRITE_IMAGE_ARGS,
CL_DEVICE_MAX_SUB_GROUPS,
CL_DEVICE_MAX_WORK_GROUP_SIZE,
CL_DEVICE_MAX_WORK_ITEM_{DIMENSIONS, SIZES},
CL_DEVICE_MEM_BASE_ADDR_ALIGN,
CL_DEVICE_NAME,
CL_DEVICE_NATIVE_VECTOR_WIDTH_X
(where X may be CHAR, INT, DOUBLE, HALF, LONG,
SHORT, FLOAT),
CL_DEVICE_NATIVE_VECTOR_WIDTH_FLOAT,

CL_DEVICE_{OPENCL_C_VERSION, PARENT_DEVICE},
CL_DEVICE_PARTITION_AFFINITY_DOMAIN,
CL_DEVICE_PARTITION_MAX_SUB_DEVICES,
CL_DEVICE_PARTITION_{PROPERTIES, TYPE},
CL_DEVICE_PIPE_MAX_ACTIVE_RESERVATIONS,
CL_DEVICE_PIPE_MAX_PACKET_SIZE,
CL_DEVICE_{PLATFORM, PRINTF_BUFFER_SIZE},
CL_DEVICE_PREFERRED_Y_ATOMIC_ALIGNMENT
(where Y may be LOCAL, GLOBAL, PLATFORM),
CL_DEVICE_PREFERRED_VECTOR_WIDTH_Z
(where Z may be CHAR, INT, DOUBLE, HALF, LONG,
SHORT, FLOAT),
CL_DEVICE_PREFERRED_INTEROP_USER_SYNC,
CL_DEVICE_PROFILE,
CL_DEVICE_PROFILING_TIMER_RESOLUTION,
CL_DEVICE_SPIR_VERSIONS,
CL_DEVICE_SUBGROUP_INDEPENDENT_FORWARD-
PROGRESS,
CL_DEVICE_QUEUE_ON_{DEVICE, HOST}_PROPERTIES,
CL_DEVICE_QUEUE_ON_DEVICE_MAX_SIZE,
CL_DEVICE_QUEUE_ON_DEVICE_PREFERRED_SIZE,
CL_DEVICE_{REFERENCE_COUNT, VENDOR_ID},
CL_DEVICE_SVM_CAPABILITIES,
CL_DEVICE_TERMINATE_CAPABILITY_KHR,
CL_DEVICE_{TYPE, VENDOR},
CL_DEVICE_VENDOR_ID,
CL_{DEVICE, DRIVER}_VERSION

`cl_int clGetDeviceAndHostTimer (cl_device_id device,
cl_ulong *device_timestamp,
cl_ulong *host_timestamp)`

`cl_int clGetHostTimer (cl_device_id device,
cl_ulong *host_timestamp)`

Partitioning a Device [4.3]

`cl_int clCreateSubDevices (cl_device_id in_device,
const cl_device_partition_property *properties,
cl_uint num_devices, cl_device_id *out_devices,
cl_uint *num_devices_ret)`

properties: [Table 4.4] CL_DEVICE_PARTITION_EQUALLY,
CL_DEVICE_PARTITION_BY_COUNTS,
CL_DEVICE_PARTITION_BY_AFFINITY_DOMAIN

The OpenCL Runtime

API calls that manage OpenCL objects such as command-queues, memory objects, program objects, kernel objects for __kernel functions in a program and calls that allow you to enqueue commands to a command-queue such as executing a kernel, reading, or writing a memory object.

Command Queues [5.1]

`cl_command_queue
clCreateCommandQueueWithProperties (cl_context context, cl_device_id device,
const cl_command_queue_properties *properties,
cl_int *errcode_ret)`

properties: [Table 5.1] CL_QUEUE_SIZE,
CL_QUEUE_PROPERTIES (bitfield which may be set to an OR of CL_QUEUE_* where * may be: OUT_OF_ORDER_EXEC_MODE_ENABLE, PROFILING_ENABLE, ON_DEVICE[DEFAULT]),
CL_QUEUE_THROTTLE_{HIGH, MED, LOW}_KHR (requires the cl_khr_throttle_hint extension),
CL_QUEUE_PRIORITY_KHR (bitfield which may be one of CL_QUEUE_PRIORITY_HIGH_KHR, CL_QUEUE_PRIORITY_MED_KHR, CL_QUEUE_PRIORITY_LOW_KHR (requires the cl_khr_priority_hints extension))

`cl_int clRetainDevice (cl_device_id device)
cl_int clReleaseDevice (cl_device_id device)`

Contexts [4.4]

`cl_context clCreateContext (const cl_context_properties *properties,
cl_uint num_devices, const cl_device_id *devices,
void (CL_CALLBACK *pfn_notify) (const char *errinfo, const void *private_info,
size_t cb, void *user_data),
void *user_data, cl_int *errcode_ret)`

properties: [Table 4.5]
NULL or CL_CONTEXT_PLATFORM,
CL_CONTEXT_INTEROP_USER_SYNC,
CL_CONTEXT_{D3D10, D3D11}_DEVICE_KHR,
CL_CONTEXT_ADAPTER_{D3D9, D3D9EX}_KHR,
CL_CONTEXT_ADAPTER_DXVA_KHR,
CL_CONTEXT_MEMORY_INITIALIZE_KHR,
CL_CONTEXT_TERMINATE_KHR,
CL_GL_CONTEXT_KHR, CL_CGL_SHAREGROUP_KHR,
CL_{EGL, GLX}_DISPLAY_KHR, CL_WGL_HDC_KHR

`cl_context clCreateContextFromType (const cl_context_properties *properties,
cl_device_type device_type,
void (CL_CALLBACK *pfn_notify) (const char *errinfo, const void *private_info,
size_t cb, void *user_data),
void *user_data, cl_int *errcode_ret)`

properties: See `clCreateContext`
device_type: See `clGetDeviceIDs`

`cl_int clRetainContext (cl_context context)`

`cl_int clReleaseContext (cl_context context)`

`cl_int clGetContextInfo (cl_context context,
cl_context_info param_name,
size_t param_value_size, void *param_value,
size_t *param_value_size_ret)`

param_name: CL_CONTEXT_REFERENCE_COUNT,
CL_CONTEXT_{DEVICES, NUM_DEVICES, PROPERTIES},
CL_CONTEXT_{D3D10, D3D11}_PREFER_SHARED_RESOURCES_KHR [Table 4.6]

`cl_int clTerminateContextKHR (cl_context context)`

Get CL Extension Function Pointers [9.2]

`void * clGetExtensionFunctionAddressForPlatform (cl_platform_id platform, const char *funcname)`

`cl_int clSetDefaultDeviceCommandQueue (cl_context context, cl_device_id device,
cl_command_queue command_queue)`

`cl_int clRetainCommandQueue (cl_command_queue command_queue)`

`cl_int clReleaseCommandQueue (cl_command_queue command_queue)`

`cl_int clGetCommandQueueInfo (cl_command_queue command_queue,
cl_command_queue_info param_name,
size_t param_value_size, void *param_value,
size_t *param_value_size_ret)`

param_name: [Table 5.2]
CL_QUEUE_CONTEXT,
CL_QUEUE_DEVICE[DEFAULT], CL_QUEUE_SIZE,
CL_QUEUE_REFERENCE_COUNT,
CL_QUEUE_PROPERTIES

Shared Virtual Memory

Shared Virtual Memory (SVM) allows the host and kernels executing on devices to directly share complex, pointer-containing data structures such as trees and linked lists. See more on SVM on page 4 of this reference guide.

SVM Sharing Granularity [5.6.1]

```
void* clSVMAlloc (
    cl_context context, cl_svm_mem_flags flags,
    size_t size, cl_uint alignment)

flags: [Table 5.14]
CL_MEM_READ_WRITE,
CL_MEM_WRITE_READ,
CL_MEM_WRITE_ONLY,
CL_MEM_SVM_FINE_GRAIN_BUFFER,
CL_MEM_SVM_ATOMICS

void clSVMFree (cl_context context, void *svm_pointer)
```

Enqueuing SVM Operations [5.6.2]

```
cl_int clEnqueueSVMFree (
    cl_command_queue command_queue,
    cl_uint num_svm_pointers, void *svm_pointers[],
    void (CL_CALLBACK* pfn_free_func)(
        cl_command_queue command_queue,
        cl_uint num_svm_pointers,
        void *svm_pointers[], void *user_data),
    void *user_data, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueSVMMemcpy (
    cl_command_queue command_queue,
    cl_bool blocking_copy, void *dst_ptr,
    const void *src_ptr, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueSVMMemFill (
    cl_command_queue command_queue,
    void *svm_ptr, const void *pattern,
    size_t pattern_size, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueSVMMap (
    cl_command_queue command_queue,
    cl_bool blocking_map, cl_map_flags map_flags,
    void *svm_ptr, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueSVMUnmap (
    cl_command_queue command_queue,
    void *svm_ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Program Objects

An OpenCL program consists of a set of kernels that are identified as functions declared with the `__kernel` qualifier in the program source.

Create Program Objects [5.8.1]

```
cl_program clCreateProgramWithSource (
    cl_context context, cl_uint count,
    const char **strings, const size_t *lengths,
    cl_int *errcode_ret)

cl_program clCreateProgramWithIL (
    cl_context context, const void *il,
    size_t length, cl_int *errcode_ret)

cl_program clCreateProgramWithBinary (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list, const size_t *lengths,
    const unsigned char **binaries,
    cl_int *binary_status, cl_int *errcode_ret)

cl_program clCreateProgramWithBuiltInKernels (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list,
    const char *kernel_names, cl_int *errcode_ret)

cl_int clRetainProgram (cl_program program)
cl_int clReleaseProgram (cl_program program)
```

Building Program Executables [5.8.2]

```
cl_int clBuildProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, void (CL_CALLBACK* pfn_notify)
    (cl_program program, void *user_data),
    void *user_data)
```

Separate Compilation and Linking [5.8.3]

```
cl_int clCompileProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, cl_uint num_input_headers,
    const cl_program *input_headers,
    const char **header_include_names,
    void (CL_CALLBACK* pfn_notify)
    (cl_program program, void *user_data),
    void *user_data)
```

Kernel Objects

A kernel object encapsulates the specific `__kernel` function and the argument values to be used when executing it. Items in blue apply when the appropriate extension is supported.

Create Kernel Objects [5.9.1]

```
cl_kernel clCreateKernel (cl_program program,
    const char *kernel_name, cl_int *errcode_ret)

cl_int clCreateKernelsInProgram (cl_program program,
    cl_uint num_kernels, cl_kernel *kernels,
    cl_uint *num_kernels_ret)

cl_int clRetainKernel (cl_kernel kernel)
cl_int clReleaseKernel (cl_kernel kernel)
```

Kernel Arguments and Queries [5.9.2-4]

```
cl_int clSetKernelArg (cl_kernel kernel, cl_uint arg_index,
    size_t arg_size, const void *arg_value)
```

```
cl_program clLinkProgram (cl_context context,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, cl_uint num_input_programs,
    const cl_program *input_programs,
    void (CL_CALLBACK* pfn_notify)
    (cl_program program, void *user_data),
    void *user_data, cl_int *errcode_ret)
```

Unload the OpenCL Compiler [5.8.6]

```
cl_int clUnloadPlatformCompiler (
    cl_platform_id platform)
```

Query Program Objects [5.8.7]

```
cl_int clGetProgramInfo (cl_program program,
    cl_program_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)

param_name: [Table 5.17]
CL_PROGRAM_REFERENCE_COUNT,
CL_PROGRAM_CONTEXT, NUM_DEVICES, DEVICES,
CL_PROGRAM_SOURCE, BINARY_SIZES, BINARIES,
CL_PROGRAM_NUM_KERNELS, KERNEL_NAMES,
CL_PROGRAM_IL

cl_int clGetProgramBuildInfo (
    cl_program program, cl_device_id device,
    cl_program_build_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)

param_name: [Table 5.18]
CL_PROGRAM_BINARY_TYPE,
CL_PROGRAM_BUILD_STATUS, OPTIONS, LOG,
CL_PROGRAM_BUILD_GLOBAL_VARIABLE_TOTAL_SIZE
```

Compiler Options [5.8.4]

SPIR options require the `cl_khr_spir` extension.

Preprocessor: `(-D processed in order for clBuildProgram or clCompileProgram)`
`-D name` `-D name=definition` `-I dir`

Math intrinsics:

`-cl-single-precision-constant`
`-cl-denorms-are-zero`
`-cl-fp32-correctly-rounded-divide-sqrt`

Optimization options:

`-cl-opt-disable` `-cl-mad-enable`
`-cl-no-signed-zeros` `-cl-finite-math-only`
`-cl-unsafe-math-optimizations` `-cl-fast-relaxed-math`
`-cl-uniform-work-group-size`

Warning request/suppress:

`-w` `-Werror`

Control OpenCL C language version:

`-cl-std=CL1.1` // OpenCL 1.1 specification
`-cl-std=CL1.2` // OpenCL 1.2 specification
`-cl-std=CL2.0` // OpenCL 2.0 specification

Query kernel argument information:

`-cl-kernel-arg-info`

Debugging options:

`-g` // generate additional errors for built-in
// functions that allow you to enqueue
// commands on a device

SPIR binary options:

`-x spir` // indicate that binary is in SPIR format
`-spir-std=x` // x is SPIR spec version, e.g.: 1.2

Linker Options [5.8.5]

Library linking options:

`-create-library` `-enable-link-options`

Program linking options:

`-cl-denorms-are-zero` `-cl-no-signed-zeros`
`-cl-finite-math-only` `-cl-fast-relaxed-math`
`-cl-unsafe-math-optimizations`

Flush and Finish [5.15]

```
cl_int clFlush (cl_command_queue command_queue)
cl_int clFinish (cl_command_queue command_queue)
```

```
cl_int clSetKernelArgSVMPointer (cl_kernel kernel,
    cl_uint arg_index, const void *arg_value)
```

```
cl_int clSetKernelExecInfo (cl_kernel kernel,
    cl_kernel_exec_info param_name,
    size_t param_value_size, const void *param_value)
```

param_name: CL_KERNEL_EXEC_INFO_SVM_PTRS,
CL_KERNEL_EXEC_INFO_SVM_FINE_GRAIN_SYSTEM

```
cl_kernel clCloneKernel (cl_kernel source_kernel,
    cl_int *errcode_ret)
```

```
cl_int clGetKernelInfo (cl_kernel kernel,
    cl_kernel_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
```

param_name: [Table 5.20]
CL_KERNEL_FUNCTION_NAME,
CL_KERNEL_NUM_ARGS,
CL_KERNEL_REFERENCE_COUNT,
CL_KERNEL_ATTRIBUTES, CONTEXT, PROGRAM)

```
cl_int clGetKernelWorkGroupInfo (cl_kernel kernel,
    cl_device_id device,
    cl_kernel_work_group_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

param_name: CL_KERNEL_GLOBAL_WORK_SIZE,
CL_KERNEL_COMPILE_WORK_GROUP_SIZE,
CL_KERNEL_COMPILE_MAX, NUM_SUB_GROUPS,
CL_KERNEL_LOCAL_PRIVATE_MEM_SIZE,
CL_KERNEL_PREFERRED_WORK_GROUP_SIZE_MULTIPLE
[Table 5.21]

```
cl_int clGetKernelArgInfo (cl_kernel kernel,
    cl_uint arg_idx, cl_kernel_arg_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

param_name: [Table 5.23]
CL_KERNEL_ARG_ACCESS_ADDRESS_QUALIFIER,
CL_KERNEL_ARG_NAME,
CL_KERNEL_ARG_TYPE_NAME_QUALIFIER)

(Continued on next page >)

Kernel Objects (continued)

```
cl_int clGetKernelSubGroupInfo (
    cl_kernel kernel, cl_device_id device,
    cl_kernel_sub_group_info param_name,
    size_t input_value_size, const void *input_value,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)

param_name: [Table 5.22]
CL_KERNEL_LOCAL_SIZE_FOR_SUB_GROUP_COUNT,
CL_KERNEL_MAX_SUB_GROUP_SIZE_FOR_
NDRANGE,
CL_KERNEL_SUB_GROUP_COUNT_FOR_NDRANGE
```

Event Objects

Event objects can be used to refer to a kernel execution command, and read, write, map, and copy commands on memory objects or user events.

Event Objects [5.11]

```
cl_event clCreateUserEvent (cl_context context,
    cl_int *errcode_ret)

cl_int clSetUserEventStatus (cl_event event,
    cl_int execution_status)

cl_int clWaitForEvents (cl_uint num_events,
    const cl_event *event_list)
```

Execute Kernels [5.10]

```
cl_int clEnqueueNDRangeKernel (
    cl_command_queue command_queue,
    cl_kernel kernel, cl_uint work_dim,
    const size_t *global_work_offset,
    const size_t *global_work_size,
    const size_t *local_work_size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clGetEventInfo (cl_event event,
    cl_event_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)

param_name: CL_EVENT_COMMAND_QUEUE_TYPE,
CL_EVENT_CONTEXT_REFERENCE_COUNT,
CL_EVENT_COMMAND_EXECUTION_STATUS [Table 5.24]
```

```
cl_int clRetainEvent (cl_event event)
```

```
cl_int clReleaseEvent (cl_event event)
```

```
cl_int clSetEventCallback (cl_event event,
    cl_int command_exec_callback_type,
    void (CL_CALLBACK *pfn_event_notify)
    (cl_event event,
    cl_int event_command_exec_status,
    void *user_data), void *user_data)
```

```
cl_int clEnqueueNativeKernel (
    cl_command_queue command_queue,
    void (CL_CALLBACK *user_func)(void *), void *args,
    size_t cb_args, cl_uint num_mem_objects,
    const cl_mem *mem_list, const void **args_mem_loc,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Markers, Barriers, Waiting for Events [5.12]

```
cl_int clEnqueueMarkerWithWaitList (
    cl_command_queue command_queue,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueBarrierWithWaitList (
    cl_command_queue command_queue,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Profiling Operations [5.14]

```
cl_int clGetEventProfilingInfo (cl_event event,
    cl_profiling_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)

param_name: [Table 5.25]
CL_PROFILING_COMMAND_COMPLETE, CL_PROFILING_COMMAND_QUEUED,
CL_PROFILING_COMMAND_SUBMIT, CL_PROFILING_COMMAND_END
```

Memory Model: Shared Virtual Memory [3.3.3]

OpenCL extends the global memory region into the host memory region through a shared virtual memory (SVM) mechanism. There are three types of SVM in OpenCL

- **Coarse-Grained buffer SVM:** Sharing occurs at the granularity of regions of OpenCL buffer memory objects. Consistency is enforced at synchronization points and with map/unmap commands to drive updates between the host and the device. This form of SVM is similar to the use of cl_mem buffers, with two differences. First, it lets kernel-instances share pointer-based data structures (such as linked-lists) with the host program. Second, concurrent access by multiple kernels on the same device is valid as long as the set of concurrently executing kernels is bounded by synchronization points. Concurrent access by multiple kernels on the same device is valid as long as the set of kernels is bounded by synchronization points. This form of SVM is similar to non-SVM use of memory; however, it lets kernel-instances share pointer-based data structures (such as linked-lists) with the host program. Program scope global variables are treated as per-device coarse-grained SVM for addressing and sharing purposes.

- **Fine-Grained buffer SVM:** Sharing occurs at the granularity of individual loads/stores into bytes within OpenCL buffer memory objects. Loads and stores may be cached. This means consistency is guaranteed at synchronization points. If the optional OpenCL atomics are supported, they can be used to provide fine-grained control of memory consistency.
- **Fine-Grained system SVM:** Sharing occurs at the granularity of individual loads/stores into bytes occurring anywhere within the host memory. Loads and stores may be cached so consistency is guaranteed at synchronization points. If the optional OpenCL atomics are supported, they can be used to provide fine-grained control of memory consistency.

Coarse-Grained buffer SVM is required in the core OpenCL specification. The two finer grained approaches are optional features in OpenCL. The various SVM mechanisms to access host memory from the work-items associated with a kernel instance are summarized in table 3-2 below.

Summary of SVM Options in OpenCL [3.3.3, Table 3-2]

SVM	Granularity of sharing	Memory allocation	Mechanisms to enforce consistency	Explicit updates between host and device?
Non-SVM buffers	OpenCL Memory objects (buffer)	clCreateBuffer	Host synchronization points on the same or between devices.	Yes, through Map and Unmap commands.
Coarse-Grained buffer SVM	OpenCL Memory objects (buffer)	clSVMAlloc	Host synchronization points between devices	Yes, through Map and Unmap commands.
Fine Grained buffer SVM	Bytes within OpenCL Memory objects (buffer)	clSVMAlloc	Synchronization points plus atomics (if supported)	No
Fine-Grained system SVM	Bytes within Host memory (system)	Host memory allocation mechanisms (e.g. malloc)	Synchronization points plus atomics (if supported)	No

Notes

Supported Data Types

The optional double scalar and vector types are supported if CL_DEVICE_DOUBLE_FP_CONFIG is not zero.

Built-in Scalar Data Types [6.1.1]

OpenCL Type	API Type	Description
bool	--	true (1) or false (0)
char	cl_char	8-bit signed
unsigned char, uchar	cl_uchar	8-bit unsigned
short	cl_short	16-bit signed
unsigned short, ushort	cl_ushort	16-bit unsigned
int	cl_int	32-bit signed
unsigned int, uint	cl_uint	32-bit unsigned
long	cl_long	64-bit signed
unsigned long, ulong	cl_ulong	64-bit unsigned
float	cl_float	32-bit float
double OPTIONAL	cl_double	64-bit IEEE 754
half	cl_half	16-bit float (storage only)
size_t	--	32- or 64-bit unsigned integer
ptrdiff_t	--	32- or 64-bit signed integer
intptr_t	--	32- or 64-bit signed integer
uintptr_t	--	32- or 64-bit unsigned integer
void	void	void

Built-in Vector Data Types [6.1.2]

OpenCL Type	API Type	Description
char _n	cl_char _n	8-bit signed
uchar _n	cl_uchar _n	8-bit unsigned
short _n	cl_short _n	16-bit signed
ushort _n	cl_ushort _n	16-bit unsigned
int _n	cl_int _n	32-bit signed
uint _n	cl_uint _n	32-bit unsigned
long _n	cl_long _n	64-bit signed
ulong _n	cl_ulong _n	64-bit unsigned
float _n	cl_float _n	32-bit float
double _n OPTIONAL	cl_double _n	64-bit float
half _n	Requires the cl_khr_fp16 extension	

Other Built-in Data Types [6.1.3]

The **OPTIONAL** types shown below are only defined if CL_DEVICE_IMAGE_SUPPORT is CL_TRUE. API type for application shown in *italics* where applicable. **Items in blue require the cl_khr_gl_msaa_sharing extension.**

OpenCL Type	Description
image2d_ <i>[msaa_]</i> t OPTIONAL	2D image handle
image3d_t OPTIONAL	3D image handle
image2d_array_ <i>[msaa_]</i> t OPTIONAL	2D image array
image1d_t OPTIONAL	1D image handle
image1d_buffer_t OPTIONAL	1D image buffer

image1d_array_t	OPTIONAL	1D image array
image2d_ <i>[msaa_]</i> depth_t	OPTIONAL	2D depth image
image2d_array_ <i>[msaa_]</i> depth_t	OPTIONAL	2D depth image array
sampler_t	OPTIONAL	sampler handle
queue_t		
ndrange_t		
clk_event_t		
reserve_id_t		
event_t		event handle
cl_mem_fence_flags		

Reserved Data Types [6.1.4]

OpenCL Type	Description
bool _n	boolean vector
half _n	16-bit, vector
quad, quad _n	128-bit float, vector
complex half, complex half _n imaginary half, imaginary half _n	16-bit complex, vector
complex float, complex float _n imaginary float, imaginary float _n	32-bit complex, vector
complex double, complex double _n imaginary double, imaginary double _n	64-bit complex, vector
complex quad, complex quad _n imaginary quad, imaginary quad _n	128-bit complex, vector
float _n <i>xm</i>	<i>n</i> * <i>m</i> matrix of 32-bit floats
double _n <i>xm</i>	<i>n</i> * <i>m</i> matrix of 64-bit floats

Vector Component Addressing [6.1.7]

Vector Components

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
float2 v;	v.x, v.s0	v.y, v.s1														
float3 v;	v.x, v.s0	v.y, v.s1	v.z, v.s2													
float4 v;	v.x, v.s0	v.y, v.s1	v.z, v.s2	v.w, v.s3												
float8 v;	v.s0	v.s1	v.s2	v.s3	v.s4	v.s5	v.s6	v.s7								
float16 v;	v.s0	v.s1	v.s2	v.s3	v.s4	v.s5	v.s6	v.s7	v.s8	v.s9	v.sA, v.sA	v.sB, v.sB	v.sC, v.sC	v.sD, v.sD	v.sE, v.sE	v.sF, v.sF

Vector Addressing Equivalences

Numeric indices are preceded by the letter s or S, e.g.: s1. Swizzling, duplication, and nesting are allowed, e.g.: v.yx, v.xx, v.lo.x

	v.lo	v.hi	v.odd	v.even
float2	v.x, v.s0	v.y, v.s1	v.y, v.s1	v.x, v.s0
float3 *	v.s01, v.xy	v.s23, v.zw	v.s13, v.yw	v.s02, v.xz
float4	v.s01, v.xy	v.s23, v.zw	v.s13, v.yw	v.s02, v.xz

*When using .lo or .hi with a 3-component vector, the .w component is undefined.

Operators and Qualifiers

Operators [6.3]

These operators behave similarly as in C99 except operands may include vector types when possible:

+	-	*	%	/	--
++	==	!=	&	~	^
>	<	>=	<=		!
&&		?:	>>	<<	=
,	op=	sizeof			

Address Space Qualifiers [6.5]

__global, global __local, local
__constant, constant __private, private

Function Qualifiers [6.7]

__kernel, kernel
__attribute__((vec_type_hint(*type*)))
 //*type* defaults to int
__attribute__((work_group_size_hint(*X*, *Y*, *Z*)))
__attribute__((reqd_work_group_size(*X*, *Y*, *Z*)))

Attribute Qualifiers [6.11]

Use to specify special attributes of enum, struct, and union types.

__attribute__((aligned(*n*))) __attribute__((endian(host)))
__attribute__((aligned)) __attribute__((endian(device)))
__attribute__((packed)) __attribute__((endian))

Use to specify special attributes of variables or structure fields.

__attribute__((aligned(*alignment*)))
__attribute__((nosvm))

Use to specify basic blocks and control-flow-statements.

__attribute__(((attr1)) {...})

Use to specify that a loop (for, while, and do loops) can be unrolled. (Must appear immediately before the loop to be affected.)

__attribute__((opencl_unroll_hint(*n*)))
__attribute__((opencl_unroll_hint))

Preprocessor Directives & Macros [6.10]

#pragma OPENCL_FP_CONTRACT <i>on-off-switch</i> <i>on-off-switch</i> : ON, OFF, DEFAULT	
__FILE__	Current source file
__func__	Current function name
__LINE__	Integer line number
__OPENCL_VERSION__	Integer version number, e.g: 200
__CL_VERSION_1_0	Substitutes integer 100 for 1.0
__CL_VERSION_1_1	Substitutes integer 110 for 1.1
__CL_VERSION_1_2	Substitutes integer 120 for 1.2
__CL_VERSION_2_0	Substitutes integer 200 for 2.0
__OPENCL_C_VERSION__	Sub. integer for OpenCL C version
__ENDIAN_LITTLE__	1 if device is little endian
__IMAGE_SUPPORT__	1 if images are supported
__FAST_RELAXED_MATH__	1 if -cl-fast-relaxed-math optimization option is specified
FP_FAST_FMA	Defined if double fma is fast
FP_FAST_FMAF	Defined if float fma is fast
FP_FAST_FMA_HALF	Defined if half fma is fast
__kernel_exec(<i>X</i> , <i>typen</i>)	Same as: __kernel __attribute__((work_group_size_hint(<i>X</i> , 1, 1))) __attribute__((vec_type_hint(<i>typen</i>)))

Blocks [6.12]

A result value type with a list of parameter types, similar to a function type. In this example:

1. The ^ declares variable “myBlock” is a Block.
2. The return type for the Block “myBlock” is int.
3. myBlock takes a single argument of type int.
4. The argument is named “num.”
5. Multiplier captured from block’s environment.

```

②    ①    ③
int (^myBlock)(int) =
    ^ (int num) {return num * multiplier;
    };
                    ④    ⑤

```

Work-Item Built-in Functions [6.13.1]

Query the number of dimensions, global, and local work size specified to `clEnqueueNDRangeKernel`, and global and local identifier of each work-item when this kernel is executed on a device. Sub-groups require the `cl_khr_subgroups` extension.

<code>uint get_work_dim ()</code>	Number of dimensions in use
<code>size_t get_global_size (uint dimindx)</code>	Number of global work-items
<code>size_t get_global_id (uint dimindx)</code>	Global work-item ID value
<code>size_t get_local_size (uint dimindx)</code>	Number of local work-items if kernel executed with uniform work-group size
<code>size_t get_enqueued_local_size (uint dimindx)</code>	Number of local work-items
<code>size_t get_local_id (uint dimindx)</code>	Local work-item ID
<code>size_t get_num_groups (uint dimindx)</code>	Number of work-groups

<code>size_t get_group_id (uint dimindx)</code>	Work-group ID
<code>size_t get_global_offset (uint dimindx)</code>	Global offset
<code>size_t get_global_linear_id ()</code>	Work-items 1-dimensional global ID
<code>size_t get_local_linear_id ()</code>	Work-items 1-dimensional local ID
<code>uint get_sub_group_size ()</code>	Number of work-items in the subgroup
<code>uint get_max_sub_group_size ()</code>	Maximum size of a subgroup
<code>uint get_num_sub_groups ()</code>	Number of subgroups
<code>uint get_enqueued_num_sub_groups ()</code>	
<code>uint get_sub_group_id ()</code>	Sub-group ID
<code>uint get_sub_group_local_id ()</code>	Unique work-item ID

Math Built-in Functions [6.13.2] [9.4.2]

T_s is type float, optionally double, or half if the `cl_khr_fp16` extension is enabled. T_n is the vector form of T_s , where n is 2, 3, 4, 8, or 16. T is T_s and T_n . All angles are in radians.

HN indicates that half and native variants are available using only the float or float n types by prepending “half_” or “native_” to the function name. Prototypes shown in brown text are available in half_ and native_ forms only using the float or float n types.

T acos (T)	Arc cosine
T acosh (T)	Inverse hyperbolic cosine
T acospi (T x)	acos (x) / π
T asin (T)	Arc sine
T asinh (T)	Inverse hyperbolic sine
T asinpi (T x)	asin (x) / π
T atan (T y_over_x)	Arc tangent
T atan2 (T y, T x)	Arc tangent of y / x
T atanh (T)	Hyperbolic arc tangent
T atanpi (T x)	atan (x) / π
T atan2pi (T x, T y)	atan2 (y , x) / π
T cbrt (T)	Cube root
T ceil (T)	Round to integer toward + infinity
T copysign (T x, T y)	x with sign changed to sign of y
T cos (T) HN	Cosine
T cosh (T)	Hyperbolic cosine
T cospi (T x)	cos (π x)
T half_divide (T x, T y) T native_divide (T x, T y)	x / y (T may only be float or float n)
T erfc (T)	Complementary error function
T erf (T)	Calculates error function of T
T exp (T x) HN	Exponential base e
T exp2 (T) HN	Exponential base 2
T exp10 (T) HN	Exponential base 10
T expm1 (T x)	$e^x - 1.0$
T fabs (T)	Absolute value
T fdim (T x, T y)	Positive difference between x and y
T floor (T)	Round to integer toward infinity
T fma (T a, T b, T c)	Multiply and add, then round
T fmax (T x, T y) T_n fmax (T_n x, T_s y)	Return y if $x < y$, otherwise it returns x

T fmin (T x, T y) T_n fmin (T_n x, T_s y)	Return y if $y < x$, otherwise it returns x
T fmod (T x, T y)	Modulus. Returns $x - y * \text{trunc}(x/y)$
T fract (T x, $T *iptr$)	Fractional value in x
T_s frexp (T x, int *exp) T_n frexp (T x, int n *exp)	Extract mantissa and exponent
T hypot (T x, T y)	Square root of $x^2 + y^2$
int[n] ilogb (T x)	Return exponent as an integer value
T_s ldexp (T x, int n) T_n ldexp (T x, int n n)	$x * 2^n$
T lgamma (T x) T_s lgamma_r (T_s x, int *signp) T_n lgamma_r (T_n x, int n *signp)	Log gamma function
T log (T) HN	Natural logarithm
T log2 (T) HN	Base 2 logarithm
T log10 (T) HN	Base 10 logarithm
T log1p (T x)	ln (1.0 + x)
T logb (T x)	Exponent of x
T mad (T a, T b, T c)	Approximates $a * b + c$
T maxmag (T x, T y)	Maximum magnitude of x and y
T minmag (T x, T y)	Minimum magnitude of x and y
T modf (T x, $T *iptr$)	Decompose floating-point number
float[n] nan (uint[n] nancode)	Quiet NaN (Return is scalar when nancode is scalar)
half[n] nan (ushort[n] nancode) double[n] nan (ulong[n] nancode)	Quiet NaN (Return is scalar when nancode is scalar)
T nextafter (T x, T y)	Next representable floating-point value after x in the direction of y
T pow (T x, T y)	Compute x to the power of y
T_s pown (T x, int y) T_n pown (T x, int n y)	Compute x^y , where y is an integer
T powr (T x, T y) HN	Compute x^y , where x is ≥ 0
T half_recip (T x) T native_recip (T x)	$1 / x$ (T may only be float or float n)
T remainder (T x, T y)	Floating point remainder
T_s remquo (T_s x, T_s y, int *quo) T_n remquo (T_n x, T_n y, int n *quo)	Remainder and quotient
T rint (T)	Round to nearest even integer
T_s rootn (T x, int y) T_n rootn (T x, int n y)	Compute x to the power of $1/y$

T round (T x)	Integral value nearest to x rounding
T rsqrt (T) HN	Inverse square root
T sin (T) HN	Sine
T sincos (T x, $T *cosval$)	Sine and cosine of x
T sinh (T)	Hyperbolic sine
T sinpi (T x)	sin (π x)
T sqrt (T) HN	Square root
T tan (T) HN	Tangent
T tanh (T)	Hyperbolic tangent
T tanpi (T x)	tan (π x)
T tgamma (T)	Gamma function
T trunc (T)	Round to integer toward zero

Math Constants [6.13.2] [9.4.2]

The values of the following symbolic constants are single-precision float.

MAXFLOAT	Value of maximum non-infinite single-precision floating-point number
HUGE_VALF	Positive float expression, evaluates to +infinity
HUGE_VAL OPTIONAL	Positive double expression, evals. to +infinity
INFINITY	Constant float expression, positive or unsigned infinity
NAN	Constant float expression, quiet NaN

When double precision is supported, macros ending in `_F` are available in type double by removing `_F` from the macro name, and in type half when the `cl_khr_fp16` extension is enabled by replacing `_F` with `_H`.

<code>M_E_F</code>	Value of e
<code>M_LOG2E_F</code>	Value of $\log_2 e$
<code>M_LOG10E_F</code>	Value of $\log_{10} e$
<code>M_LN2_F</code>	Value of $\log_e 2$
<code>M_LN10_F</code>	Value of $\log_e 10$
<code>M_PI_F</code>	Value of π
<code>M_PI_2_F</code>	Value of $\pi / 2$
<code>M_PI_4_F</code>	Value of $\pi / 4$
<code>M_1_PI_F</code>	Value of $1 / \pi$
<code>M_2_PI_F</code>	Value of $2 / \pi$
<code>M_2_SQRTPI_F</code>	Value of $2 / \sqrt{\pi}$
<code>M_SQRT2_F</code>	Value of $\sqrt{2}$
<code>M_SQRT1_2_F</code>	Value of $1 / \sqrt{2}$

Integer Built-in Functions [6.13.3]

T is type char, charn, uchar, uchar n , short, short n , ushort, ushort n , int, int n , uint, uint n , long, long n , ulong, or ulong n , where n is 2, 3, 4, 8, or 16. Tu is the unsigned version of T . Tsc is the scalar version of T .

Tu abs (Tx)	$ x $
Tu abs_diff (Tx, Ty)	$ x - y $ without modulo overflow
T add_sat (Tx, Ty)	$x + y$ and saturates the result
T hadd (Tx, Ty)	$(x + y) >> 1$ without mod. overflow
T rhadd (Tx, Ty)	$(x + y + 1) >> 1$
T clamp ($Tx, T min, T max$) T clamp ($Tx, Tsc min, Tsc max$)	$\min(\max(x, minval), maxval)$
T clz (Tx)	number of leading 0-bits in x
T ctz (Tx)	number of trailing 0-bits in x
T mad_hi (Ta, Tb, Tc)	$mul_hi(a, b) + c$
T mad_sat (Ta, Tb, Tc)	$a * b + c$ and saturates the result
T max (Tx, Ty) T max ($Tx, Tsc y$)	y if $x < y$, otherwise it returns x
T min (Tx, Ty) T min ($Tx, Tsc y$)	y if $y < x$, otherwise it returns x
T mul_hi (Tx, Ty)	high half of the product of x and y
T rotate (Tv, Ti)	$result[indx] = v[indx] << i[indx]$

T sub_sat (Tx, Ty)	$x - y$ and saturates the result
T popcount (Tx)	Number of non-zero bits in x
For upsample , return type is scalar when the parameters are scalar.	
short $[n]$ upsample (char $[n]$ hi , uchar $[n]$ lo)	$result[i] = ((short)hi[i] << 8) lo[i]$
ushort $[n]$ upsample (uchar $[n]$ hi , uchar $[n]$ lo)	$result[i] = ((ushort)hi[i] << 8) lo[i]$
int $[n]$ upsample (short $[n]$ hi , ushort $[n]$ lo)	$result[i] = ((int)hi[i] << 16) lo[i]$
uint $[n]$ upsample (ushort $[n]$ hi , ushort $[n]$ lo)	$result[i] = ((uint)hi[i] << 16) lo[i]$
long $[n]$ upsample (int $[n]$ hi , uint $[n]$ lo)	$result[i] = ((long)hi[i] << 32) lo[i]$
ulong $[n]$ upsample (uint $[n]$ hi , uint $[n]$ lo)	$result[i] = ((ulong)hi[i] << 32) lo[i]$

The following fast integer functions optimize the performance of kernels. In these functions, T is type int, uint, int n or int n , where n is 2, 3, 4, 8, or 16.

T mad24 (Tx, Ty, Tz)	Multiply 24-bit integer values x, y , add 32-bit int. result to 32-bit integer z
T mul24 (Tx, Ty)	Multiply 24-bit integer values x and y

Common Built-in Functions [6.13.4] [9.4.3]

These functions operate component-wise and use round to nearest even rounding mode. Ts is type float, optionally double, or half if cl_khr_fp16 is enabled. Tn is the vector form of Ts , where n is 2, 3, 4, 8, or 16. T is Ts and Tn .

T clamp ($Tx, T min, T max$) Tn clamp ($Tn x, Ts min, Ts max$)	Clamp x to range given by min, max
T degrees ($T radians$)	$radians$ to $degrees$
T max (Tx, Ty) Tn max ($Tn x, Ts y$)	Max of x and y
T min (Tx, Ty) Tn min ($Tn x, Ts y$)	Min of x and y
T mix (Tx, Ty, Ta) Tn mix ($Tn x, Tn y, Ts a$)	Linear blend of x and y
T radians ($T degrees$)	$degrees$ to $radians$
T step ($T edge, Tx$) Tn step ($Ts edge, Tn x$)	0.0 if $x < edge$, else 1.0
T smoothstep ($T edge0, T edge1, Tx$) T smoothstep ($Ts edge0, Ts edge1, Tn x$)	Step and interpolate
T sign (Tx)	Sign of x

Relational Built-in Functions [6.13.6]

These functions can be used with built-in scalar or vector types as arguments and return a scalar or vector integer result. T is type float, float n , char, charn, uchar, uchar n , short, short n , ushort, ushort n , int, int n , uint, uint n , long, long n , ulong, ulong n , or optionally double or doublen. Ti is type char, charn, short, short n , int, int n , long, or long n . Tu is type uchar, uchar n , ushort, ushort n , uint, uint n , long, or long n . n is 2, 3, 4, 8, or 16. **half** and **halfn** types require the cl_khr_fp16 extension [9.4.5].

int isequal (float x , float y) int n isequal (float n x , float n y) int isequal (double x , double y) long n isequal (doublen x , doublen y) int isequal (half x , half y) short n isequal (halfn x , halfn y)	Compare of $x == y$
int isnotequal (float x , float y) int n isnotequal (float n x , float n y) int isnotequal (double x , double y) long n isnotequal (doublen x , doublen y) int isnotequal (half x , half y) short n isnotequal (halfn x , halfn y)	Compare of $x != y$
int isgreater (float x , float y) int n isgreater (float n x , float n y) int isgreater (double x , double y) long n isgreater (doublen x , doublen y) int isgreater (half x , half y) short n isgreater (halfn x , halfn y)	Compare of $x > y$
int isgreaterequal (float x , float y) int n isgreaterequal (float n x , float n y) int isgreaterequal (double x , double y)	Compare of $x >= y$
long n isgreaterequal (doublen x , doublen y) int isgreaterequal (half x , half y) short n isgreaterequal (halfn x , halfn y)	Compare of $x >= y$
int isless (float x , float y) int n isless (float n x , float n y) int isless (double x , double y)	Compare of $x < y$

long n isless (doublen x , doublen y) int isless (half x , half y) short n isless (halfn x , halfn y)	Compare of $x < y$
int islessequal (float x , float y) int n islessequal (float n x , float n y) int islessequal (double x , double y) long n islessequal (doublen x , doublen y) int islessequal (half x , half y) short n islessequal (halfn x , halfn y)	Compare of $x <= y$
int islessgreater (float x , float y) int n islessgreater (float n x , float n y) int islessgreater (double x , double y) long n islessgreater (doublen x , doublen y) int islessgreater (half x , half y) short n islessgreater (halfn x , halfn y)	Compare of $(x < y) (x > y)$
int isfinite (float) int n isfinite (float n) int isfinite (double) long n isfinite (doublen) int isfinite (half) short n isfinite (halfn)	Test for finite value
int isinf (float) int n isinf (float n) int isinf (double) long n isinf (doublen) int isinf (half) short n isinf (halfn)	Test for + or - infinity
int isnan (float) int n isnan (float n)	Test for a NaN
int isnan (double) long n isnan (doublen) int isnan (half) short n isnan (halfn)	Test for a NaN
int isnormal (float) int n isnormal (float n) int isnormal (double)	Test for a normal value

long n isnormal (doublen) int isnormal (half) short n isnormal (halfn)	Test for a normal value
int isordered (float x , float y) int n isordered (float n x , float n y) int isordered (double x , double y) long n isordered (doublen x , doublen y) int isordered (half x , half y) short n isordered (halfn x , halfn y)	Test if arguments are ordered
int isunordered (float x , float y) int n isunordered (float n x , float n y) int isunordered (double x , double y) long n isunordered (doublen x , doublen y) int isunordered (half x , half y) short n isunordered (halfn x , halfn y)	Test if arguments are unordered
int signbit (float) int n signbit (float n) int signbit (double) long n signbit (doublen) int signbit (half) short n signbit (halfn)	Test for sign bit
int any ($Ti x$)	1 if MSB in component of x is set; else 0
int all ($Ti x$)	1 if MSB in all components of x are set; else 0
T bitselect ($Ta, Tb, Ti c$) half bitselect (half a , half b , half c) halfn bitselect (halfn a , halfn b , halfn c)	Each bit of result is corresponding bit of a if corresponding bit of c is 0
T select ($Ta, Tb, Ti c$) T select ($Ta, Tb, Tu c$) halfn select (halfn a , halfn b , short n c) half select (half a , half b , short c) halfn select (halfn a , halfn b , ushort n c) half select (half a , half b , ushort c)	For each component of a vector type, result $[i]$ = if MSB of $c[i]$ is set ? $b[i]$: $a[i]$ For scalar type, result = $c ? b : a$

Geometric Built-in Functions [6.13.5] [9.4.4]

Ts is scalar type float, optionally double, or half if the half extension is enabled. T is Ts and the 2-, 3-, or 4-component vector forms of Ts .

float{3,4} cross (float{3,4} $p0$, float{3,4} $p1$) double{3,4} cross (double{3,4} $p0$, double{3,4} $p1$) half{3,4} cross (half{3,4} $p0$, half{3,4} $p1$)	Cross product
--	---------------

Ts distance ($Tp0, Tp1$)	Vector distance
Ts dot ($Tp0, Tp1$)	Dot product
Ts length (Tp)	Vector length
T normalize (Tp)	Normal vector length 1

float fast_distance (float $p0$, float $p1$) float fast_distance (float n $p0$, float n $p1$)	Vector distance
float fast_length (float p) float fast_length (float n p)	Vector length
float fast_normalize (float p) float n fast_normalize (float n p)	Normal vector length 1

Vector Data Load/Store [6.13.7] [9.4.6]

T is type char, uchar, short, ushort, int, uint, long, ulong, or float, optionally double, or half if the `cl_khr_fp16` extension is enabled. Tn refers to the vector form of type T , where n is 2, 3, 4, 8, or 16. R defaults to current rounding mode, or is one of the rounding modes listed in 6.2.3.2.

<code>Tn vloadn</code> (size_t offset, const [constant] T^*p)	Read vector data from address ($p + (\text{offset} * n)$)
<code>void vstoren</code> (Tn data, size_t offset, T^*p)	Write vector data to address ($p + (\text{offset} * n)$)
<code>float vload_half</code> (size_t offset, const [constant] half $*p$)	Read a half from address ($p + \text{offset}$)
<code>floatn vload_halfn</code> (size_t offset, const [constant] half $*p$)	Read a halfn from address ($p + (\text{offset} * n)$)

<code>void vstore_half</code> (float data, size_t offset, half $*p$)	Write a half to address ($p + \text{offset}$)
<code>void vstore_half_R</code> (float data, size_t offset, half $*p$)	Write a half to address ($p + \text{offset}$)
<code>void vstore_half</code> (double data, size_t offset, half $*p$)	Write a half to address ($p + \text{offset}$)
<code>void vstore_half_R</code> (double data, size_t offset, half $*p$)	Write a half to address ($p + \text{offset}$)
<code>void vstore_halfn</code> (floatn data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)
<code>void vstore_halfn_R</code> (floatn data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)
<code>void vstore_halfn</code> (floatn data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)
<code>void vstore_halfn_R</code> (floatn data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)
<code>void vstore_halfn</code> (double data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)
<code>void vstore_halfn_R</code> (double data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)

<code>void vstore_halfn_R</code> (double data, size_t offset, half $*p$)	Write a half vector to address ($p + (\text{offset} * n)$)
<code>floatn vloada_halfn</code> (size_t offset, const [constant] half $*p$)	Read half vector data from ($p + (\text{offset} * n)$). For half3, read from ($p + (\text{offset} * 4)$).
<code>void vstorea_halfn</code> (floatn data, size_t offset, half $*p$)	Write half vector data to ($p + (\text{offset} * n)$). For half3, write to ($p + (\text{offset} * 4)$).
<code>void vstorea_halfn_R</code> (floatn data, size_t offset, half $*p$)	Write half vector data to ($p + (\text{offset} * n)$). For half3, write to ($p + (\text{offset} * 4)$).
<code>void vstorea_halfn</code> (double data, size_t offset, half $*p$)	Write half vector data to ($p + (\text{offset} * n)$). For half3, write to ($p + (\text{offset} * 4)$).
<code>void vstorea_halfn_R</code> (double data, size_t offset, half $*p$)	Write half vector data to ($p + (\text{offset} * n)$). For half3, write to ($p + (\text{offset} * 4)$).

Synchronization & Memory Fence Functions [6.13.8]

flags argument is the memory address space, set to a 0 or an OR'd combination of CLK_X_MEM_FENCE where X may be LOCAL, GLOBAL, or IMAGE. Memory fence functions provide ordering between memory operations of a work-item. Sub-groups require the `cl_khr_subgroups` extension.

<code>void work_group_barrier</code> (cl_mem_fence_flags flags[, memory_scope scope])	Work-items in a work-group must execute this before any can continue
<code>void atomic_work_item_fence</code> (cl_mem_fence_flags flags[, memory_scope scope])	Orders loads and stores of a work-item executing a kernel
<code>void sub_group_barrier</code> (cl_mem_fence_flags flags[, memory_scope scope])	Work-items in a sub-group must execute this before any can continue

Atomic Functions [6.13.11]

OpenCL C implements a subset of the C11 atomics (see section 7.17 of the C11 specification) and synchronization operations.

In the following tables, **A** refers to an atomic_ type (not including atomic_flag). **C** refers to its corresponding non-atomic type. **M** refers to the type of the other argument for arithmetic operations. For atomic integer types, **M** is **C**. For atomic pointer types, **M** is ptrdiff_t.

The type atomic_ * is a 32-bit integer. atomic_long and atomic_ulong require extension cl_khr_int64_base_atomics or cl_khr_int64_extended_atomics. The atomic_double type requires double precision support. The default scope is work_group for local atomics and all_svm_devices for global atomics. The extensions cl_khr_int64_base_atomics and cl_khr_int64_extended_atomics implement atomic operations on 64-bit signed and unsigned integers to locations in __global and __local memory.

See the table under Atomic Types and Enum Constants for information about parameter types memory_order, memory_scope, and memory_flag.

<code>void atomic_init</code> (volatile A *obj, C value)	Initializes the atomic object pointed to by obj to the value value.
<code>void atomic_work_item_fence</code> (cl_mem_fence_flags flags, memory_order order, memory_scope scope)	Effects based on value of order. flags must be CLK_(GLOBAL, LOCAL, IMAGE)_MEM_FENCE or a combination of these.
<code>void atomic_store</code> (volatile A *object, C desired)	Atomically replace the value pointed to by object with the value of desired. Memory is affected according to the value of order.
<code>void atomic_store_explicit</code> (volatile A *object, C desired, memory_order order[, memory_scope scope])	Atomically replace the value pointed to by object with the value of desired. Memory is affected according to the value of order.
<code>C atomic_load</code> (volatile A *object)	Atomically returns the value pointed to by object. Memory is affected according to the value of order.
<code>C atomic_load_explicit</code> (volatile A *object, memory_order order[, memory_scope scope])	Atomically returns the value pointed to by object. Memory is affected according to the value of order.
<code>C atomic_exchange</code> (volatile A *object, C desired)	Atomically replace the value pointed to by object with desired. Memory is affected according to the value of order.
<code>C atomic_exchange_explicit</code> (volatile A *object, C desired, memory_order order[, memory_scope scope])	Atomically replace the value pointed to by object with desired. Memory is affected according to the value of order.
<code>bool atomic_compare_exchange_strong</code> (volatile A *object, C *expected, C desired)	Atomically compares the value pointed to by object for equality with that in expected, and if true, replaces the value pointed to by object with desired, and if false, updates the value in expected with the value pointed to by object.
<code>bool atomic_compare_exchange_strong_explicit</code> (volatile A *object, C *expected, C desired, memory_order success, memory_order failure[, memory_scope scope])	Atomically compares the value pointed to by object for equality with that in expected, and if true, replaces the value pointed to by object with desired, and if false, updates the value in expected with the value pointed to by object.
<code>bool atomic_compare_exchange_weak</code> (volatile A *object, C *expected, C desired)	These operations are atomic read-modify-write operations.
<code>bool atomic_compare_exchange_weak_explicit</code> (volatile A *object, C *expected, C desired, memory_order success, memory_order failure[, memory_scope scope])	These operations are atomic read-modify-write operations.
<code>C atomic_fetch_<key></code> (volatile A *object, M operand)	Atomically replaces the value pointed to by object with the result of the computation applied to the value pointed to by object and the given operand.
<code>C atomic_fetch_<key>_explicit</code> (volatile A *object, M operand, memory_order order[, memory_scope scope])	Atomically replaces the value pointed to by object with the result of the computation applied to the value pointed to by object and the given operand.

Async Copies and Prefetch [6.13.10] [9.4.7]

T is type char, charn, uchar, uchar, short, shortn, ushort, ushortn, int, intrn, uint, uintn, long, longn, ulong, ulongn, float, floatn, optionally double or doublen, or half or halfn if the `cl_khr_fp16` extension is enabled.

<code>event_t async_work_group_copy</code> (__local T^*dst , const __global T^*src , size_t num_gentypes, event_t event)	Copies num_gentypes T elements from src to dst
<code>event_t async_work_group_copy</code> (__global T^*dst , const __local T^*src , size_t num_gentypes, event_t event)	
<code>event_t async_work_group_strided_copy</code> (__local T^*dst , const __global T^*src , size_t num_gentypes, size_t src_stride, event_t event)	
<code>event_t async_work_group_strided_copy</code> (__global T^*dst , const __local T^*src , size_t num_gentypes, size_t dst_stride, event_t event)	
<code>void wait_group_events</code> (int num_events, event_t *event_list)	Wait for completion of async_work_group_copy
<code>void prefetch</code> (const __global T^*p , size_t num_gentypes)	Prefetch num_gentypes * sizeof(T) bytes into global cache

<code>bool atomic_flag_test_and_set</code> (volatile atomic_flag *object)	Atomically sets the value pointed to by object to true. Memory is affected according to the value of order. Returns atomically, the value of the object immediately before the effects.
<code>bool atomic_flag_test_and_set_explicit</code> (volatile atomic_flag *object, memory_order order[, memory_scope scope])	Atomically sets the value pointed to by object to true. Memory is affected according to the value of order. Returns atomically, the value of the object immediately before the effects.
<code>void atomic_flag_clear</code> (volatile atomic_flag *object)	Atomically sets the value pointed to by object to false. The order argument shall not be memory_order_acquire nor memory_order_acq_rel. Memory is affected according to the value of order.
<code>void atomic_flag_clear_explicit</code> (volatile atomic_flag *object, memory_order order[, memory_scope scope])	Atomically sets the value pointed to by object to false. The order argument shall not be memory_order_acquire nor memory_order_acq_rel. Memory is affected according to the value of order.

Values for key for atomic_fetch and modify functions

key	op	computation	key	op	computation
add	+	addition	and	&	bitwise and
sub	-	subtraction	min	min	compute min
or		bitwise inclusive or	max	max	compute max
xor	^	bitwise exclusive or			

Atomic Types and Enum Constants

memory_scope_sub_group requires the `cl_khr_subgroups` extension.

Parameter Type	Values
memory_order	memory_order_relaxed memory_order_acq_rel memory_order_seq_cst
memory_scope	memory_scope_work_item memory_scope_work_group memory_scope_sub_group memory_scope_all_svm_devices memory_scope_device (default for functions that do not take a memory_scope argument)

Atomic integer and floating-point types

† indicates types supported by a limited subset of atomic operations

‡ indicates size depends on whether implemented on 64-bit or 32-bit architecture.

§ indicates types supported only if both 64-bit extensions are supported.

atomic_int	atomic_long §	atomic_float †	atomic_intptr_t ‡§	atomic_size_t ‡§
atomic_uint	atomic_ulong §	atomic_double ‡§	atomic_uintptr_t ‡§	atomic_ptrdiff_t ‡§
atomic_flag				

Atomic Macros

#define ATOMIC_VAR_INIT(C value)	Expands to a token sequence to initialize an atomic object of a type that is initialization-compatible with value.
#define ATOMIC_FLAG_INIT	Initialize an atomic_flag to the clear state.

Address Space Qualifier Functions [6.13.9]

T refers to any of the built-in data types supported by OpenCL C or a user-defined type.

[const] global <i>T</i> * to_global ([const] <i>T</i> * <i>ptr</i>)	global address space
[const] local <i>T</i> * to_local ([const] <i>T</i> * <i>ptr</i>)	local address space
[const] private <i>T</i> * to_private ([const] <i>T</i> * <i>ptr</i>)	private address space
[const] cl_mem_fence_flags get_fence ([const] <i>T</i> * <i>ptr</i>)	Memory fence value: CLK_GLOBAL_MEM_FENCE, CLK_LOCAL_MEM_FENCE

printf Function [6.13.13]

Writes output to an implementation-defined stream.

int **printf** (constant char **restrict format*, ...)

printf output synchronization

When the event associated with a particular kernel invocation completes, the output of applicable **printf** calls is flushed to the implementation-defined output stream.

printf format string

The format string follows C99 conventions and supports an optional vector specifier:

%[flags][width][.precision][vector][length] *conversion*

Examples:

The following examples show the use of the vector specifier in the **printf** format string.

```
float4 f = (float4)(1.0f, 2.0f, 3.0f, 4.0f);
printf("f4 = %2.2v4f\n", f);
```

Output: f4 = 1.00,2.00,3.00,4.00

```
uchar4 uc = (uchar4)(0xFA, 0xFB, 0xFC, 0xFD);
printf("uc = %v4x\n", uc);
```

Output: uc = 0xfa,0xfb,0xfc,0xfd

```
uint2 ui = (uint2)(0x12345678, 0x87654321);
printf("unsigned short value = (%v2hx)\n", ui);
```

Output: unsigned short value = (0x5678,0x4321)

Miscellaneous Vector Functions [6.13.12]

Tm and *Tn* are type charn, uchar, shortn, ushortn, intn, uintn, longn, ulongn, floatn, optionally doublen, or halfn if the **cl_khr_fp16** extension is supported, where *n* is 2,4,8, or 16 except in **vec_step** it may also be 3. *TUn* is uchar, ushortn, uintn, or ulongn.

int vec_step (<i>Tm a</i>) int vec_step (<i>typename</i>)	Takes built-in scalar or vector data type argument. Returns 1 for scalar, 4 for 3-component vector, else number of elements in the specified type.
<i>Tn</i> shuffle (<i>Tm x</i> , <i>TUn mask</i>) <i>Tn</i> shuffle2 (<i>Tm x</i> , <i>Tm y</i> , <i>TUn mask</i>)	Construct permutation of elements from one or two input vectors, return a vector with same element type as input and length that is the same as the shuffle mask.

Enqueuing and Kernel Query Built-in Functions [6.13.17] [9.17.3.6]

A kernel may enqueue code represented by Block syntax, and control execution order with event dependencies including user events and markers. There are several advantages to using the Block syntax: it is more compact; it does not require a cl_kernel object; and enqueueing can be done as a single semantic step. Sub-groups require the **cl_khr_subgroups** extension. The macro CLK_NULL_EVENT refers to an invalid device event. The macro CLK_NULL_QUEUE refers to an invalid device queue.

```
int enqueue_kernel (queue_t queue, kernel_enqueue_flags_t flags,  
const ndrange_t ndrange, void (^block)(void))
```

```
int enqueue_kernel (queue_t queue, kernel_enqueue_flags_t flags,  
const ndrange_t ndrange, uint num_events_in_wait_list,  
const clk_event_t *event_wait_list, clk_event_t *event_ret,  
void (^block)(void))
```

```
int enqueue_kernel (queue_t queue, kernel_enqueue_flags_t flags,  
const ndrange_t ndrange,  
void (^block)(local void *, ...), uint size0, ...)
```

```
int enqueue_kernel (queue_t queue, kernel_enqueue_flags_t flags,  
const ndrange_t ndrange,  
uint num_events_in_wait_list, const clk_event_t *event_wait_list,  
clk_event_t *event_ret, void (^block)(local void *, ...), uint size0, ...)
```

Allows a work-item to enqueue a block for execution to *queue*. Work-items can enqueue multiple blocks to a device queue(s).

flags may be one of CLK_ENQUEUE_FLAGS_{NO_WAIT, WAIT_KERNEL, WAIT_WORK_GROUP}

Workgroup Functions [6.13.15] [9.17.3.4]

T is type int, uint, long, ulong, or float, optionally double, or half if the **cl_khr_fp16** extension is supported. Sub-groups require the **cl_khr_subgroups** extension. Double and vector types require double precision support.

Returns a non-zero value if *predicate* evaluates to non-zero for all or any workitems in the work-group or sub-group.

```
int work_group_all (int predicate)
```

```
int work_group_any (int predicate)
```

```
int sub_group_all (int predicate)
```

```
int sub_group_any (int predicate)
```

Return result of reduction operation specified by <*op*> for all values of *x* specified by workitems in work-group or sub-group. <*op*> may be min, max, or add.

```
T work_group_reduce <op> (T x)
```

```
T sub_group_reduce <op> (T x)
```

Broadcast the value of *a* to all work-items in the work-group or sub-group. *local_id* must be the same value for all workitems in the work-group. *n* may be 2 or 3.

```
T work_group_broadcast (T a, size_t local_id)
```

```
T work_group_broadcast (T a, size_t local_id_x,  
size_t local_id_y)
```

```
T work_group_broadcast (T a, size_t local_id_x,  
size_t local_id_y, size_t local_id_z)
```

```
T sub_group_broadcast (T x, size_t local_id)
```

Do an exclusive or inclusive scan operation specified by <*op*> of all values specified by work-items in the work-group or sub-group. The scan results are returned for each work-item. <*op*> may be min, max, or add.

```
T work_group_scan_exclusive <op> (T x)
```

```
T work_group_scan_inclusive <op> (T x)
```

```
T sub_group_scan_exclusive <op> (T x)
```

```
T sub_group_scan_inclusive <op> (T x)
```

Pipe Built-in Functions [6.13.16.2-4]

T represents the built-in OpenCL C scalar or vector integer or floating-point data types or any user defined type built from these scalar and vector data types. Half scalar and vector types require the **cl_khr_fp16** extension. Sub-groups require the **cl_khr_subgroups** extension. Double or vector double types require double precision support. The macro CLK_NULL_RESERVE_ID refers to an invalid reservation ID.

int read_pipe (__read_only pipe <i>T p</i> , <i>T</i> * <i>ptr</i>)	Read packet from <i>p</i> into <i>ptr</i> .
int read_pipe (__read_only pipe <i>T p</i> , reserve_id_t <i>reserve_id</i> , uint <i>index</i> , <i>T</i> * <i>ptr</i>)	Read packet from reserved area of the pipe <i>reserve_id</i> and <i>index</i> into <i>ptr</i> .
int write_pipe (__write_only pipe <i>T p</i> , const <i>T</i> * <i>ptr</i>)	Write packet specified by <i>ptr</i> to <i>p</i> .
int write_pipe (__write_only pipe <i>T p</i> , reserve_id_t <i>reserve_id</i> , uint <i>index</i> , const <i>T</i> * <i>ptr</i>)	Write packet specified by <i>ptr</i> to reserved area <i>reserve_id</i> and <i>index</i> .

bool is_valid_reserve_id (reserve_id_t <i>reserve_id</i>)	Return true if <i>reserve_id</i> is a valid reservation ID and false otherwise.
reserve_id_t reserve_read_pipe (__read_only pipe <i>T p</i> , uint <i>num_packets</i>)	Reserve <i>num_packets</i> entries for reading from or writing to <i>p</i> .
reserve_id_t reserve_write_pipe (__write_only pipe <i>T p</i> , uint <i>num_packets</i>)	
void commit_read_pipe (__read_only pipe <i>T p</i> , reserve_id_t <i>reserve_id</i>)	Indicates that all reads and writes to <i>num_packets</i> associated with reservation <i>reserve_id</i> are completed.
void commit_write_pipe (__write_only pipe <i>T p</i> , reserve_id_t <i>reserve_id</i>)	
uint get_pipe_max_packets (pipe <i>T p</i>)	Returns maximum number of packets specified when <i>p</i> was created.
uint get_pipe_num_packets (pipe <i>T p</i>)	Returns the number of available entries in <i>p</i> .

void work_group_commit_read_pipe (pipe <i>T p</i> , reserve_id_t <i>reserve_id</i>) void work_group_commit_write_pipe (pipe <i>T p</i> , reserve_id_t <i>reserve_id</i>) void sub_group_commit_read_pipe (pipe <i>T p</i> , reserve_id_t <i>reserve_id</i>) void sub_group_commit_write_pipe (pipe <i>T p</i> , reserve_id_t <i>reserve_id</i>)	Indicates that all reads and writes to <i>num_packets</i> associated with reservation <i>reserve_id</i> are completed.
reserve_id_t work_group_reserve_read_pipe (pipe <i>T p</i> , uint <i>num_packets</i>) reserve_id_t work_group_reserve_write_pipe (pipe <i>T p</i> , uint <i>num_packets</i>) reserve_id_t sub_group_reserve_read_pipe (pipe <i>T p</i> , uint <i>num_packets</i>) reserve_id_t sub_group_reserve_write_pipe (pipe <i>T p</i> , uint <i>num_packets</i>)	Reserve <i>num_packets</i> entries for reading from or writing to <i>p</i> . Returns a valid reservation ID if the reservation is successful.

uint get_kernel_work_group_size (void (^ <i>block</i>)(void)) uint get_kernel_work_group_size (void (^ <i>block</i>)(local void *, ...))	Query the maximum work-group size that can be used to execute a block.
uint get_kernel_preferred_work_group_size_multiple (void (^ <i>block</i>)(void)) uint get_kernel_preferred_work_group_size_multiple (void (^ <i>block</i>)(local void *, ...))	Returns the preferred multiple of work-group size for launch.
int enqueue_marker (queue_t <i>queue</i> , uint <i>num_events_in_wait_list</i> , const clk_event_t * <i>event_wait_list</i> , clk_event_t * <i>event_ret</i>)	Enqueue a marker command to <i>queue</i> .
uint get_kernel_sub_group_count_for_ndrange (const ndrange_t <i>ndrange</i> , void (^ <i>block</i>)(void)) uint get_kernel_sub_group_count_for_ndrange (const ndrange_t <i>ndrange</i> , void (^ <i>block</i>)(local void *, ...))	Returns number of subgroups in each workgroup of the dispatch.
uint get_kernel_max_sub_group_size_for_ndrange (const ndrange_t <i>ndrange</i> , void (^ <i>block</i>)(void)) uint get_kernel_max_sub_group_size_for_ndrange (const ndrange_t <i>ndrange</i> , void (^ <i>block</i>)(local void *, ...))	Returns the maximum sub-group size for a block.

Event Built-in Functions [6.13.17.8]

T is type int, uint, long, ulong, or float, optionally double, or half if the `cl_khr_fp16` extension is enabled.

<code>void retain_event (clk_event_t event)</code>	Increments event reference count.
<code>void release_event (clk_event_t event)</code>	Decrements event reference count.
<code>clk_event_t create_user_event ()</code>	Create a user event.
<code>bool is_valid_event (clk_event_t event)</code>	True for valid event.
<code>void set_user_event_status (clk_event_t event, int status)</code>	Sets the execution status of a user event. <i>status</i> : CL_COMPLETE or a negative error value.
<code>void capture_event_profiling_info (clk_event_t event, clk_profiling_info name, global void *value)</code>	Captures profiling information for command associated with <i>event</i> in value.

Helper Built-in Functions [6.13.17.9]

<code>queue_t get_default_queue (void)</code>	Default queue or CLK_NULL_QUEUE
<code>ndrange_t ndrange_1D (size_t global_work_size)</code> <code>ndrange_t ndrange_1D (size_t global_work_size, size_t local_work_size)</code> <code>ndrange_t ndrange_1D (size_t global_work_offset, size_t global_work_size, size_t local_work_size)</code>	Builds a 1D ND-range descriptor.
<code>ndrange_t ndrange_nD (const size_t global_work_size[n])</code> <code>ndrange_t ndrange_nD (size_t global_work_size, const size_t local_work_size[n])</code> <code>ndrange_t ndrange_nD (const size_t global_work_offset, const size_t global_work_size, const size_t local_work_size[n])</code>	Builds a 2D or 3D ND-range descriptor. <i>n</i> may be 2 or 3.

OpenCL Image Processing Reference

A subset of the OpenCL API 2.1 and C Language 2.0 specifications pertaining to image processing and graphics.

Image Objects

Items in blue apply when the appropriate extension is supported.

Create Image Objects [5.3.1]

`cl_mem clCreateImage (cl_context context, cl_mem_flags flags, const cl_image_format *image_format, const cl_image_desc *image_desc, void *host_ptr, cl_int *errcode_ret)`
flags: See `clCreateBuffer`

Query List of Supported Image Formats [5.3.2]

`cl_int clGetSupportedImageFormats (cl_context context, cl_mem_flags flags, cl_mem_object_type image_type, cl_uint num_entries, cl_image_format *image_formats, cl_uint *num_image_formats)`
flags: See `clCreateBuffer`

image_type: CL_MEM_OBJECT_IMAGE1D, 2D, 3D, CL_MEM_OBJECT_IMAGE1D_BUFFER, CL_MEM_OBJECT_IMAGE1D_ARRAY

Read, Write, Copy, Fill Image Objects [5.3.3-4]

`cl_int clEnqueueReadImage (cl_command_queue command_queue, cl_mem image, cl_bool blocking_read, const size_t *origin, const size_t *region, size_t row_pitch, size_t slice_pitch, void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueWriteImage (cl_command_queue command_queue, cl_mem image, cl_bool blocking_write, const size_t *origin, const size_t *region, size_t input_row_pitch, size_t input_slice_pitch, const void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueFillImage (cl_command_queue command_queue, cl_mem image, const void *fill_color, const size_t *origin, const size_t *region, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueCopyImage (cl_command_queue command_queue, cl_mem src_image, cl_mem dst_image, const size_t *src_origin, const size_t *dst_origin, const size_t *region, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)`

Copy Between Image, Buffer Objects [5.3.5]

`cl_int clEnqueueCopyImageToBuffer (cl_command_queue command_queue, cl_mem src_image, cl_mem dst_buffer, const size_t *src_origin, const size_t *region, size_t dst_offset, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueCopyBufferToImage (cl_command_queue command_queue, cl_mem src_buffer, cl_mem dst_image, size_t src_offset, const size_t *dst_origin, const size_t *region, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)`

Map and Unmap Image Objects [5.3.6]

`void * clEnqueueMapImage (cl_command_queue command_queue, cl_mem image, cl_bool blocking_map, cl_map_flags map_flags, const size_t *origin, const size_t *region, size_t *image_row_pitch, size_t *image_slice_pitch, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event, cl_int *errcode_ret)`
map_flags: CL_MAP_READ, CL_MAP_WRITE, CL_MAP_WRITE_INVALIDATE_REGION

Query Image Objects [5.3.7]

`cl_int clGetImageInfo (cl_mem image, cl_image_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)`
param_name: [Table 5.10] CL_IMAGE_FORMAT, CL_IMAGE_ARRAY_ELEMENT_SIZE, CL_IMAGE_ROW_SLICE_PITCH, CL_IMAGE_HEIGHT_WIDTH_DEPTH, CL_IMAGE_NUM_SAMPLES_MIP_LEVELS, CL_IMAGE_DX9_MEDIA_PLANE_KHR, CL_IMAGE_D3D10_D3D11_SUBRESOURCE_KHR

Image Formats [5.3.1.1]

Supported image formats: `image_channel_order` with `image_channel_data_type`.

Built-in support [Table 5.8]

CL_R (read or write): CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16, CL_SNORM_INT8,16, CL_SIGNED_INT8,16,32, CL_UNSIGNED_INT8,16,32

CL_DEPTH (read or write): CL_FLOAT, CL_UNORM_INT16

CL_DEPTH_STENCIL (read only): CL_FLOAT, CL_UNORM_INT24
(Requires the extension `cl_khr_gl_depth_images`)

CL_RG (read or write): CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16, CL_SNORM_INT8,16, CL_SIGNED_INT8,16,32, CL_UNSIGNED_INT8,16,32

CL_RGBA (read or write): CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16, CL_SNORM_INT8,16, CL_SIGNED_INT8,16,32, CL_UNSIGNED_INT8,16,32

CL_BGRA (read or write): CL_UNORM_INT8

CL_sRGBA (read only): CL_UNORM_INT8
(Requires the extension `cl_khr_srgb_image_writes`)

Optional support [Table 5.6]

CL_R, CL_A (read and write): CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16, CL_SIGNED_INT8,16,32, CL_UNSIGNED_INT8,16,32, CL_SNORM_INT8,16

CL_INTENSITY: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16, CL_SNORM_INT8,16

CL_DEPTH_STENCIL: Only used if extension `cl_khr_gl_depth_images` is enabled and channel data type = CL_UNORM_INT24 or CL_FLOAT

CL_LUMINANCE: CL_UNORM_INT8,16, CL_HALF_FLOAT, CL_FLOAT, CL_SNORM_INT8,16

CL_RG, CL_RA: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16, CL_SIGNED_INT8,16,32, CL_UNSIGNED_INT8,16,32, CL_SNORM_INT8,16

CL_RGB: CL_UNORM_SHORT_555,565, CL_UNORM_INT101010

CL_ARGB: CL_UNORM_INT8, CL_SIGNED_INT8, CL_UNSIGNED_INT8, CL_SNORM_INT8

CL_BGRA: CL_SIGNED, UNSIGNED, INT8, CL_SNORM_INT8

Notes

Image Read and Write Functions [6.13.14]

The built-in functions defined in this section can only be used with image memory objects created with `clCreateImage`. `sampler` specifies the addressing and filtering mode to use. `aQual` refers to one of the access qualifiers. For samplerless read functions this may be `read_only` or `read_write`.

- Writes to images with sRGB channel orders requires device support of the `cl_khr_srgb_image_writes` extension.
- `read_imageh` and `write_imageh` require the `cl_khr_fp16` extension.
- MSAA images require the `cl_khr_gl_msaa_sharing` extension.
- Image 3D writes require the extension `cl_khr_3d_image_writes`. [9.4.8]

Read and write functions for 2D images

Read an element from a 2D image, or write a color value to a location in a 2D image.

```
float4 read_imagef(read_only image2d_t image,
                  sampler_t sampler, {int2, float2} coord)

int4 read_imagei(read_only image2d_t image,
                  sampler_t sampler, {int2, float2} coord)

uint4 read_imageui(read_only image2d_t image,
                   sampler_t sampler, {int2, float2} coord)

float4 read_imagef(read_only image2d_array_t image,
                  sampler_t sampler, {int4, float4} coord)

int4 read_imagei(read_only image2d_array_t image,
                  sampler_t sampler, {int4, float4} coord)

uint4 read_imageui(read_only image2d_array_t image,
                   sampler_t sampler, {int4, float4} coord)

float read_imagef(read_only image2d_depth_t image,
                  sampler_t sampler, {int2, float2} coord)

float read_imagef(read_only image2d_array_depth_t image,
                  sampler_t sampler, {int4, float4} coord)

float4 read_imagef(aQual image2d_t image, int2 coord)

int4 read_imagei(aQual image2d_t image, int2 coord)

uint4 read_imageui(aQual image2d_t image, int2 coord)

float4 read_imagef(aQual image2d_array_t image, int4 coord)

int4 read_imagei(aQual image2d_array_t image, int4 coord)

uint4 read_imageui(aQual image2d_array_t image, int4 coord)

float read_imagef(aQual image2d_depth_t image, int2 coord)

float read_imagef(aQual image2d_array_depth_t image,
                  int4 coord)

half4 read_imageh(read_only image2d_t image,
                  sampler_t sampler, {int2, float2} coord)

half4 read_imageh(aQual image2d_t image, int2 coord)

half4 read_imageh(read_only image2d_array_t image,
                  sampler_t sampler, {int4, float4} coord)

half4 read_imageh(aQual image2d_array_t image,
                  int4 coord)

void write_imagef(aQual image2d_t image,
                  int2 coord, float4 color)

void write_imagei(aQual image2d_t image,
                  int2 coord, int4 color)

void write_imageui(aQual image2d_t image,
                  int2 coord, uint4 color)

void write_imageh(aQual image2d_t image,
                  int2 coord, half4 color)

void write_imagef(aQual image2d_array_t image,
                  int4 coord, float4 color)

void write_imagei(aQual image2d_array_t image,
                  int4 coord, int4 color)

void write_imageui(aQual image2d_array_t image,
                  int4 coord, uint4 color)
```

```
void write_imagef(aQual image2d_depth_t image,
                  int2 coord, float depth)
```

```
void write_imagef(aQual image2d_array_depth_t image,
                  int4 coord, float depth)
```

```
void write_imageh(aQual image2d_array_t image,
                  int4 coord, half4 color)
```

Read and write functions for 1D images

Read an element from a 1D image, or write a color value to a location in a 1D image.

```
float4 read_imagef(read_only image1d_t image,
                  sampler_t sampler, {int, float} coord)

int4 read_imagei(read_only image1d_t image,
                  sampler_t sampler, {int, float} coord)

uint4 read_imageui(read_only image1d_t image,
                   sampler_t sampler, {int, float} coord)

float4 read_imagef(read_only image1d_array_t image,
                  sampler_t sampler, {int2, float2} coord)

int4 read_imagei(read_only image1d_array_t image,
                  sampler_t sampler, {int2, float2} coord)

uint4 read_imageui(read_only image1d_array_t image,
                   sampler_t sampler, {int2, float2} coord)

float4 read_imagef(aQual image1d_t image, int coord)

float4 read_imagef(aQual image1d_buffer_t image, int coord)

int4 read_imagei(aQual image1d_t image, int coord)

uint4 read_imageui(aQual image1d_t image, int coord)

int4 read_imagei(aQual image1d_buffer_t image, int coord)

uint4 read_imageui(aQual image1d_buffer_t image, int coord)

half4 read_imageh(read_only image1d_t image,
                  sampler_t sampler, {int, float} coord)

half4 read_imageh(aQual image1d_t image, int coord)

half4 read_imageh(read_only image1d_array_t image,
                  sampler_t sampler, {int2, float2} coord)

half4 read_imageh(aQual image1d_array_t image, int2 coord)

half4 read_imageh(aQual image1d_buffer_t image, int coord)
```

```
void write_imagef(aQual image1d_t image,
                  int coord, float4 color)
```

```
void write_imagei(aQual image1d_t image,
                  int coord, int4 color)
```

```
void write_imageui(aQual image1d_t image,
                  int coord, uint4 color)
```

```
void write_imageh(aQual image1d_t image,
                  int coord, half4 color)
```

```
void write_imagef(aQual image1d_buffer_t image,
                  int coord, float4 color)
```

```
void write_imagei(aQual image1d_buffer_t image,
                  int coord, int4 color)
```

```
void write_imageui(aQual image1d_buffer_t image,
                  int coord, uint4 color)
```

```
void write_imageh(aQual image1d_buffer_t image,
                  int coord, half4 color)
```

```
void write_imagef(aQual image1d_array_t image,
                  int2 coord, float4 color)
```

```
void write_imagei(aQual image1d_array_t image,
                  int2 coord, int4 color)
```

```
void write_imageui(aQual image1d_array_t image,
                  int2 coord, uint4 color)
```

```
void write_imageh(aQual image1d_array_t image,
                  int2 coord, half4 color)
```

Read and write functions for 3D images

Read an element from a 3D image, or write a color value to a location in a 3D image. [Writing to 3D images requires the `cl_khr_3d_image_writes` extension \[9.4.8\].](#)

```
float4 read_imagef(read_only image3d_t image,
                  sampler_t sampler, {int4, float4} coord)

int4 read_imagei(read_only image3d_t image,
                  sampler_t sampler, int4 coord)

int4 read_imagei(read_only image3d_t image,
                  sampler_t sampler, float4 coord)

uint4 read_imageui(read_only image3d_t image,
                   sampler_t sampler, {int4, float4} coord)

float4 read_imagef(aQual image3d_t image, int4 coord)

int4 read_imagei(aQual image3d_t image, int4 coord)

uint4 read_imageui(aQual image3d_t image, int4 coord)

half4 read_imageh(read_only image3d_t image,
                  sampler_t sampler, {int4, float4} coord)

half4 read_imageh(aQual image3d_t image, int4 coord)
```

```
void write_imagef(aQual image3d_t image,
                  int4 coord, float4 color)
```

```
void write_imagei(aQual image3d_t image,
                  int4 coord, int4 color)
```

```
void write_imageui(aQual image3d_t image,
                  int4 coord, uint4 color)
```

```
void write_imageh(aQual image3d_t image,
                  int4 coord, half4 color)
```

Extended mipmap read and write functions [9.17.2.1]

These functions require the `cl_khr_mipmap_image` and `cl_khr_mipmap_image_writes` extensions.

```
float read_imagef(read_only image2d_[depth_]t image,
                  sampler_t sampler, float2 coord, float lod)

int4 read_imagei(read_only image2d_t image,
                  sampler_t sampler, float2 coord, float lod)

uint4 read_imageui(read_only image2d_t image,
                   sampler_t sampler, float2 coord, float lod)

float read_imagef(read_only image2d_[depth_]t image,
                  sampler_t sampler, float2 coord, float2 gradient_x,
                  float2 gradient_y)

int4 read_imagei(read_only image2d_t image,
                  sampler_t sampler, float2 coord, float2 gradient_x,
                  float2 gradient_y)

uint4 read_imageui(read_only image2d_t image,
                   sampler_t sampler, float2 coord, float2 gradient_x,
                   float2 gradient_y)

float4 read_imagef(read_only image1d_t image,
                  sampler_t sampler, float coord, float lod)

int4 read_imagei(read_only image1d_t image,
                  sampler_t sampler, float coord, float lod)

uint4 read_imageui(read_only image1d_t image,
                   sampler_t sampler, float coord, float lod)

float4 read_imagef(read_only image1d_t image,
                  sampler_t sampler, float coord, float gradient_x,
                  float gradient_y)

int4 read_imagei(read_only image1d_t image,
                  sampler_t sampler, float coord, float gradient_x,
                  float gradient_y)

uint4 read_imageui(read_only image1d_t image,
                   sampler_t sampler, float coord, float gradient_x,
                   float gradient_y)

float4 read_imagef(read_only image3d_t image,
                  sampler_t sampler, float4 coord, float lod)

int4 read_imagei(read_only image3d_t image,
                  sampler_t sampler, float4 coord, float lod)

uint4 read_imageui(read_only image3d_t image,
                   sampler_t sampler, float4 coord, float lod)

float4 read_imagef(read_only image3d_t image,
                  sampler_t sampler, float4 coord, float4 gradient_x,
                  float4 gradient_y)
```

(Continued on next page >)

Image Read and Write (continued)

Extended mipmap read and write functions (cont'd)

```
int4 read_imagei(read_only image3d_t image,
  sampler_t sampler, float4 coord, float4 gradient_x,
  float4 gradient_y)

uint4 read_imageui(read_only image3d_t image,
  sampler_t sampler, float4 coord, float4 gradient_x,
  float4 gradient_y)

float4 read_imagef(read_only image1d_array_t image,
  sampler_t sampler, float2 coord, float lod)

int4 read_imagei(read_only image1d_array_t image,
  sampler_t sampler, float2 coord, float lod)

uint4 read_imageui(read_only image1d_array_t image,
  sampler_t sampler, float2 coord, float lod)

float4 read_imagef(read_only image1d_array_t image,
  sampler_t sampler, float2 coord, float gradient_x,
  float gradient_y)

int4 read_imagei(read_only image1d_array_t image,
  sampler_t sampler, float2 coord, float gradient_x,
  float gradient_y)

uint4 read_imageui(read_only image1d_array_t image,
  sampler_t sampler, float2 coord, float gradient_x,
  float gradient_y)

float read_imagef(read_only image2d_array_t image,
  sampler_t sampler, float4 coord, float lod)

int4 read_imagei(read_only image2d_array_t image,
  sampler_t sampler, float4 coord, float lod)
```

```
uint4 read_imageui(read_only image2d_array_t image,
  sampler_t sampler, float4 coord, float lod)

float read_imagef(read_only image2d_array_t image,
  sampler_t sampler, float4 coord, float2 gradient_x,
  float2 gradient_y)

int4 read_imagei(read_only image2d_array_t image,
  sampler_t sampler, float4 coord, float2 gradient_x,
  float2 gradient_y)

uint4 read_imageui(read_only image2d_array_t image,
  sampler_t sampler, float4 coord, float2 gradient_x,
  float2 gradient_y)

void write_imagef(aQual image2d_t image,
  int2 coord, int lod, float4 color)

void write_imagei(aQual image2d_t image,
  int2 coord, int lod, int4 color)

void write_imageui(aQual image2d_t image,
  int2 coord, int lod, uint4 color)

void write_imagef(aQual image1d_t image,
  int coord, int lod, float4 color)

void write_imagei(aQual image1d_t image,
  int coord, int lod, int4 color)

void write_imageui(aQual image1d_t image,
  int coord, int lod, uint4 color)

void write_imagef(aQual image1d_array_t image,
  int2 coord, int lod, float4 color)

void write_imagei(aQual image1d_array_t image,
  int2 coord, int lod, int4 color)

void write_imageui(aQual image1d_array_t image,
  int2 coord, int lod, uint4 color)
```

```
void write_imagef(aQual image2d_array_t image,
  int4 coord, int lod, float4 color)

void write_imagei(aQual image2d_array_t image,
  int4 coord, int lod, int4 color)

void write_imageui(aQual image2d_array_t image,
  int4 coord, int lod, uint4 color)

void write_imagef(aQual image3d_t image,
  int4 coord, int lod, float4 color)

void write_imagei(aQual image3d_t image,
  int4 coord, int lod, int4 color)

void write_imageui(aQual image3d_t image,
  int4 coord, int lod, uint4 color)
```

Extended multi-sample image read functions [9.12.3]

The extension `cl_khr_gl_msaa_sharing` adds the following built-in functions.

```
float read_imagef(aQual image2d_msaa_depth_t image,
  int2 coord, int sample)

float read_imagef(aQual image2d_array_depth_msaa_t image,
  int4 coord, int sample)

float4 read_imagef(i, ui) (image2d_msaa_t image,
  int2 coord, int sample)

float4 read_imagef(i, ui) (image2d_array_msaa_t image,
  int4 coord, int sample)
```

Image Query Functions [6.13.14.5] [9.12]

The MSAA forms require the extension `cl_khr_gl_msaa_sharing`. Mipmap requires the extension `cl_khr_mipmap_image`.

Query image width, height, and depth in pixels

```
int get_image_width(aQual image{1,2,3}d_t image)
int get_image_width(aQual image1d_buffer_t image)
int get_image_width(aQual image{1,2}d_array_t image)
int get_image_width(aQual image2d_array_depth_t image)
int get_image_width(aQual image2d_array_msaa_t image)
int get_image_width(aQual image2d_array_msaa_depth_t image)

int get_image_height(aQual image{2,3}d_t image)
int get_image_height(aQual image2d_array_t image)
int get_image_height(aQual image2d_array_depth_t image)
int get_image_height(aQual image2d_array_msaa_t image)
int get_image_height(aQual image2d_array_msaa_depth_t image)

int get_image_depth(image3d_t image)
```

Query image array size

```
size_t get_image_array_size(aQual image1d_array_t image)
size_t get_image_array_size(aQual image2d_array_t image)
size_t get_image_array_size(aQual image2d_array_depth_t image)
size_t get_image_array_size(aQual image2d_array_msaa_depth_t image)
```

Query image dimensions

```
int2 get_image_dim(aQual image2d_t image)
int2 get_image_dim(aQual image2d_array_t image)
int4 get_image_dim(aQual image3d_t image)

int2 get_image_dim(aQual image2d_array_depth_t image)
int2 get_image_dim(aQual image2d_array_msaa_t image)
int2 get_image_dim(aQual image2d_array_msaa_depth_t image)
```

Query image Channel data type and order

```
int get_image_channel_data_type(aQual image{1,2,3}d_t image)
int get_image_channel_data_type(aQual image1d_buffer_t image)
int get_image_channel_data_type(aQual image{1,2}d_array_t image)
int get_image_channel_data_type(aQual image2d_array_depth_t image)
int get_image_channel_data_type(aQual image2d_array_msaa_t image)
int get_image_channel_data_type(aQual image2d_array_msaa_depth_t image)

int get_image_channel_order(aQual image{1,2,3}d_t image)
int get_image_channel_order(aQual image1d_buffer_t image)
int get_image_channel_order(aQual image{1,2}d_array_t image)
int get_image_channel_order(aQual image2d_array_depth_t image)
int get_image_channel_order(aQual image2d_array_msaa_t image)
int get_image_channel_order(aQual image2d_array_msaa_depth_t image)
```

Extended query functions [9.18.2.1]

These functions require the `cl_khr_mipmap_image` extension.

```
int get_image_num_mip_levels(aQual image1d_t image)
int get_image_num_mip_levels(aQual image2d_depth_t image)
int get_image_num_mip_levels(aQual image3d_t image)
int get_image_num_mip_levels(aQual image1d_array_t image)
int get_image_num_mip_levels(aQual image2d_array_depth_t image)

int get_image_num_samples(aQual image2d_array_msaa_t image)
int get_image_num_samples(aQual image2d_array_msaa_depth_t image)
```

Access Qualifiers [6.6]

Apply to 2D and 3D image types to declare if the image memory object is being read or written by a kernel.

```
__read_only, read_only
__write_only, write_only
```

Sampler Objects [5.7]

Items in blue require the `cl_khr_mipmap_image` extension.

```
cl_sampler clCreateSamplerWithProperties(
  cl_context context,
  const cl_sampler_properties *sampler_properties,
  cl_int *errcode_ret)
```

sampler_properties: [Table 5.15]

```
CL_SAMPLER_NORMALIZED_COORDS,
CL_SAMPLER_ADDRESSING_MODE,
CL_SAMPLER_MIP_FILTER_MODE,
CL_SAMPLER_LOD_MIN, CL_SAMPLER_LOD_MAX
```

```
cl_int clRetainSampler(cl_sampler sampler)
```

```
cl_int clReleaseSampler(cl_sampler sampler)
```

```
cl_int clGetSamplerInfo(cl_sampler sampler,
  cl_sampler_info param_name,
  size_t param_value_size, void *param_value,
  size_t *param_value_size_ret)
```

param_name: CL_SAMPLER_REFERENCE_COUNT, CL_SAMPLER_CONTEXT, CL_SAMPLER_ADDRESSING_MODE, CL_SAMPLER_NORMALIZED_COORDS [Table 5.16]

Sampler Declaration Fields [6.13.14.1]

The sampler can be passed as an argument to the kernel using `clSetKernelArg`, or can be declared in the outermost scope of kernel functions, or it can be a constant variable of type `sampler_t` declared in the program source.

```
const sampler_t <sampler-name> =
  <normalized-mode> | <address-mode> | <filter-mode>
```

normalized-mode: CLK_NORMALIZED_COORDS_{TRUE, FALSE}

address-mode: CLK_ADDRESS_X, where X may be NONE, REPEAT, CLAMP, CLAMP_TO_EDGE, MIRROR, MIRROR_REPEAT

filter-mode: CLK_FILTER_NEAREST, CLK_FILTER_LINEAR

OpenCL Extensions Reference

Section and table references are to the OpenCL Extensions 2.1 specification.

Using OpenCL Extensions [9]

The following extensions extend the OpenCL API. Extensions shown in *italics* provide core features.

To control an extension: `#pragma OPENCL EXTENSION extension_name : {enable | disable}`

To test if an extension is supported, use `clGetPlatformInfo()` or `clGetDeviceInfo()`

To get the address of the extension function: `clGetExtensionFunctionAddressForPlatform()`

<code>cl_apple_gl_sharing</code> (see <code>cl_khr_gl_sharing</code>)
<code>cl_khr_3d_image_writes</code>
<code>cl_khr_byte_addressable_store</code>
<code>cl_khr_context_abort</code>
<code>cl_khr_d3d10_sharing</code>

<code>cl_khr_d3d11_sharing</code>
<code>cl_khr_depth_images</code>
<code>cl_khr_device_enqueue_local_arg_types</code>
<code>cl_khr_dx9_media_sharing</code>
<code>cl_khr_egl_event</code>
<code>cl_khr_egl_image</code>
<code>cl_khr_fp16</code>
<code>cl_khr_fp64</code>
<code>cl_khr_gl_depth_images</code>
<code>cl_khr_gl_event</code>
<code>cl_khr_gl_msaa_sharing</code>
<code>cl_khr_gl_sharing</code>
<code>cl_khr_global_int32_base_atomics - atomic_*</code>
<code>cl_khr_global_int32_extended_atomics - atomic_*</code>
<code>cl_khr_icd</code>

<code>cl_khr_image2d_from_buffer</code>
<code>cl_khr_initialize_memory</code>
<code>cl_khr_int64_base_atomics - atom_*</code>
<code>cl_khr_int64_extended_atomics - atom_*</code>
<code>cl_khr_local_int32_base_atomics - atomic_*</code>
<code>cl_khr_local_int32_extended_atomics - atomic_*</code>
<code>cl_khr_mipmap_image</code>
<code>cl_khr_mipmap_image_writes</code>
<code>cl_khr_priority_hints</code>
<code>cl_khr_srgb_image_writes</code>
<code>cl_khr_spir</code>
<code>cl_khr_subgroups</code>
<code>cl_khr_terminate_context</code>
<code>cl_khr_throttle_hints</code>

OpenGL Sharing [9.5 - 9.7]

These functions require the `cl_khr_gl_sharing` or `cl_apple_gl_sharing` extension.

CL Context > GL Context, Sharegroup [9.5.5]

```
cl_int clGetGLContextInfoKHR (
    const cl_context_properties *properties,
    cl_gl_context_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
param_name: CL_DEVICES_FOR_GL_CONTEXT_KHR,
             CL_CURRENT_DEVICE_FOR_GL_CONTEXT_KHR
```

CL Buffer Objects > GL Buffer Objects [9.6.2]

```
cl_mem clCreateFromGLBuffer (cl_context context,
    cl_mem_flags flags, GLuint bufobj, cl_int *errcode_ret)
flags: CL_MEM_READ_ONLY, WRITE_ONLY, READ_WRITE
```

CL Image Objects > GL Textures [9.6.3]

```
cl_mem clCreateFromGLTexture (cl_context context,
    cl_mem_flags flags, GLenum texture_target,
    GLint miplevel, GLuint texture, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
texture_target: GL_TEXTURE_{1D,2D}[_ARRAY],
                GL_TEXTURE_{3D,BUFFER,RECTANGLE},
                GL_TEXTURE_CUBE_MAP_POSITIVE_{X,Y,Z},
                GL_TEXTURE_CUBE_MAP_NEGATIVE_{X,Y,Z},
                GL_TEXTURE_2D_MULTISAMPLE[_ARRAY] (Requires
                extension cl_khr_gl_msaa_sharing)
```

DX9 Media Surface Sharing [9.9]

The header file is `cl_dx9_media_sharing.h`. Enable the extension `cl_khr_dx9_media_sharing`.

```
cl_int clGetDeviceIDsFromDX9MediaAdapterKHR (
    cl_platform_id platform, cl_uint num_media_adapters,
    cl_dx9_media_adapter_type_khr *media_adapters_type,
    void *media_adapters,
    cl_dx9_media_adapter_set_khr media_adapter_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_int *num_devices)
media_adapter_type:
    CL_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR
media_adapter_set: CL_{ALL, PREFERRED}_DEVICES_
    FOR_DX9_MEDIA_ADAPTER_KHR
cl_mem clCreateFromDX9MediaSurfaceKHR (
    cl_context context, cl_mem_flags flags,
    cl_dx9_media_adapter_type_khr adapter_type,
    void *surface_info, cl_uint plane, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
adapter_type: CL_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR
cl_int clEnqueue{Acquire, Release}DX9MediaSurfacesKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

CL Image Objects > GL Renderbuffers [9.6.4]

```
cl_mem clCreateFromGLRenderbuffer (
    cl_context context, cl_mem_flags flags,
    GLuint renderbuffer, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
```

Query Information [9.6.5]

```
cl_int clGetGLObjectInfo (cl_mem memobj,
    cl_object_type *gl_object_type,
    GLuint *gl_object_name)
*gl_object_type returns:
    CL_GL_OBJECT_TEXTURE_BUFFER,
    CL_GL_OBJECT_TEXTURE_{1D, 2D, 3D},
    CL_GL_OBJECT_TEXTURE_{1D, 2D}_ARRAY,
    CL_GL_OBJECT_{BUFFER, RENDERBUFFER}
cl_int clGetGLTextureInfo (cl_mem memobj,
    cl_gl_texture_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
param_name: CL_GL_{TEXTURE_TARGET,
                MIPMAP_LEVEL}, CL_GL_NUM_SAMPLES
(Requires extension cl_khr_gl_msaa_sharing)
```

Share Objects [9.6.6]

```
cl_int clEnqueue{Acquire, Release}GLObjects (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

CL Event Objects > GL Sync Objects [9.7.4]

```
cl_event clCreateEventFromGLSyncKHR (
    cl_context context, GLsync sync,
    cl_int *errcode_ret)
Requires the cl_khr_egl_event extension.
```

Direct3D 11 Sharing [9.10.7.3 - 9.10.7.6]

These functions require the `cl_khr_d3d11_sharing` extension. Associated header file is `cl_d3d11.h`.

```
cl_int clGetDeviceIDsFromD3D11KHR (
    cl_platform_id platform,
    cl_d3d11_device_source_khr d3d_device_source,
    void *d3d_object,
    cl_d3d11_device_set_khr d3d_device_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_int *num_devices)
d3d_device_source: CL_D3D11_DEVICE_KHR,
                   CL_D3D11_DXGI_ADAPTER_KHR
d3d_device_set: CL_ALL_DEVICES_FOR_D3D11_KHR,
                CL_PREFERRED_DEVICES_FOR_D3D11_KHR
cl_mem clCreateFromD3D11BufferKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Buffer *resource, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
```

Direct3D 10 Sharing [9.8.7]

These functions require the `cl_khr_d3d10_sharing` extension. The associated header file is `cl_d3d10.h`.

```
cl_int clGetDeviceIDsFromD3D10KHR (
    cl_platform_id platform,
    cl_d3d10_device_source_khr d3d_device_source,
    void *d3d_object,
    cl_d3d10_device_set_khr d3d_device_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_int *num_devices)
d3d_device_source:
    CL_D3D10_{DEVICE, DXGI_ADAPTER}_KHR
d3d_device_set:
    CL_{ALL, PREFERRED}_DEVICES_FOR_D3D10_KHR
cl_mem clCreateFromD3D10BufferKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Buffer *resource, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
cl_mem clCreateFromD3D10Texture2DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Texture2D *resource, UINT subresource,
    cl_int *errcode_ret)
flags: See clCreateFromD3D10BufferKHR
cl_mem clCreateFromD3D10Texture3DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Texture3D *resource, UINT subresource,
    cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
cl_int clEnqueue{Acquire, Release}D3D10ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_mem clCreateFromD3D11Texture3DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Texture3D *resource, UINT subresource,
    cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
```

```
cl_mem clCreateFromD3D11Texture2DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Texture2D *resource,
    UINT subresource, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
```

```
cl_int clEnqueue{Acquire, Release}D3D11ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

EGL Interoperability [9.18, 9.19]

Create CL Image Objects from EGL

These functions require the extension `cl_khr_egl_image`.

```
cl_mem clCreateFromEGLImageKHR (
    cl_context context, CLEGLDisplayKHR display,
    CLEGLImageKHR image, cl_mem_flags flags,
    const cl_egl_image_properties_khr *properties,
    cl_int *errcode_ret)
```

```
cl_int clEnqueue{Acquire, Release}EGLObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list,
    cl_event *event)
```

Create CL Event Objects from EGL

This function requires the extension `cl_khr_egl_event`.

```
cl_event clCreateEventFromEGLSyncKHR (
    cl_context context, CLEGLSyncKHR sync,
    CLEGLDisplayKHR display, cl_int *errcode_ret)
```

Example of Enqueuing Kernels

Arguments that are a pointer type to local address space [6.13.17.2]

A block passed to `enqueue_kernel` can have arguments declared to be a pointer to local memory. The `enqueue_kernel` built-in function variants allow blocks to be enqueued with a variable number of arguments. Each argument must be declared to be a void pointer to local memory. These `enqueue_kernel` built-in function variants also have a corresponding number of arguments each of type `uint` that follow the block argument. These arguments specify the size of each local memory pointer argument of the enqueued block.

```
kernel void
my_func_A_local_arg1(global int *a, local int *lptr, ...)
{
    ...
}

kernel void
my_func_A_local_arg2(global int *a,
    local int *lptr1, local float4 *lptr2, ...)
{
    ...
}

kernel void
my_func_B(global int *a, ...)
{
    ...
    ndrange_t ndrange = ndrange_1d(...);
    uint local_mem_size = compute_local_mem_size();
    enqueue_kernel(get_default_queue(),
        CLK_ENQUEUE_FLAGS_WAIT_KERNEL,
        ndrange,
        ^(local void *p){
            my_func_A_local_arg1(a, (local int *)p, ...);},
        local_mem_size);
}

kernel void
my_func_C(global int *a, ...)
{
    ...
    ndrange_t ndrange = ndrange_1d(...);
    void (^my_blk_A)(local void *, local void *) =
        ^(local void *lptr1, local void *lptr2){
            my_func_A_local_arg2(
                a,
                (local int *)lptr1,
                (local float4 *)lptr2, ...);};
    // calculate local memory size for lptr
    // argument in local address space for my_blk_A
    uint local_mem_size = compute_local_mem_size();
    enqueue_kernel(get_default_queue(),
        CLK_ENQUEUE_FLAGS_WAIT_KERNEL,
        ndrange,
        my_blk_A,
        local_mem_size, local_mem_size * 4);
}
```

A Complete Example [6.13.17.3]

The example below shows how to implement an iterative algorithm where the host enqueues the first instance of the nd-range kernel (`dp_func_A`). The kernel `dp_func_A` will launch a kernel (`evaluate_dp_work_A`) that will determine if new nd-range work needs to be performed. If new nd-range work does need to be performed, then `evaluate_dp_work_A` will enqueue a new instance of `dp_func_A`. This process is repeated until all the work is completed.

```
kernel void
dp_func_A(queue_t q, ...)
{
    ...
    // queue a single instance of evaluate_dp_work_A to
    // device queue q. queued kernel begins execution after
    // kernel dp_func_A finishes

    if (get_global_id(0) == 0)
    {
        enqueue_kernel(q,
            CLK_ENQUEUE_FLAGS_WAIT_KERNEL,
            ndrange_1d(1),
            ^{evaluate_dp_work_A(q, ...)});
    }
}

kernel void
evaluate_dp_work_A(queue_t q,...)
{
    // check if more work needs to be performed
    bool more_work = check_new_work(...);
    if (more_work)
    {
        size_t global_work_size = compute_global_size(...);
        void (^dp_func_A_blk)(void) =
            ^{dp_func_A(q, ...)};

        // get local WG-size for kernel dp_func_A
        size_t local_work_size =
            get_kernel_work_group_size(dp_func_A_blk);

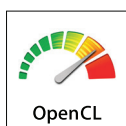
        // build nd-range descriptor
        ndrange_t ndrange = ndrange_1D(global_work_size,
            local_work_size);

        // enqueue dp_func_A
        enqueue_kernel(q,
            CLK_ENQUEUE_FLAGS_WAIT_KERNEL,
            ndrange,
            dp_func_A_blk);
    }
    ...
}
```


Notes

The following index shows the page number for each item included in this guide. The color of the row in the table below is the color of the box to which you should refer.

A		clEnqueueReadBuffer[Rect]	2	clRetainProgram	3	Markers, Barriers, Waiting for Events	4
Access Qualifiers	12	clEnqueueReadImage	10	clRetainSampler	12	Math Built-in Functions	6
Address Space Qualifiers	5	clEnqueueReleaseD3D10ObjectsKHR	13	clSetDefaultDeviceCommandQueue	1	Math Constants	6
Address Space Qualifier Functions	9	clEnqueueReleaseD3D11ObjectsKHR	13	clSetEventCallback	4	Memory Fence Functions	8
Architecture Diagram	2	clEnqueueReleaseDX9MediaSurfacesKHR	13	clSetKernelArg	3	Memory Objects	2
Async Copies and Prefetch	8	clEnqueueReleaseEGLOObjectsKHR	13	clSetKernelArgSVMPointer	3	Migrate Memory Objects	2
Atomic Functions	8	clEnqueueReleaseGLOObjects	12	clSetKernelExecInfo	3	O	
Attribute Qualifiers	5	clEnqueueSVM[Un]Map	3	clSetMemObjectDestructorCallback	2	OpenCL Class Diagram	2
B		clEnqueueSVMFree	3	clSetUserEventStatus	4	OpenCL Extensions	13
Barriers	4	clEnqueueSVMMem{cpy, Fill}	3	clSVMAlloc	3	OpenGL Sharing	13
Blocks	6	clEnqueueUnmapMemObject	2	clSVMFree	3	Operators	5
Buffer Objects	2	clEnqueueWriteBuffer[Rect]	2	clTerminateContextKHR	1	Optimization options	3
C		clEnqueueWriteImage	10	clUnloadPlatformCompiler	3	P	
cl_khr_*	13	clFinish	3	clWaitForEvents	4	Partitioning a Device	1
clBuildProgram	3	clFlush	3	Code Examples	14	Pipe Built-in Functions	9
clCloneKernel	3	clGetCommandQueueInfo	1	Command Queues	1	Pipes	2
clCompileProgram	3	clGetContextInfo	1	Common Built-in Functions	7	Prefetch	8
clCreateBuffer	2	clGetDeviceIDs	1	Compiler Options	3	Preprocessor	3
clCreateCommandQueueWithProperties	1	clGetDeviceIDsFromD3D10KHR	13	Contexts	1	Preprocessor Directives & Macros	5
clCreateContext	1	clGetDeviceIDsFromD3D11KHR	13	Conversions and Type Casting	2	printf Function	9
clCreateContextFromType	1	clGetDeviceIDsFromDX9MediaAdapterKHR	13	Copy Between Image, Buffer Objects	10	Profiling Operations	4
clCreateEventFromEGLsyncKHR	13	clGetDeviceInfo	1	D		Program linking options	3
clCreateEventFromGLsyncKHR	13	clGetDeviceAndHostTimer	1	Data Types	5	Program Objects	3
clCreateFromD3D10BufferKHR	13	clGetEventInfo	4	Debugging options	3	Q	
clCreateFromD3D10Texture2DKHR	13	clGetEventProfilingInfo	4	Device Architecture Diagram	2	Qualifiers	5
clCreateFromD3D10Texture3DKHR	13	clGetExtensionFunctionAddressForPlatform	1	Direct3D 10 Sharing	13	Query Image Objects	10
clCreateFromD3D11BufferKHR	13	clGetGLContextInfoKHR	13	Direct3D 11 Sharing	13	Query Image Functions	12
clCreateFromD3D11Texture2DKHR	13	clGetGLObjectInfo	13	DX9 Media Surface Sharing	13	Query List of Supported Image Formats	10
clCreateFromD3D11Texture3DKHR	13	clGetGLTextureInfo	13	E - F		Query Memory Object	2
clCreateFromDX9MediaSurfaceKHR	13	clGetHostTimer	1	EGL Interoperability	13	Query Program Objects	3
clCreateFromEGLImageKHR	13	clGetImageInfo	10	Enqueuing & Kernel Query Built-in Functions	9	Querying Platform Info & Devices	1
clCreateFromGLBuffer	13	clGetKernelArgInfo	3	Enqueuing Kernels Code Examples	14	R	
clCreateFromGLRenderbuffer	13	clGetKernelInfo	3	Event Built-in Functions	10	Read, Write, Copy Buffer Objects	2
clCreateFromGLTexture	13	clGetKernelSubGroupInfo	4	Event Objects	4	Read, Write, Copy, Fill Image Objects	10
clCreateImage	10	clGetKernelWorkGroupInfo	3	Execute Kernels	4	Relational Built-in Functions	7
clCreateKernel	3	clGetMemObjectInfo	2	Extension Function Pointers	1	S - T	
clCreateKernelsInProgram	3	clGetPipeInfo	2	Extensions	13	Sampler Objects, Declaration Fields	12
clCreatePipe	2	clGetPlatformIDs	1	Fence Functions	8	Scalar Data Types	5
clCreateProgramWithBinary	3	clGetPlatformInfo	1	Flush and Finish	3	Separate Compilation and Linking	3
clCreateProgramWithBuiltInKernels	3	clGetProgramBuildInfo	3	Function Qualifiers	5	Shared Virtual Memory	3,4
clCreateProgramWithIL	3	clGetProgramInfo	3	G - H		SPIR binary options	3
clCreateProgramWithSource	3	clGetSamplerInfo	12	Geometric Built-in Functions	7	Supported Data Types	5
clCreateSamplerWithProperties	12	clGetSupportedImageFormats	10	Helper Built-in Functions	10	SVM Sharing Granularity	3
clCreateSubBuffer	2	clIcdGetPlatformIDsKHR	1	I		Synchronization & Memory Fence Functions	8
clCreateSubDevices	1	clLinkProgram	3	Image Formats	10	Type Casting Examples	2
clCreateUserEvent	4	clReleaseCommandQueue	1	Image Objects	10	Types	5
clEnqueueAcquireD3D10ObjectsKHR	13	clReleaseContext	1	Image Query Functions	12	U - V	
clEnqueueAcquireD3D11ObjectsKHR	13	clReleaseDevice	1	Image Read and Write Functions	11-12	Unload the OpenCL Compiler	3
clEnqueueAcquireDX9MediaSurfacesKHR	13	clReleaseEvent	4	Integer Built-in Functions	7	Unroll attribute qualifiers	5
clEnqueueAcquireEGLOObjectsKHR	13	clReleaseKernel	3	K		Vector Component Addressing	5
clEnqueueAcquireGLOObjects	13	clReleaseMemObject	2	Kernel Arguments and Queries	3	Vector Data Load/Store	8
clEnqueueBarrierWithWaitList	4	clReleaseProgram	3	Kernel Objects	3	Vector Functions	9
clEnqueueCopyBuffer[Rect]	2	clReleaseSampler	12	Kernel Query Built-in Functions	9	Vector Data Types	5
clEnqueueCopyBufferToImage	10	clRetainCommandQueue	1	L		W	
clEnqueueCopyImage	10	clRetainContext	1	Library linking options	3	Waiting for Events	4
clEnqueueCopyImageToBuffer	10	clRetainDevice	1	Linker Options	3	Warning request/suppress	3
clEnqueueFillBuffer	2	clRetainEvent	4	M		Workgroup Functions	9
clEnqueueFillImage	10	clRetainKernel	3	Map and Unmap Image Objects	10	Work-Item Built-in Functions	6
clEnqueueMapBuffer	2	clRetainMemObject	2	Map Buffer Objects	2		
clEnqueueMapImage	10						
clEnqueueMarkerWithWaitList	4						
clEnqueueMigrateMemObjects	2						
clEnqueueNativeKernel	4						
clEnqueueNDRangeKernel	4						



KHRONOS™
GROUP

The Khronos Group is an industry consortium creating open standards for the authoring and acceleration of parallel computing, graphics and dynamic media on a wide variety of platforms and devices. See www.khronos.org to learn more about the Khronos Group.

OpenCL is a trademark of Apple Inc. and is used under license by Khronos.