

TURBOT Dual-E

Rev : R200

TABLE OF CONTENTS

Page	Index	Page	Index
1	Cover Page	26	DC-DC VOLTAGE
2	Block Diagram	27	M.2 Interface (USB 2.0, SIM, SATA)
3	CPU-DDI/VGA	28	GPIO/HW Mapping
4	CPU-DDR3-CHA	29	POWER DELIVERY
5	CPU-DDR3-CHB	30	Power Sequence
6	CPU-PE/HDA/SD/eMMC/Strap		
7	CPU-SPI/CLOCK/RTC/PMC/UART		
8	CPU-USB/LPC/SMB/I2C		
9	CPU-PWR1		
10	CPU-PWR2		
11	CPU-GND		
12	Memory_CHA-1		
13	Memory_CHA-2		
14	uSD CARD CAGE		
15	HDMI / VGA		
16	USB3+USB2		
17	EXPANSION I/O		
18	LAN1 Intel i210/i211		
19	LAN2 Intel i210/i211		
20	CSI/GPIO Header/UART_DBG		
21	PWR BTN / LED / HOLE		
22	DC-5VSB/3VSB / VCC / VCC3		
23	DC/DC VCORE REGULATOR		
24	DC/DC VGFX REGULATOR		
25	DC-DIMM/DDR_VTT		

REVISION HISTORY:

Rev	Date	Notes
X100	2016/04/29	Prototype Release
R100	2016/10/19	Production Release
R200	2016/12/16	Changed M.2 RESET# from 3.3V to 1.8V

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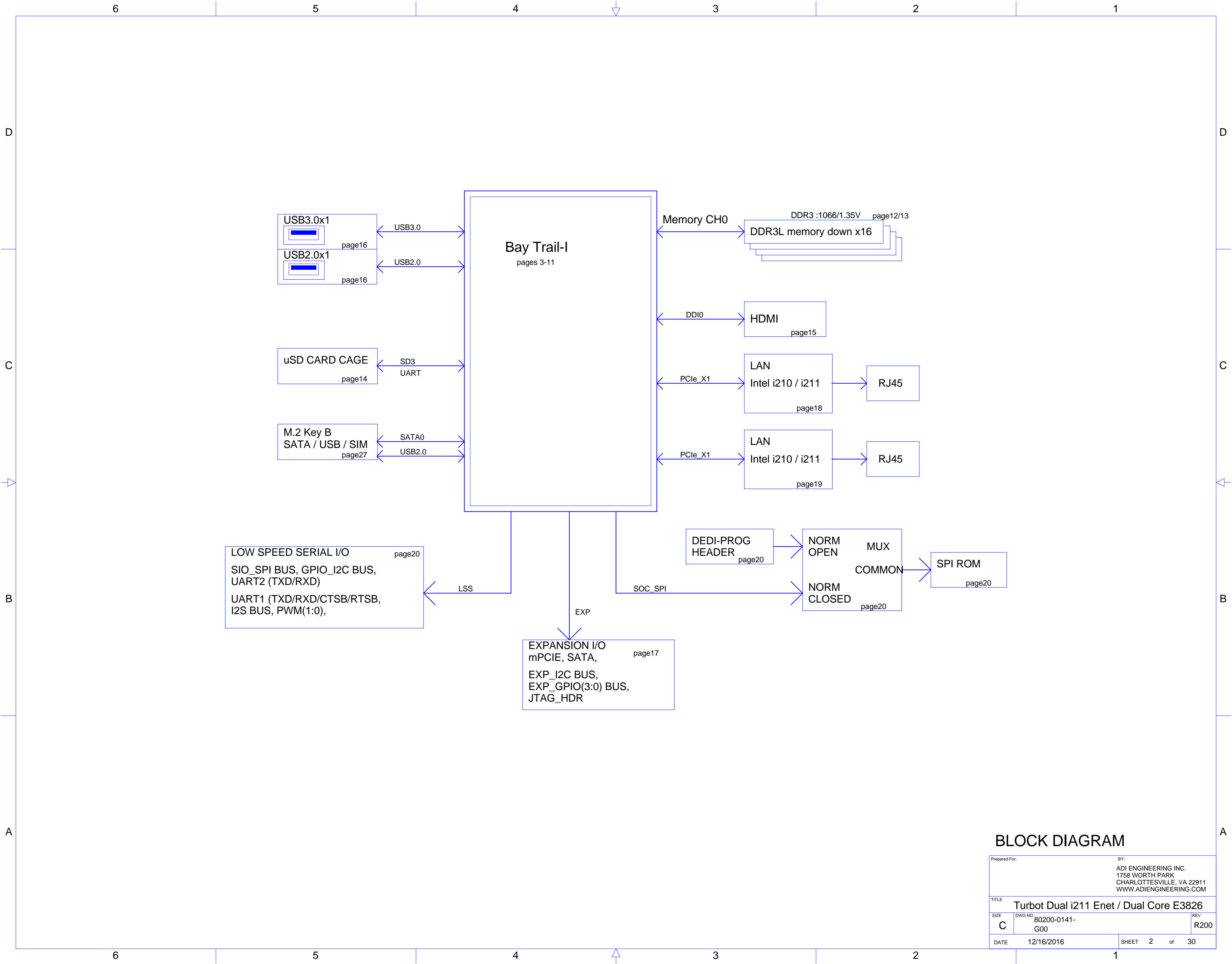
Minnowboard Turbot Dual-E design derived from Minnowboard Turbot B as Designed by ADI Engineering, A Division of Silicom and Minnowboard MAX as designed by CircuitCo LLC.

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TITLE Turbot Dual i211 Enet / Dual Core E3826			
SIZE C	DWG NO 80200-0141-G00	REV R200	
DATE 04/17/2017		SHEET 1	of 30



BLOCK DIAGRAM

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DATE 12/16/2016		SHEET	2 of 30

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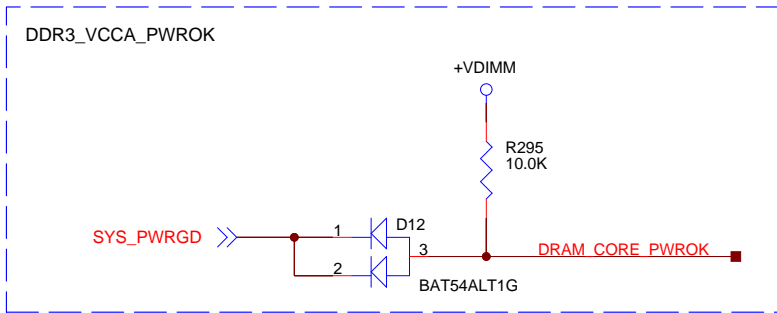
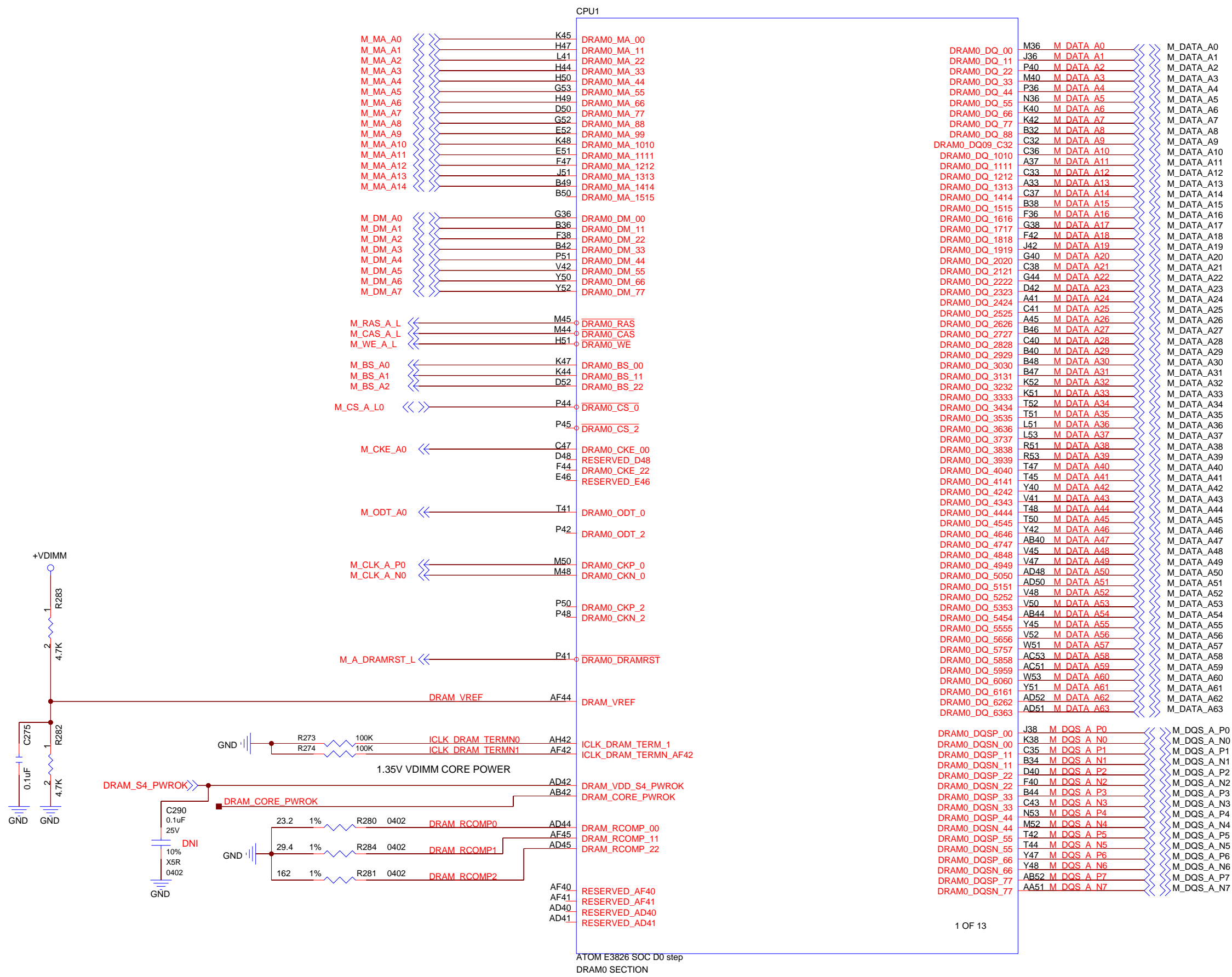
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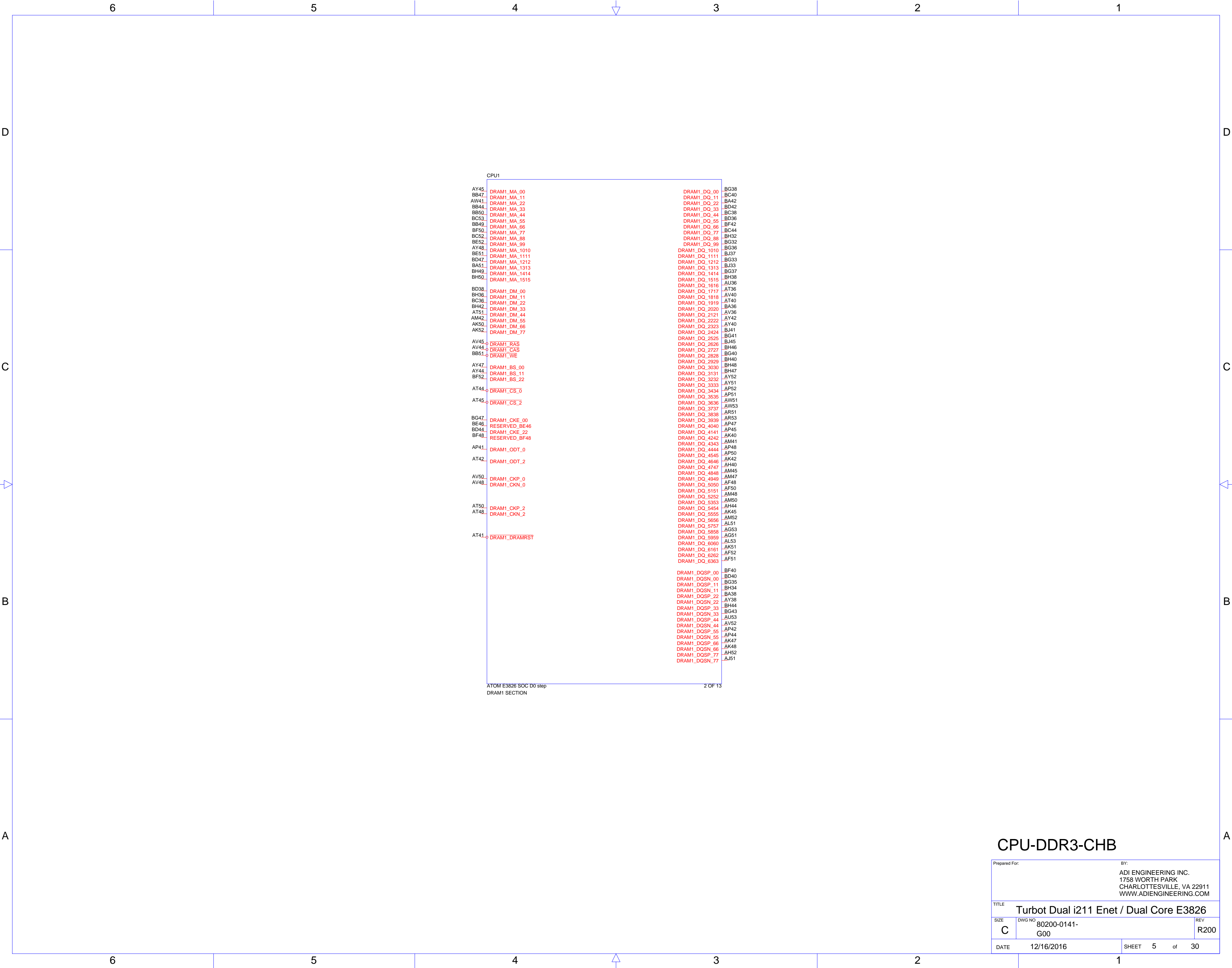
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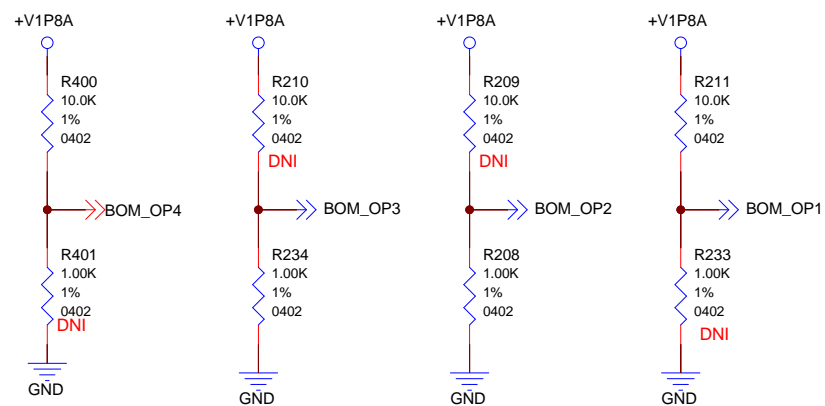


CPU-DDR3-CHA

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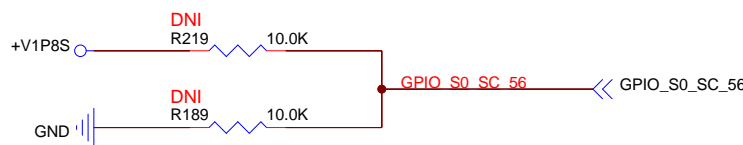


BOM Option
Hardware Identification (HW ID)
"1001"



HW STRAP

STRAP RESISTORS SHOULD BE PLACED CLOSE TO SOC



Top Swap(A16 Override)

H	Top address bit is unchanged
L	Top address bit is inverted

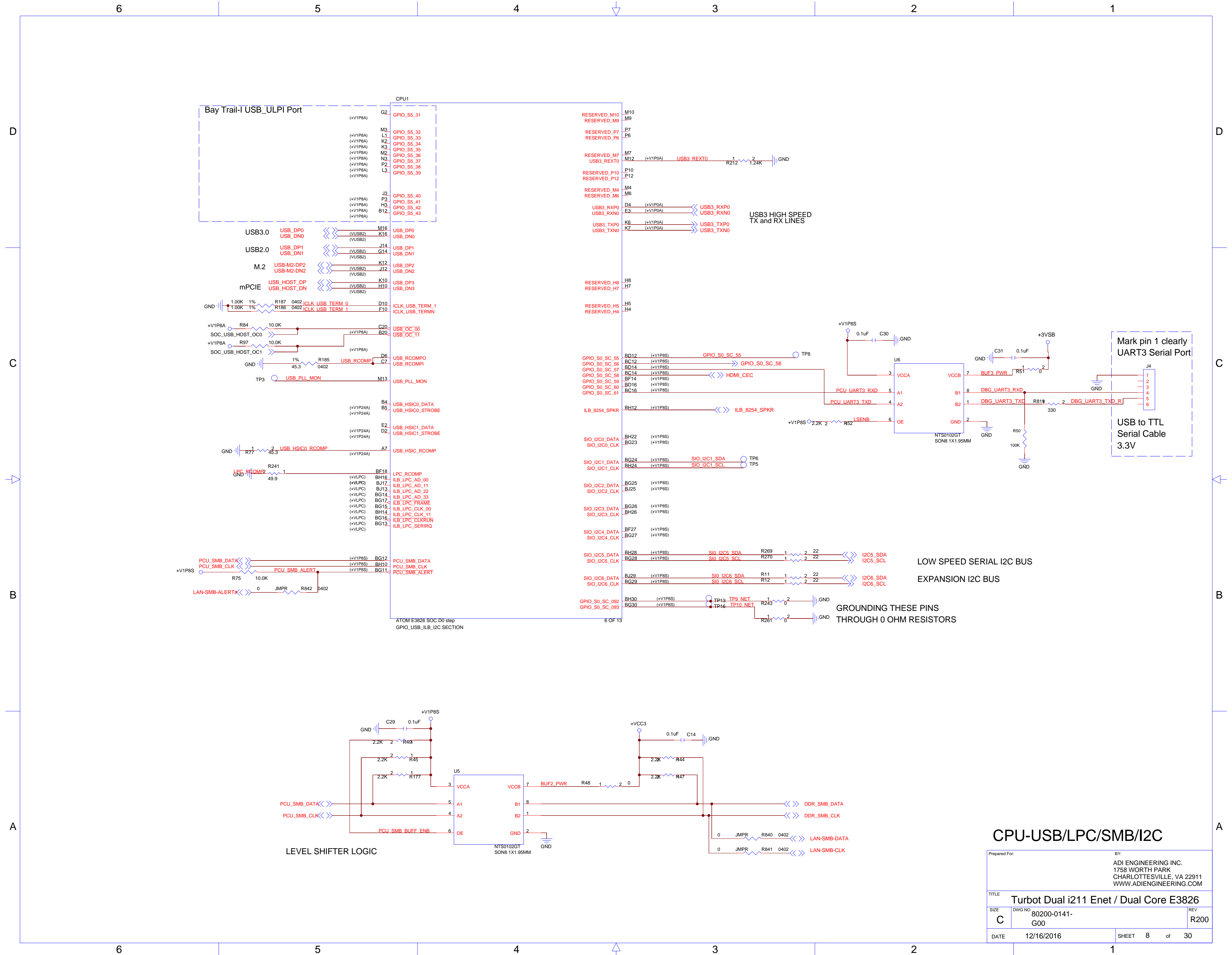
Strap Pin	Function
GPIO_S0_SC63 (LPE_I2S_FRM)	BIOS Boot Selection 0=LPC *1=SPI
GPIO_S0_SC65 (LPE_I2S_DATAOUT)	Security Flash Descriptors 0=Override *1=Normal Operation

CPU-PE/HDA/SD/eMMC/Strap

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CPU-PWR1/TPM/LPC

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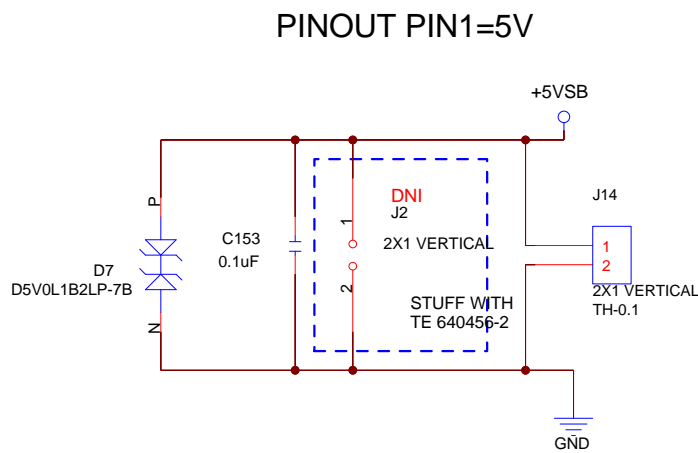
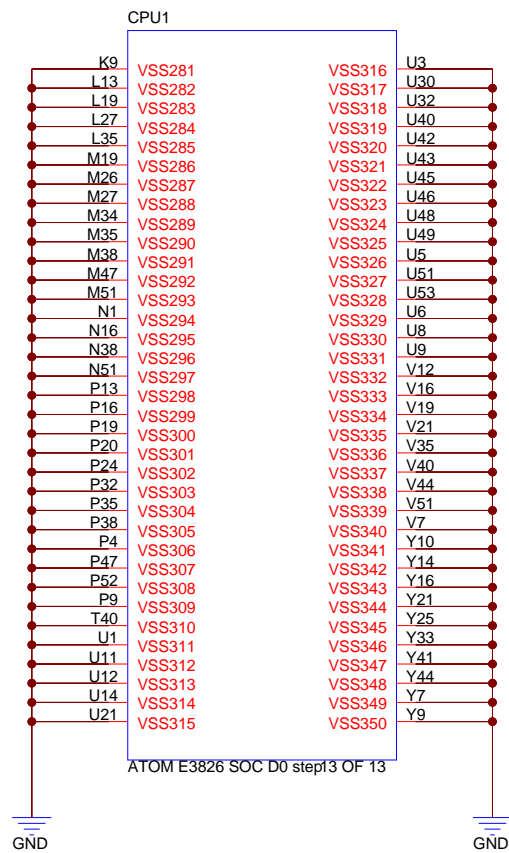
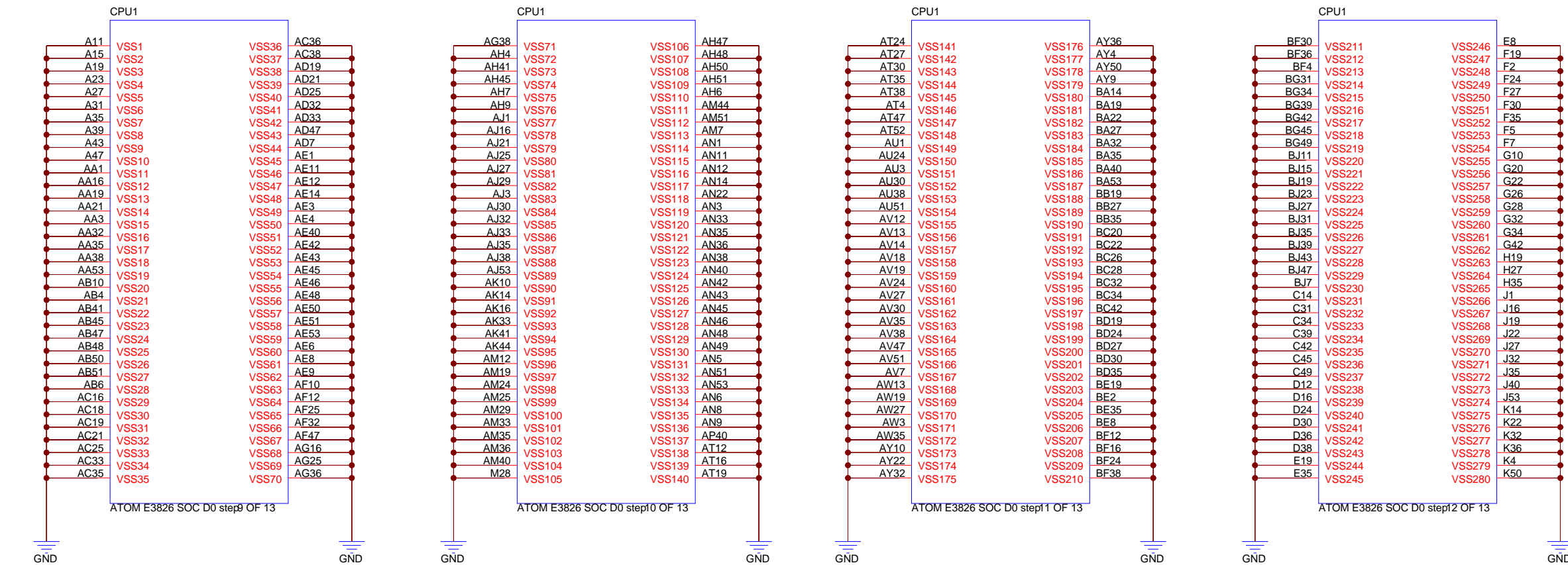
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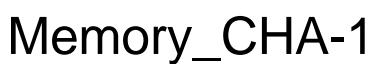
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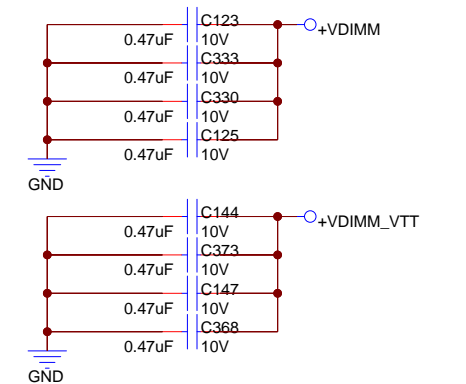
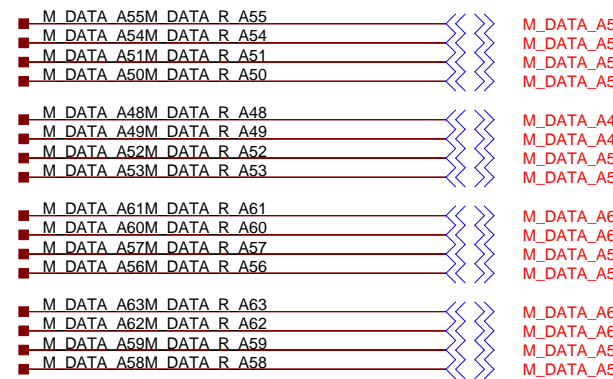
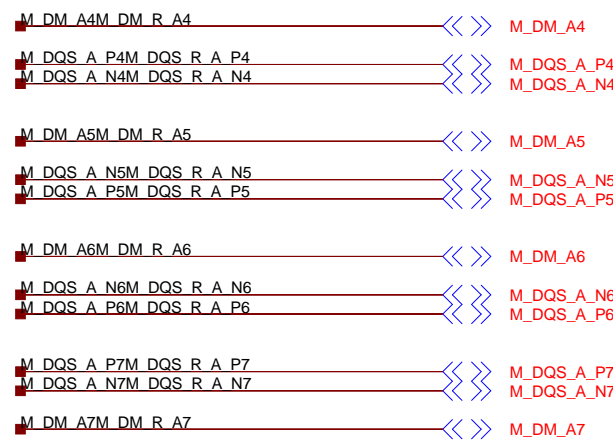
Note: Do not Populate J2 and J14 at same time as they occupy the same space. J2 is 2.54 mm pitch and J14 is 2.00 mm pitch.

CPU-GND

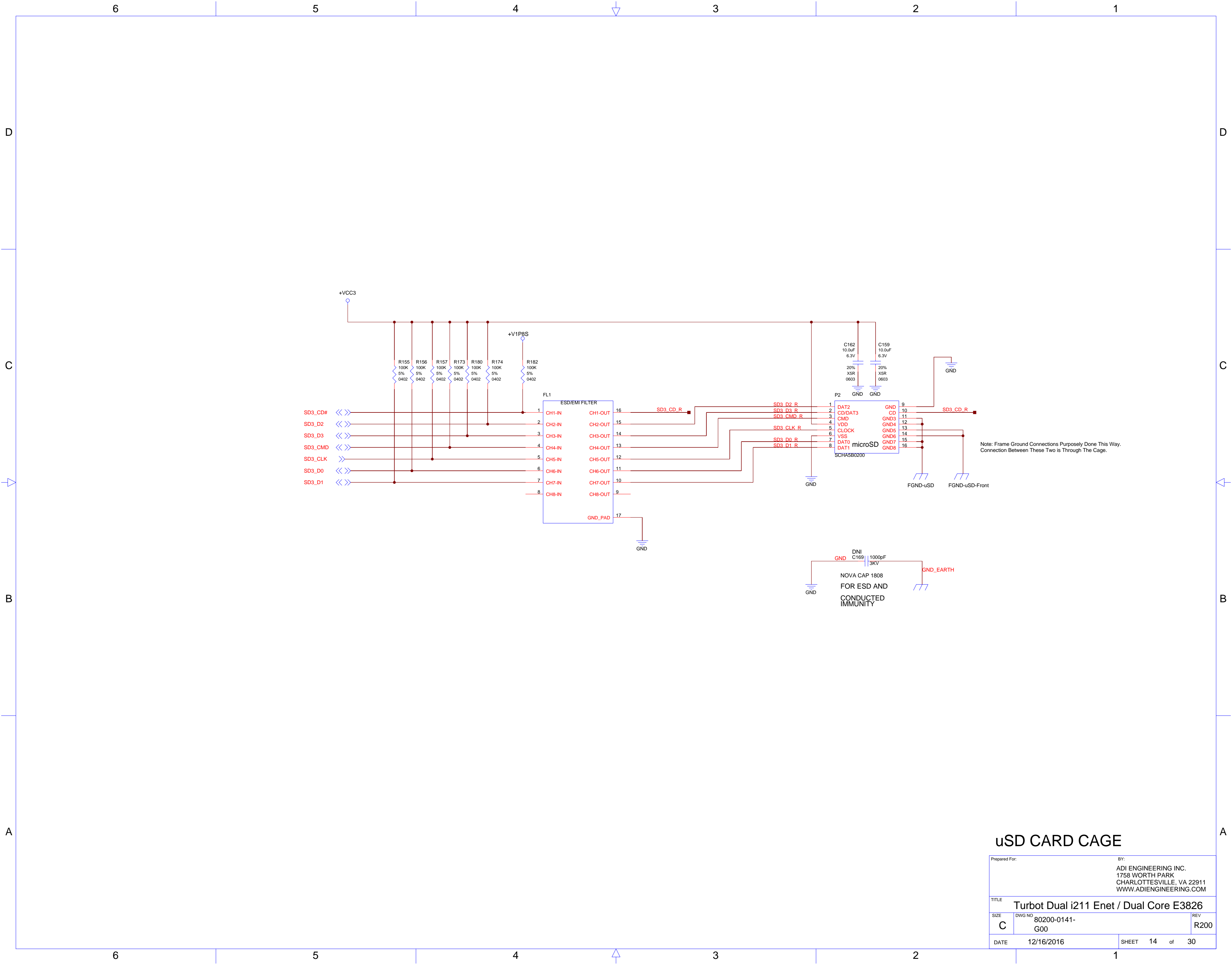
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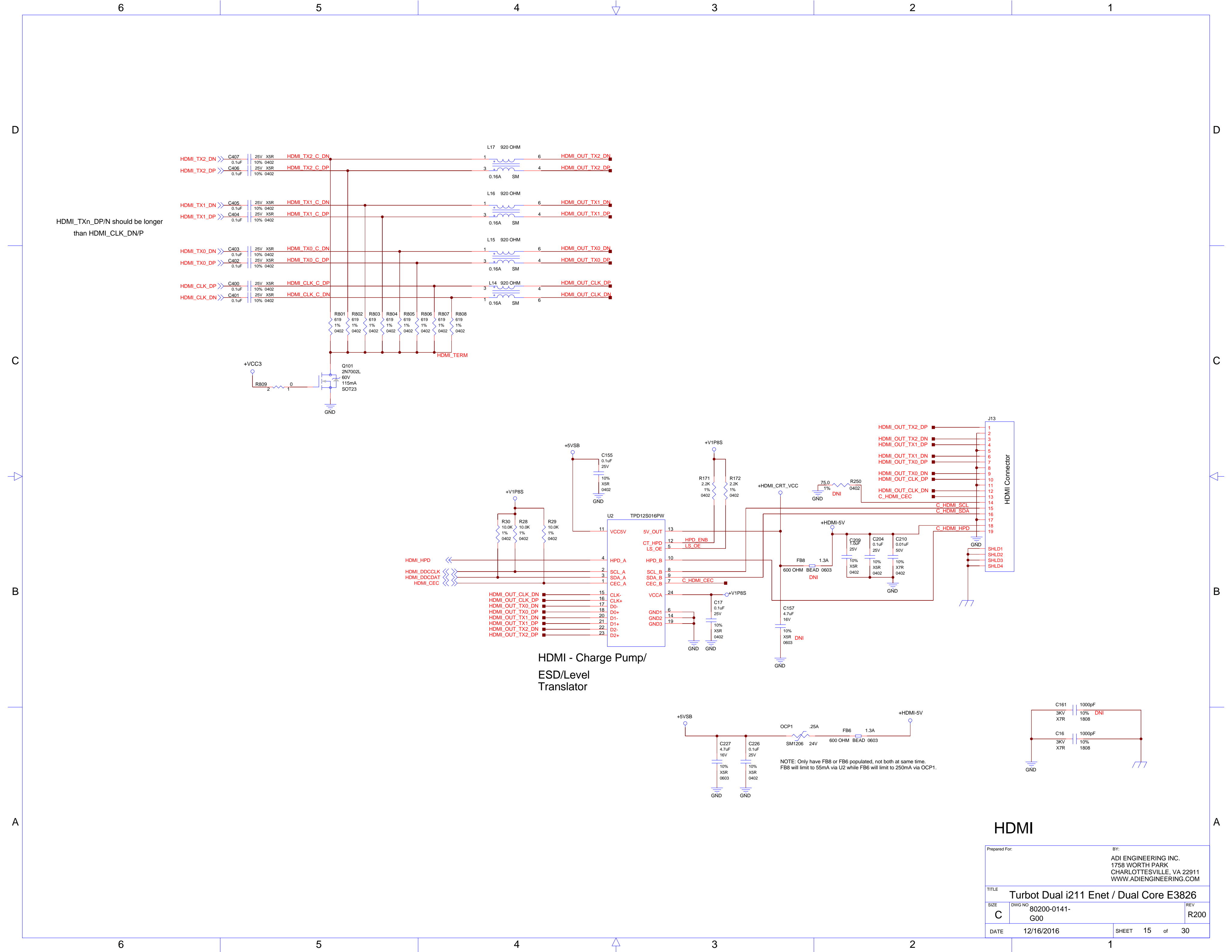


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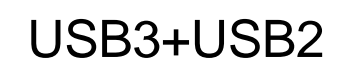
uSD CARD CAGE

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HDMI

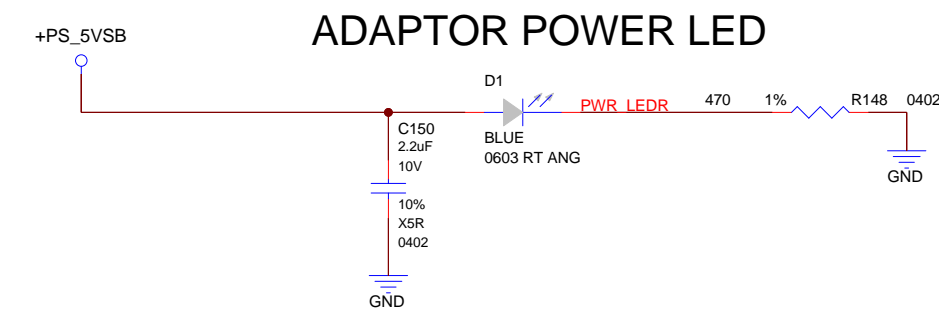
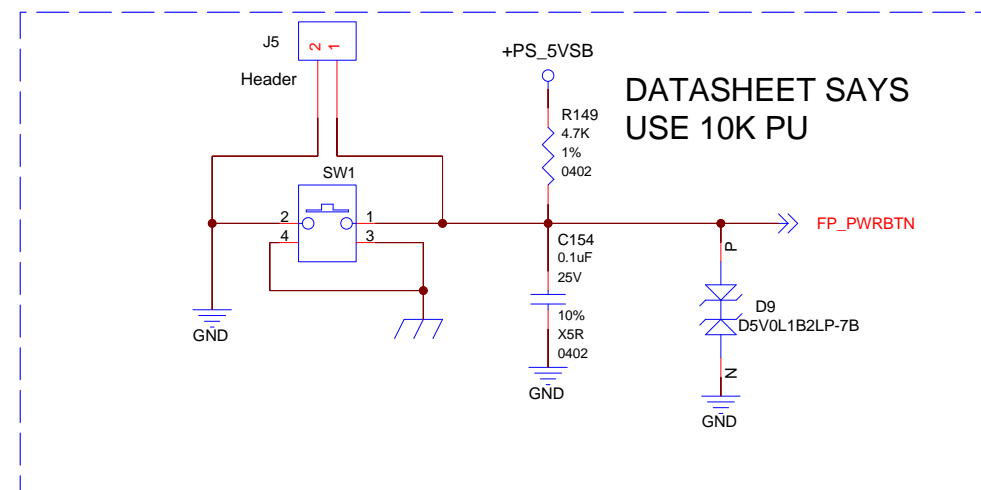
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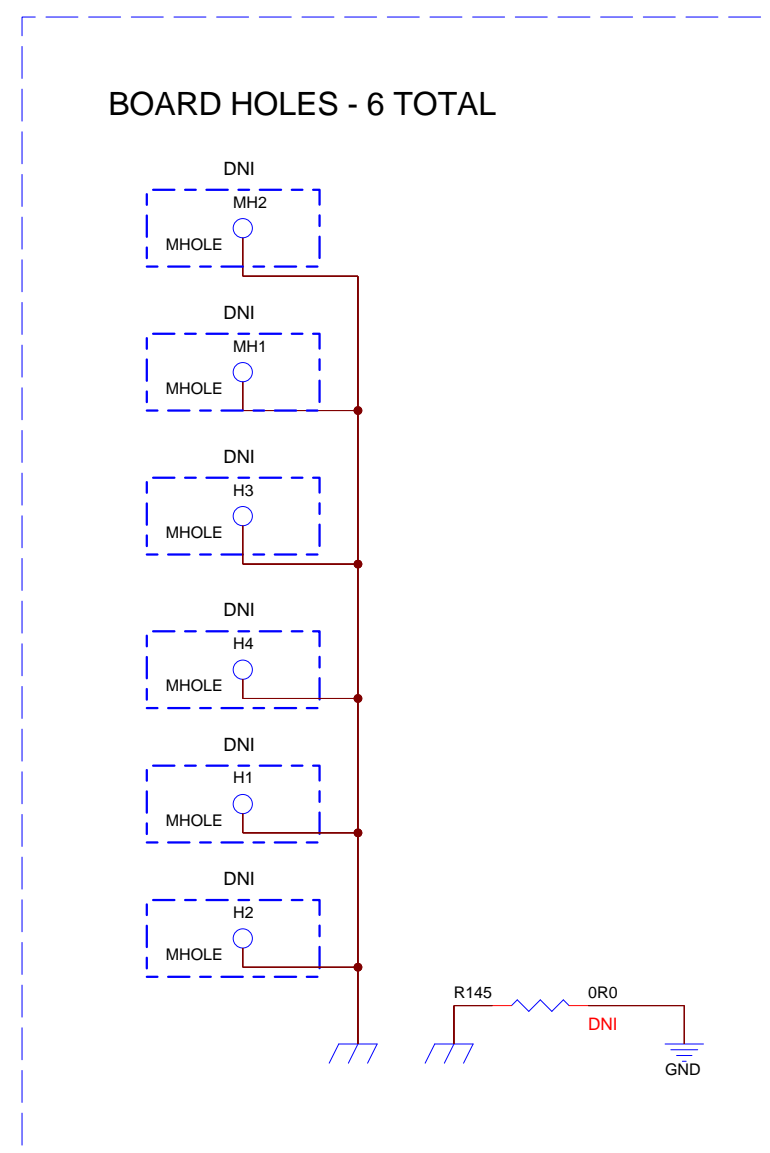
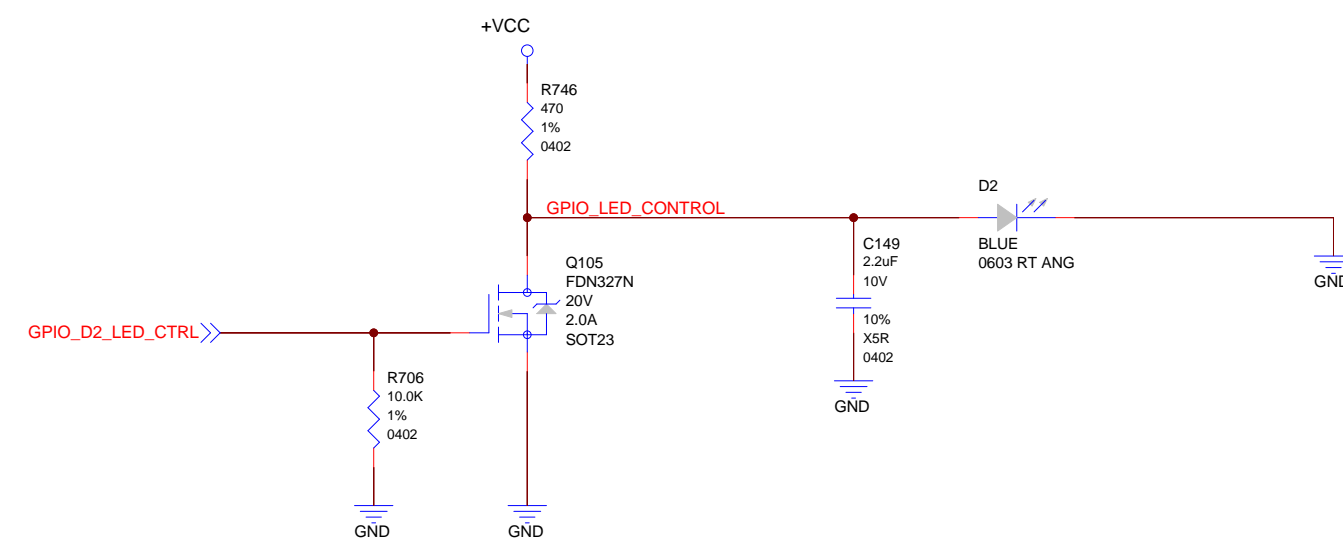
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DATE	12/16/2016	SHEET	17 of 30

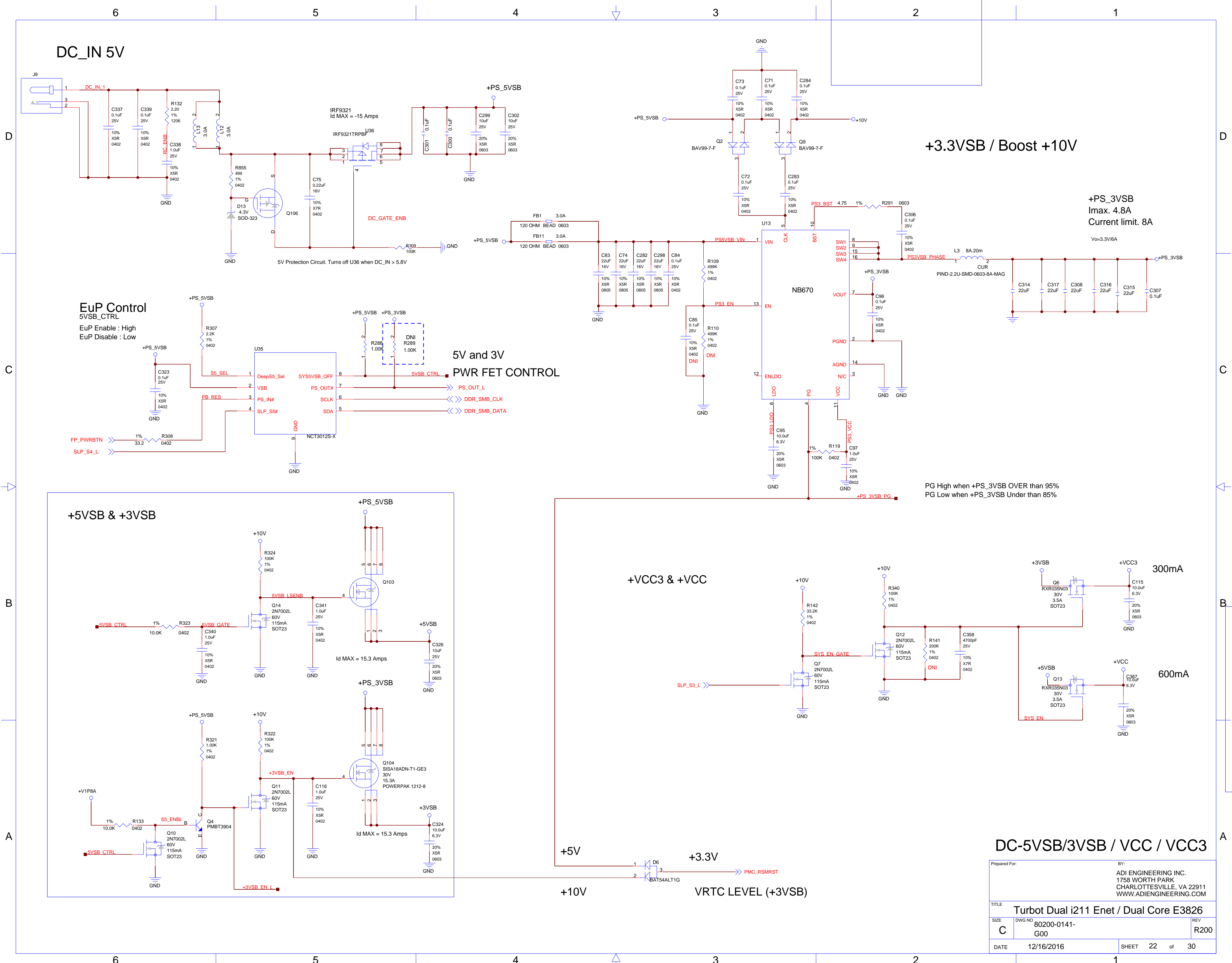


SYSTEM POWER LED - GPIO CONTROLLED



PWR BTN / LED / HOLES

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+5VSB & +3VSB

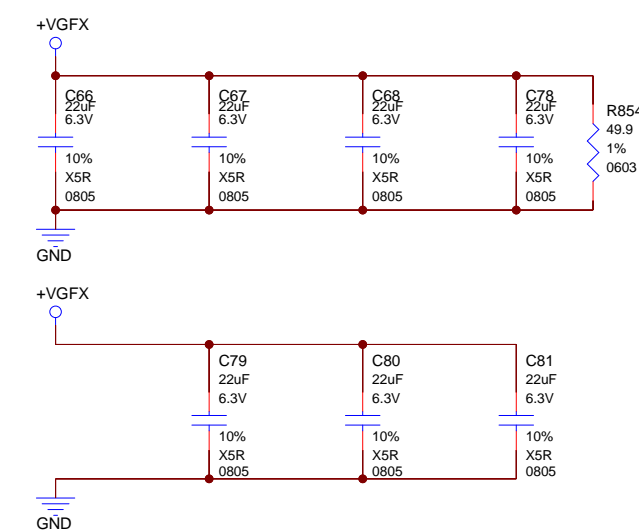
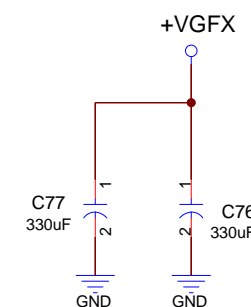
+VCC3 & +VCC

+3.3VSB / Boost +10V

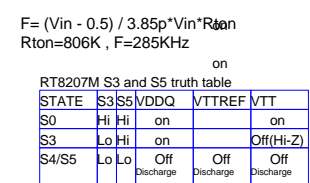
+PS_3VSB
Imax. 4.8A
Current limit. 8A
V_{ov}=3.3V/6A

DC-5VSB/3VSB / VCC / VCC3

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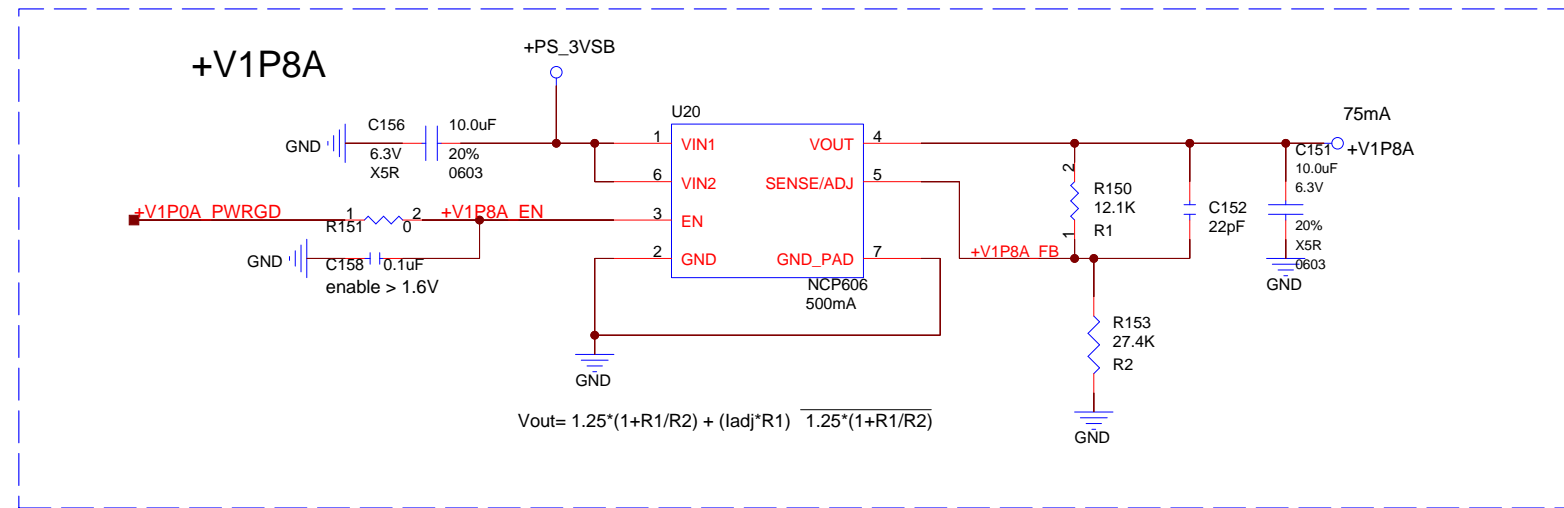
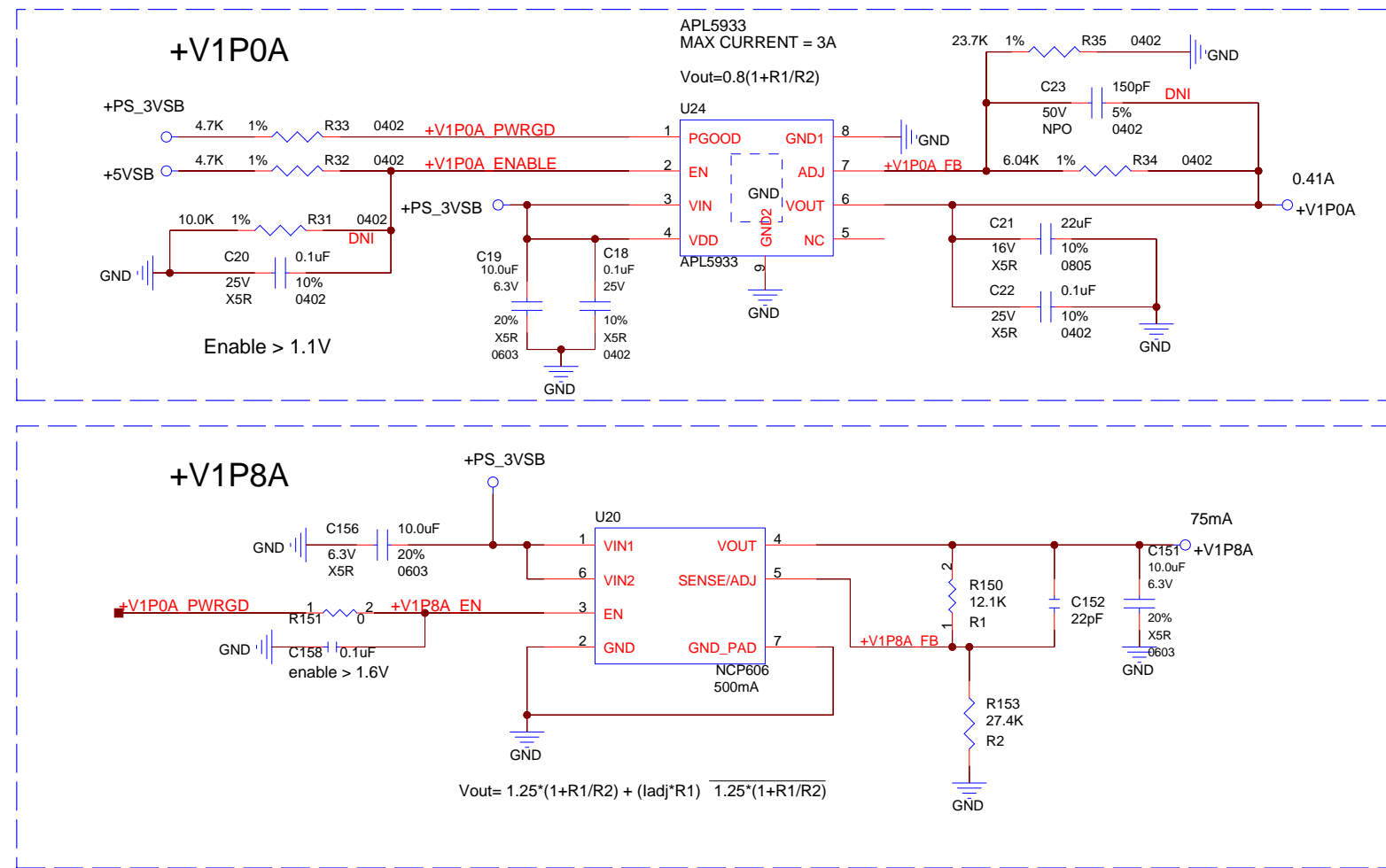
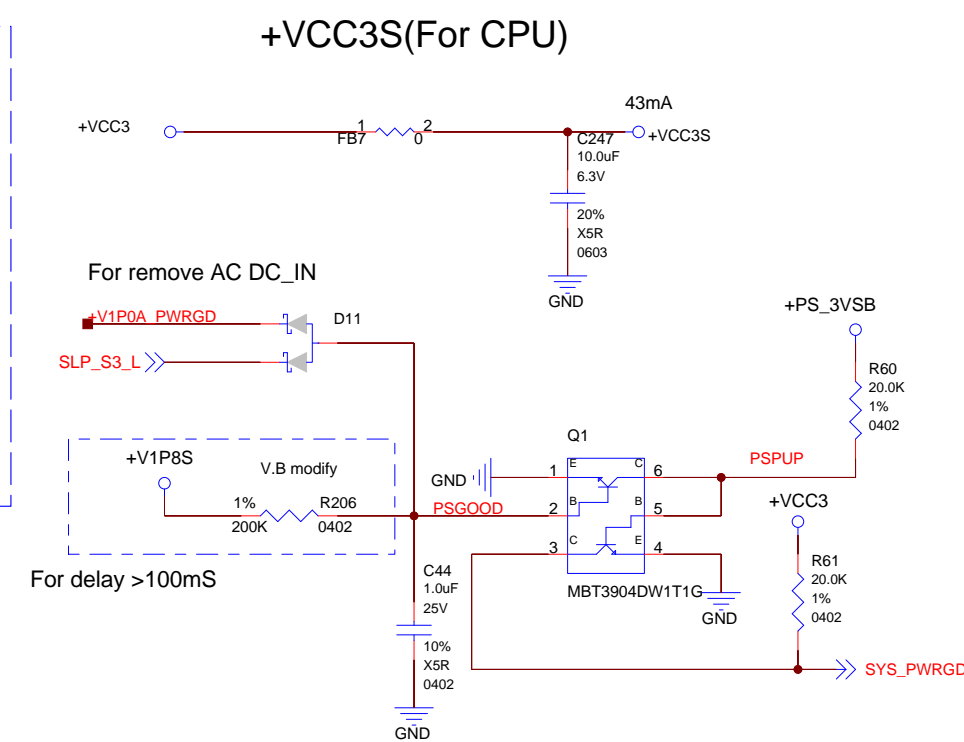
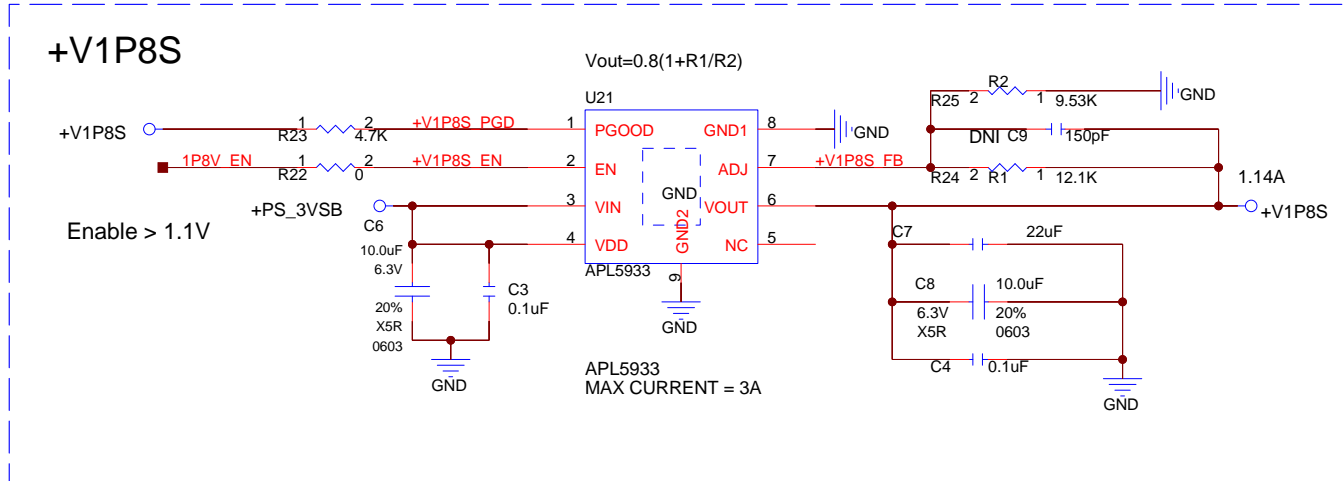
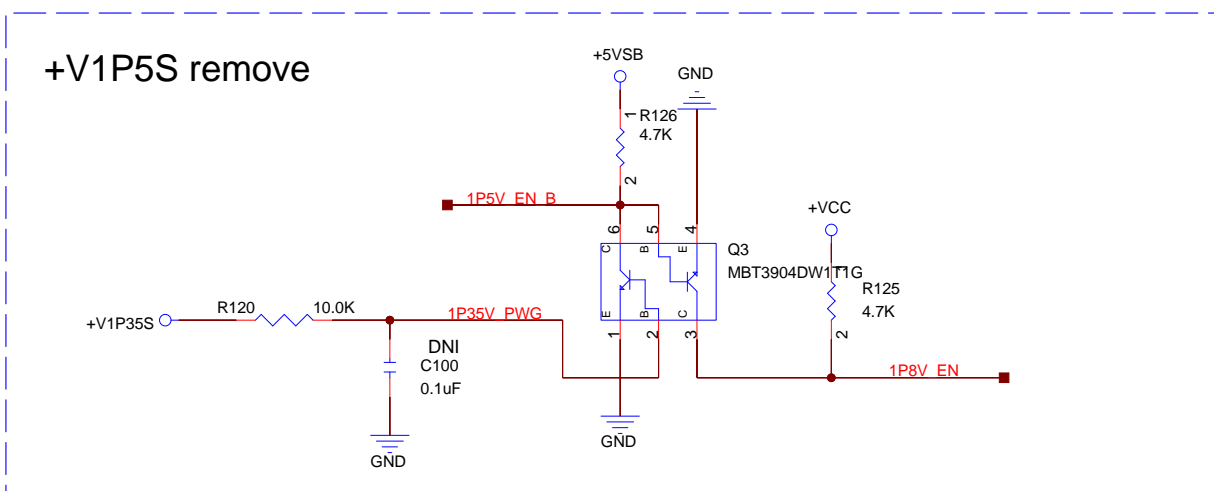
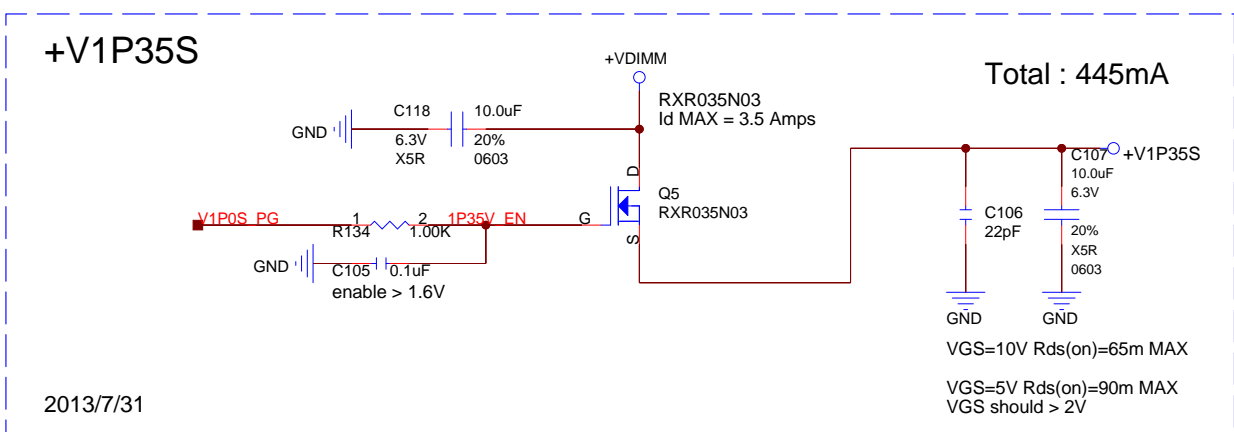
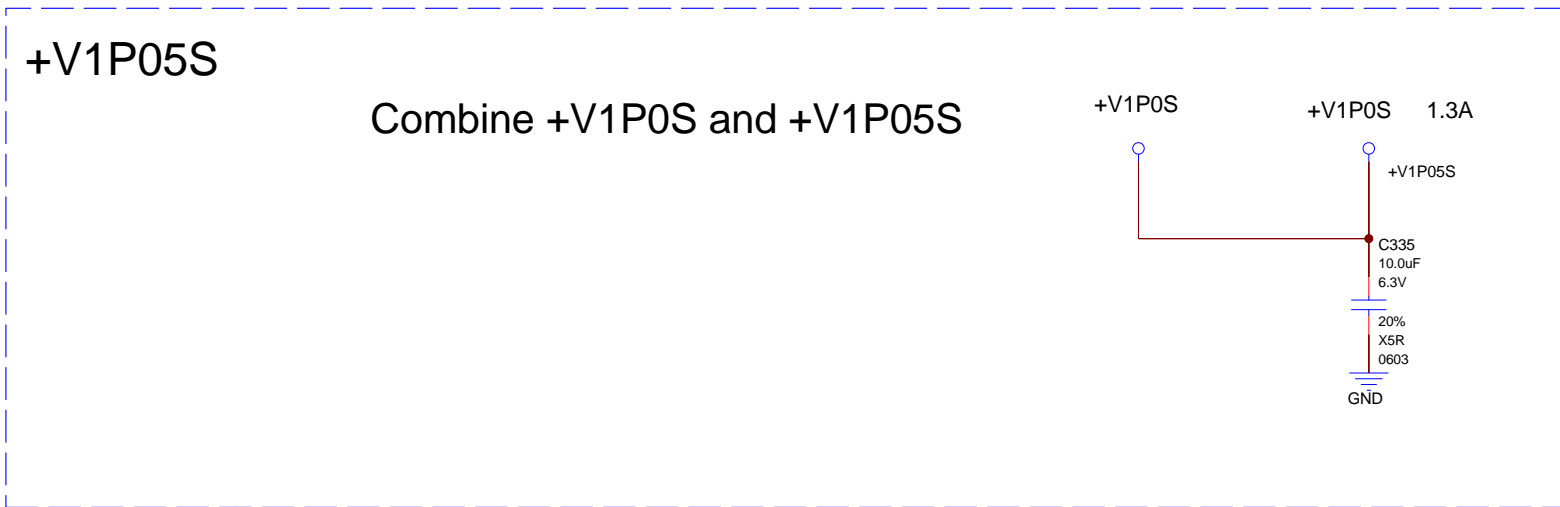
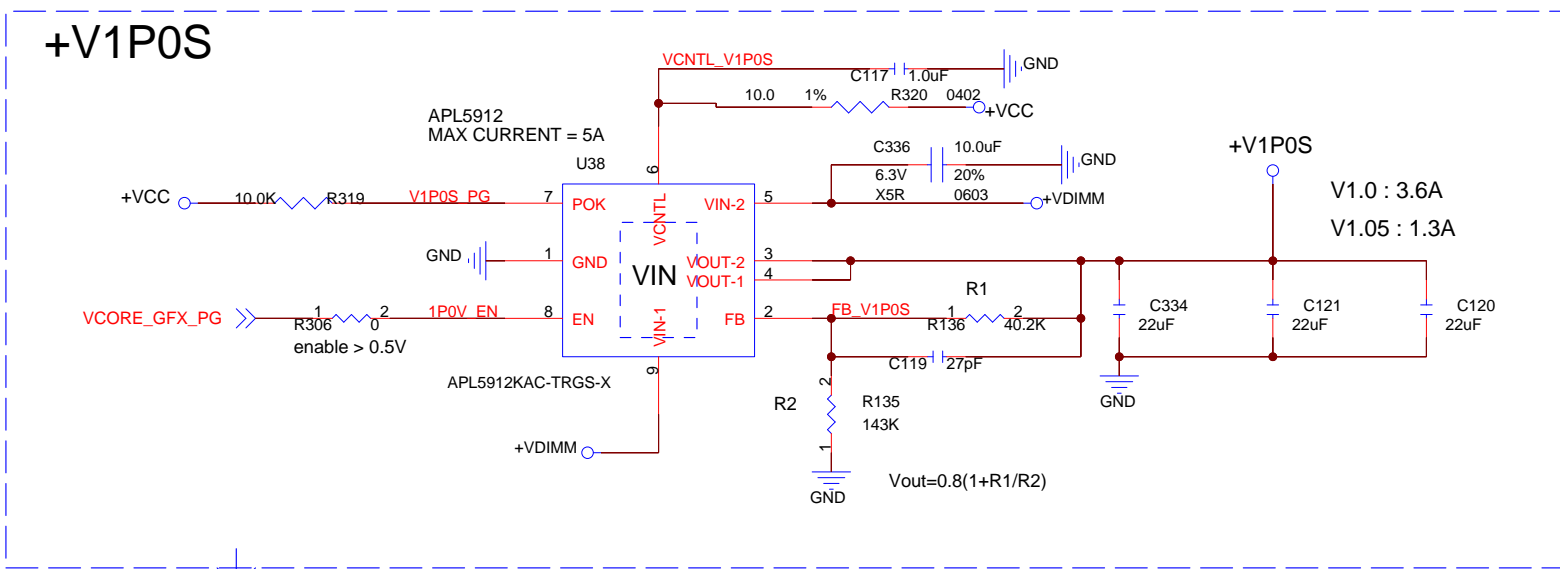
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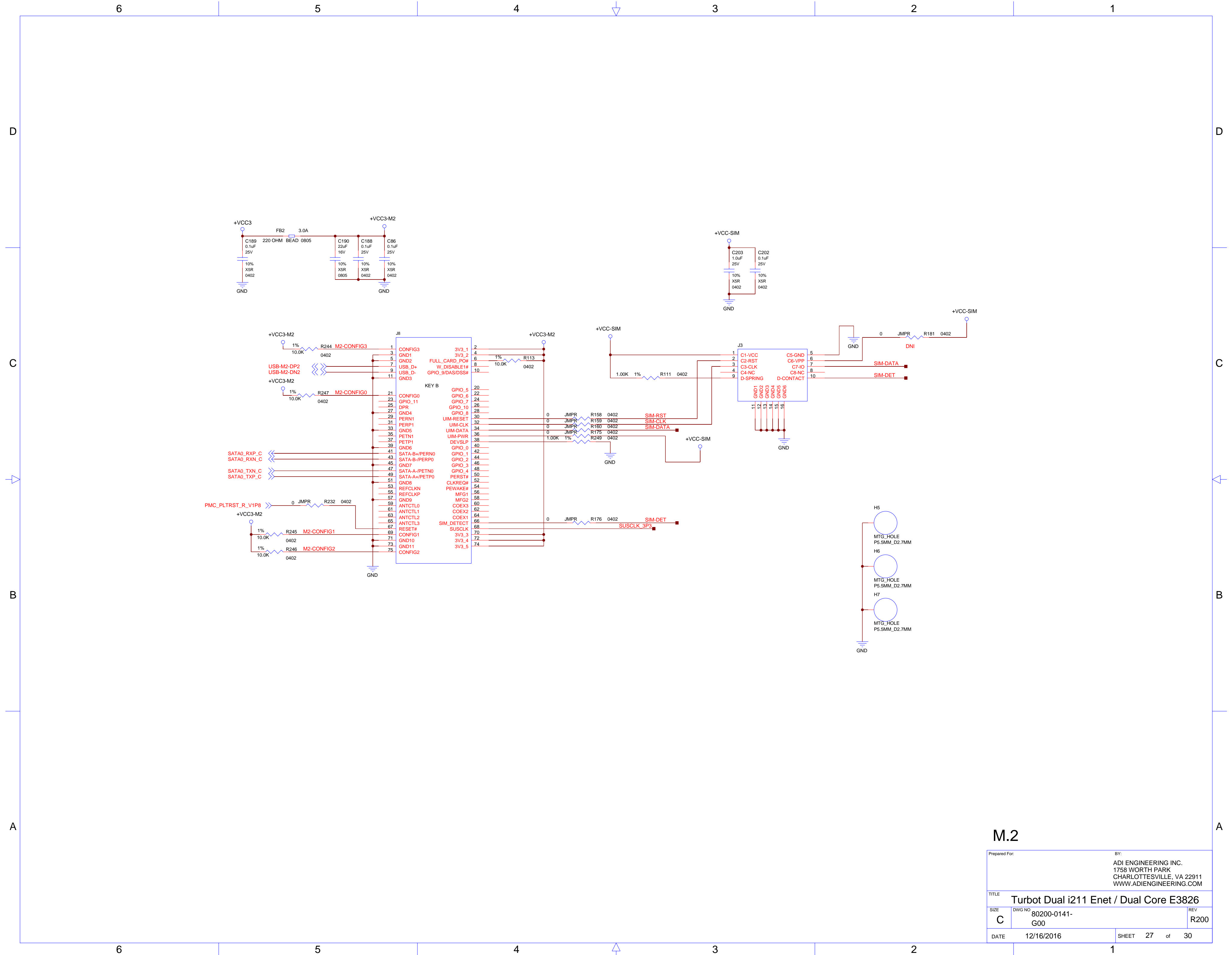


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DC-DC VOLTAGE

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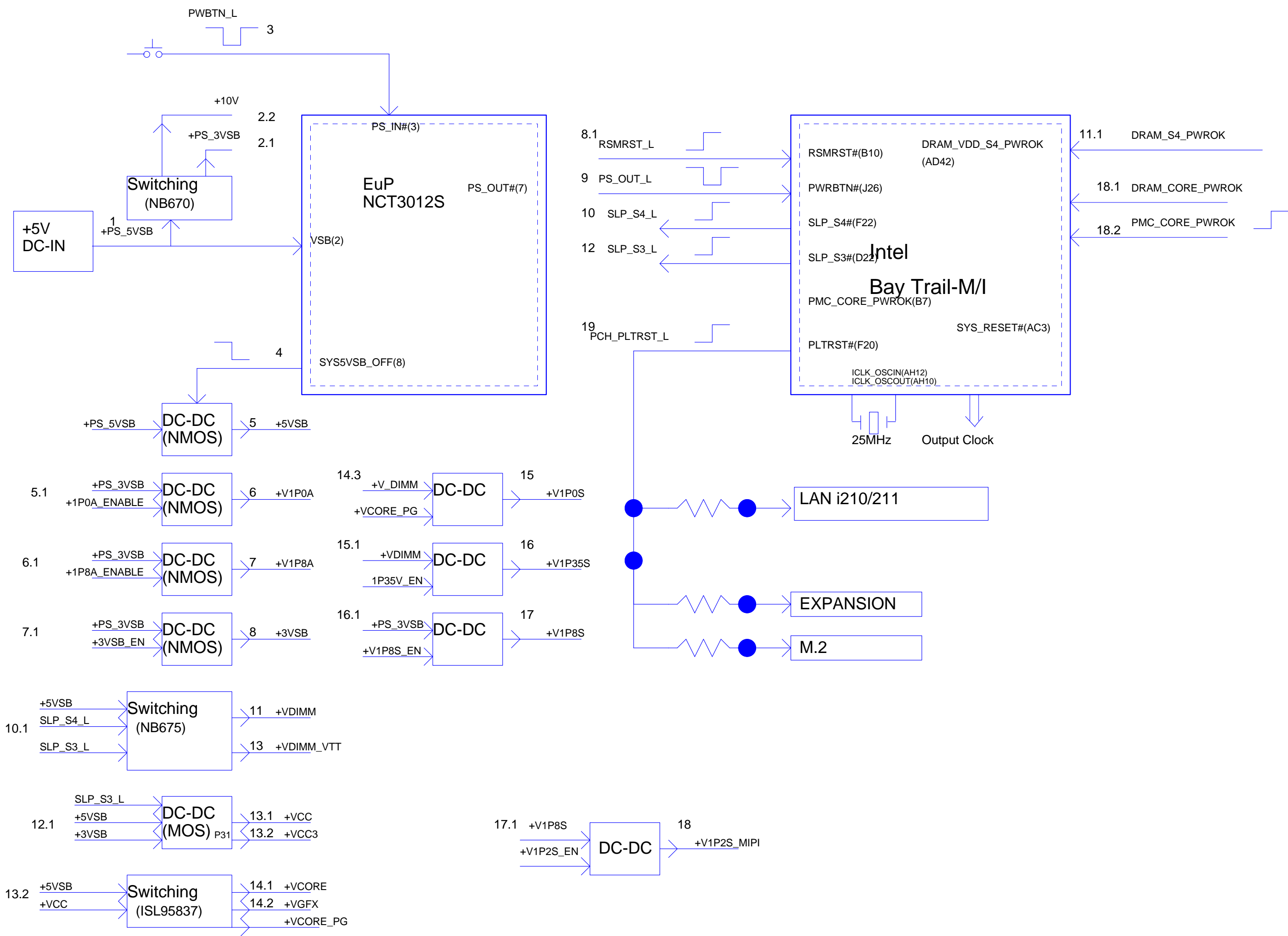


M.2

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POWER DELIVERY

PWR Sequence & RST Diagram



POWER SEQUENCE

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