
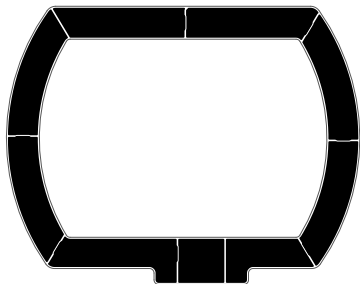


<h1>Light Board – 0</h1>			
<h2>Fab Drawing</h2>			 <b>Isaac Rex</b>
Rev: 0	Engineer: Isaac Rex	Sheet: /1	
Date: 2025-08-10	Size: A		
File: Light Board – 0.kicad_pcb			



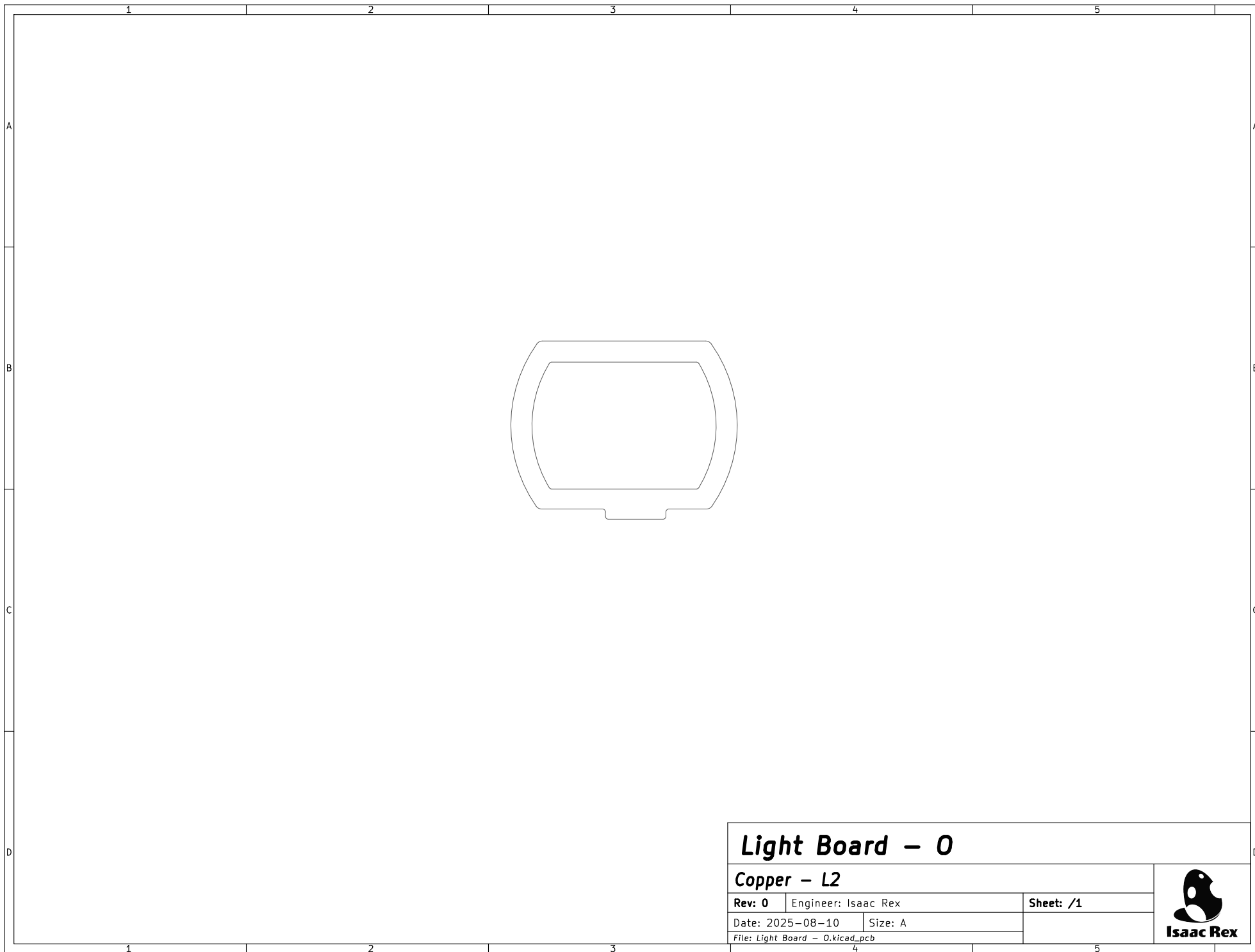
**Light Board – 0**


**Copper – L1**

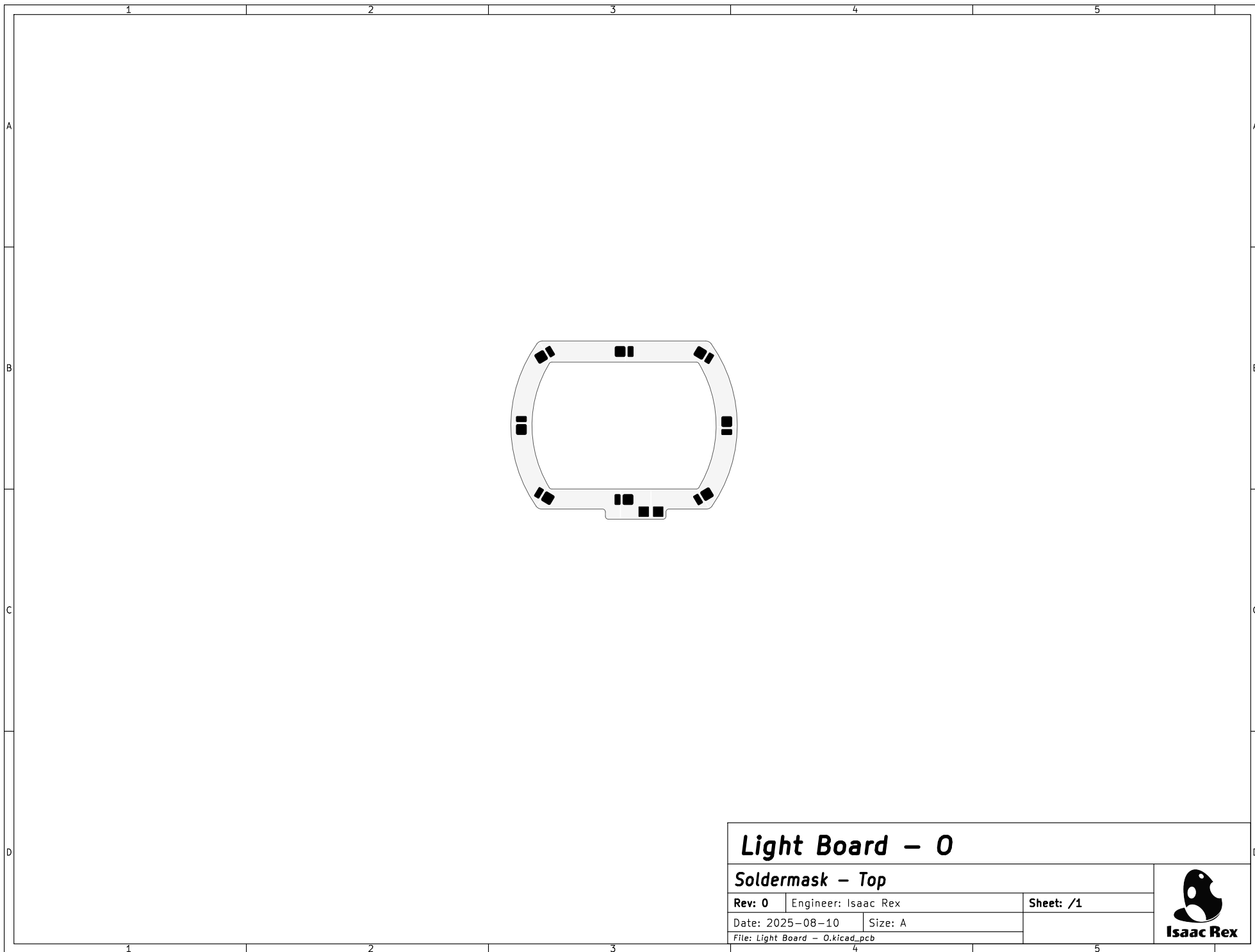
Rev: 0	Engineer: Isaac Rex	Sheet: /1
Date: 2025-08-10	Size: A	
File: Light Board – 0.kicad_pcb		



**Isaac Rex**

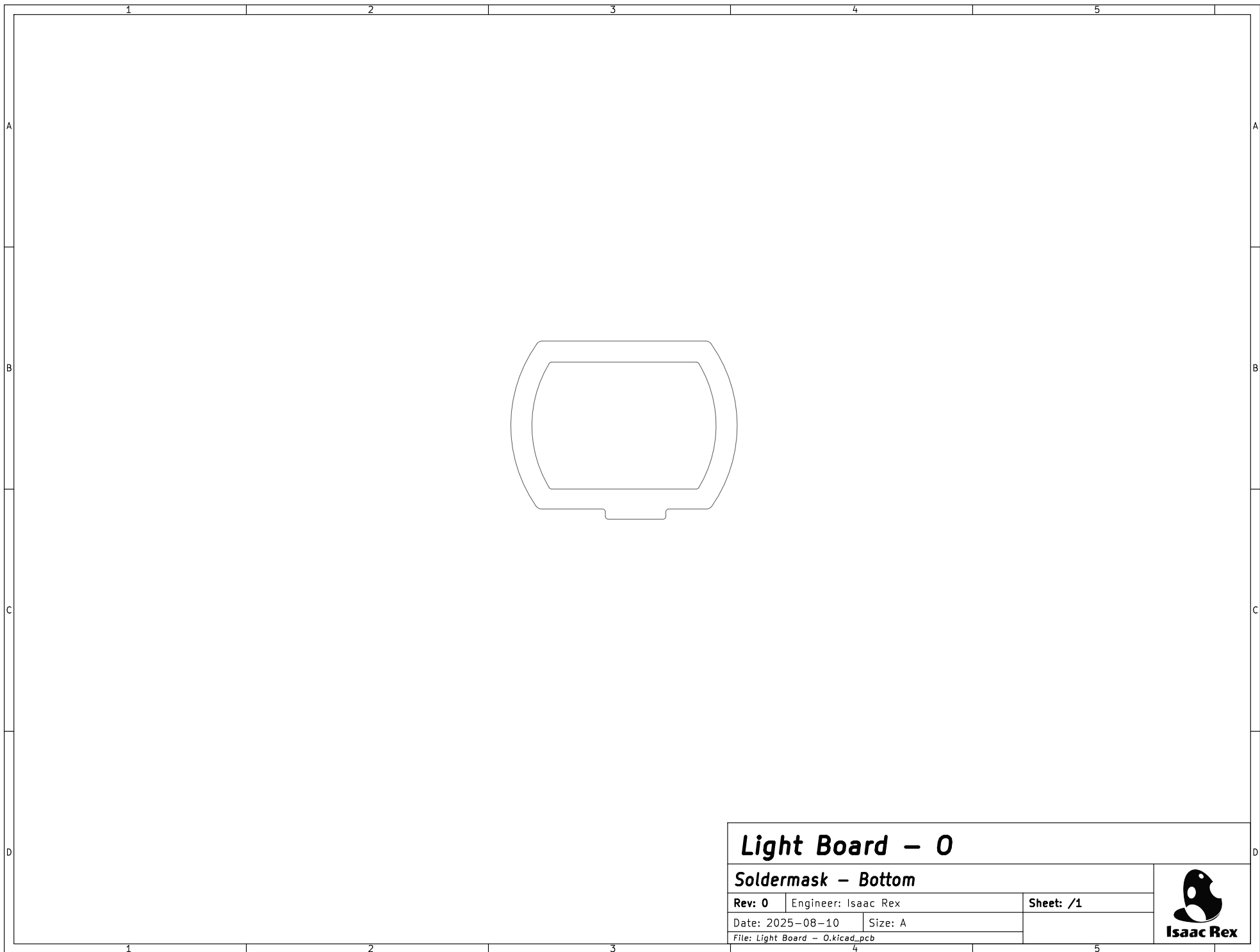


<h1>Light Board – 0</h1>				
<h2>Copper – L2</h2>			 <b>Isaac Rex</b>	
Rev: 0	Engineer: Isaac Rex			Sheet: /1
Date: 2025–08–10		Size: A		
File: Light Board – 0.kicad_pcb				




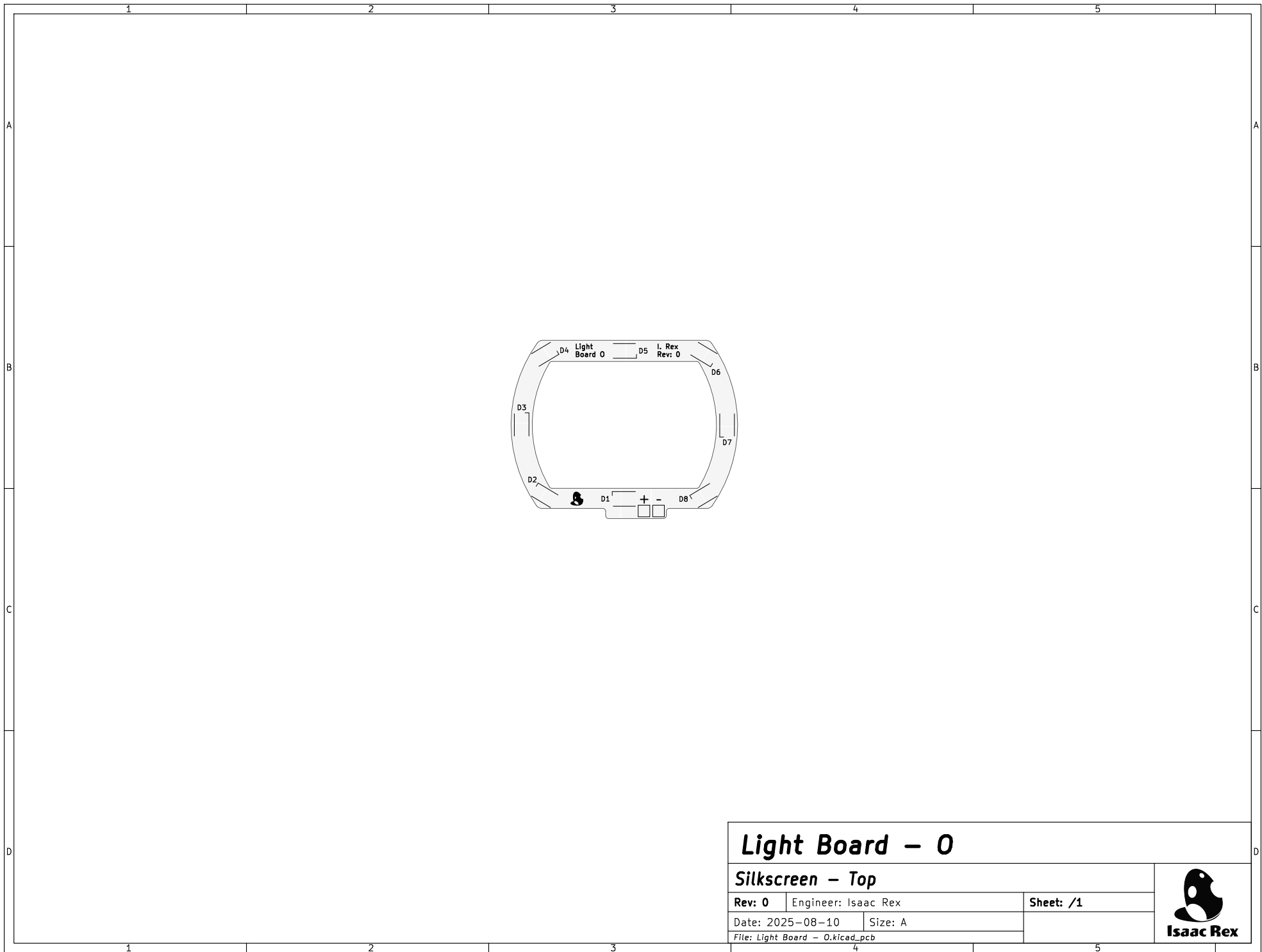
<b>Light Board – 0</b>		
<b>Soldermask – Top</b>		
<b>Rev:</b> 0	Engineer: Isaac Rex	<b>Sheet:</b> /1
Date: 2025-08-10	Size: A	
File: Light Board – 0.kicad_pcb		

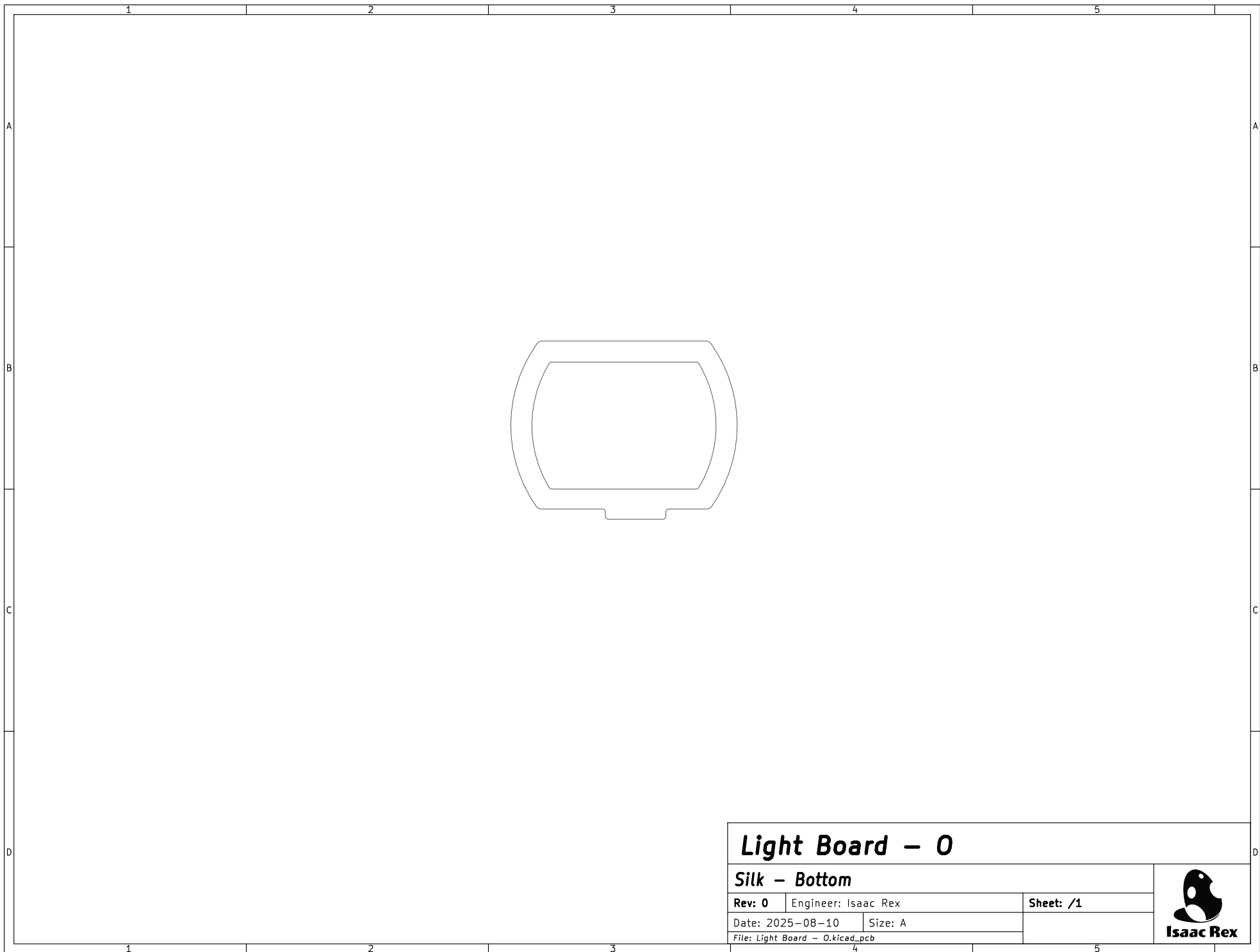




<b>Light Board – 0</b>		
<b>Soldermask – Bottom</b>		
<b>Rev:</b> 0	Engineer: Isaac Rex	<b>Sheet:</b> /1
Date: 2025-08-10	Size: A	
File: Light Board – 0.kicad_pcb		

  
**Isaac Rex**





<b>Light Board – 0</b>			
<b>Silk – Bottom</b>			
<b>Rev:</b> 0	Engineer: Isaac Rex	<b>Sheet:</b> /1	
Date: 2025-08-10		Size: A	
File: Light Board – 0.kicad_pcb			



Isaac Rex