

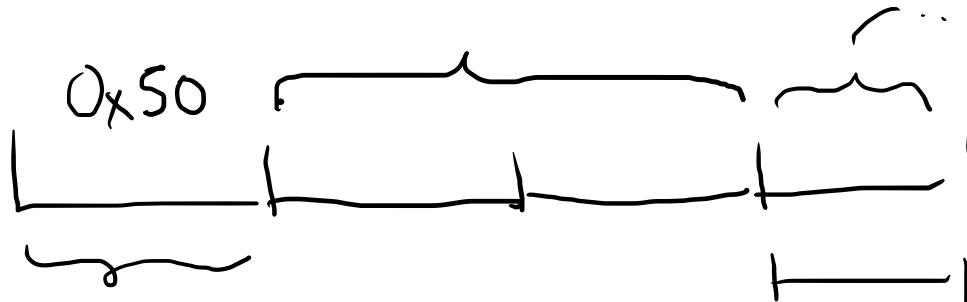
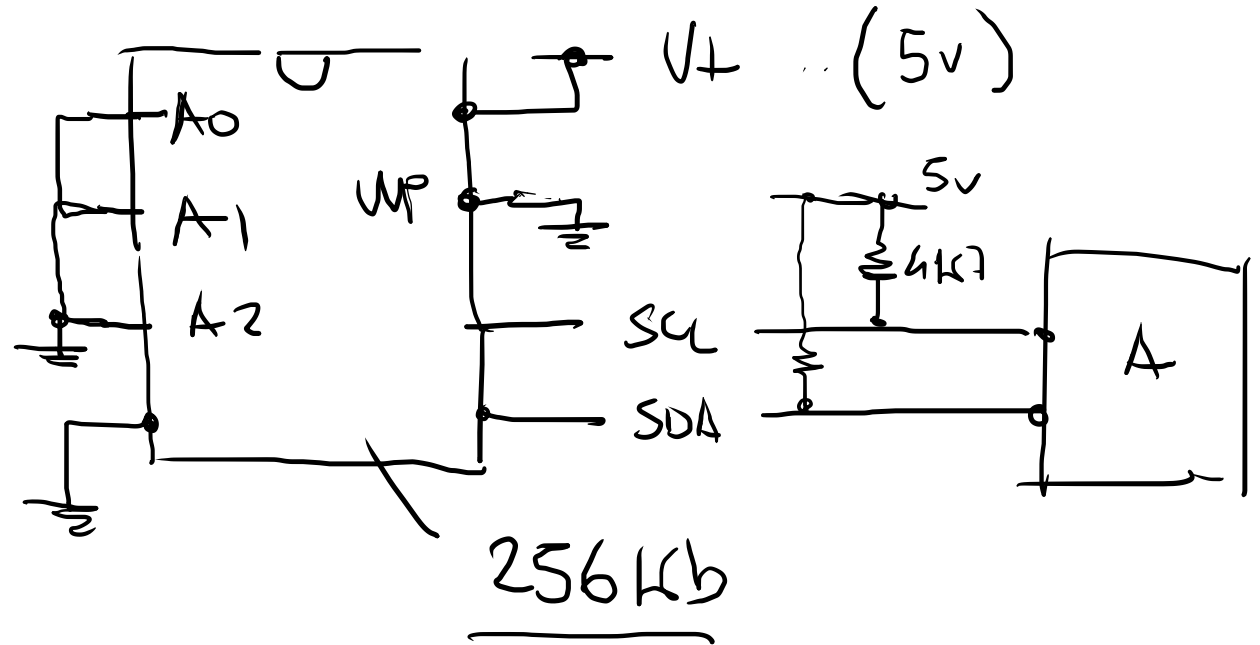
EEPROM

24LC256

12C

100000

wire.h



mask



$\gg 8$

$\text{mask} \gg 8 = 0000\dots A$



— AND &



0x FF

$\text{mask} \& 0xFF$

0x 00FF