


DK-LM3S9B96 Firmware Development Package

USER'S GUIDE



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1 Introduction

The Texas Instruments Stellaris[®] DK-LM3S9B96 evaluation board is a platform that can be used for developing software and prototyping a hardware design. It contains a Stellaris ARM[®] Cortex[™] -M3-based microcontroller, along with a QVGA color touchscreen display, a user button, a small speaker, a potentiometer thumbwheel, a microSD card slot, a stereo audio codec, a USB OTG connector and an Ethernet connector. Additionally, many of the microcontroller's pins are connected to jumpers, allowing for easy connection to other hardware for the purposes of prototyping (after the stake headers have been populated by the customer).

The board also contains 1 MB of SSI-connected flash memory and a daughter board with 8 MB of SDRAM.

This document describes the board-specific drivers and example applications that are provided for this development board.

2 Expansion Boards

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2.1 Introduction

The dk-lm3s9b96 development kit board contains a header populated with the microcontroller's Extended Peripheral Interface (EPI) and I2C0 signals. The EPI is a flexible, high-speed parallel bus allowing connection of external peripherals or memories and several expansion boards are available which illustrate how EPI may be used in different operating modes. Expansion boards not requiring EPI may also make use of dk-lm3s9b96's ability to mux other signals to these pins to gain access to peripherals such as UART and SSI or use them as simple GPIOs.

2.2 EPI Configuration for Expansion Boards

The EPI must be configured differently depending upon the mode in which it is to be used and the peripherals to which it is connected. There are several steps required to configure the pins used to connect your peripherals via EPI and the EPI peripheral itself:

1. Enable all GPIO ports containing pins which are used by EPI using calls to the SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOx) function.
2. Enable the EPI peripheral using a call to the SysCtlPeripheralEnable(SYSCTL_PERIPH_EPI0) function.
3. Ensure that the required EPI signals are connected to the required pins by writing the GPIO Port Control Register (GPIO_O_PCTL) for each GPIO port containing EPI signals.
4. Configure the operating mode of the EPI by calling the EPIModeSet() function and passing the required mode.
5. Configure the clock rate of the EPI module by calling the EPIDividerSet() function.
6. Configure parameters specific to the EPI operating mode by calling one of the EPIConfigGPMoDeSet(), EPIConfigHB8Set(), EPIConfigHB16Set() or EPIConfigSDRAMSet() functions depending upon the mode you selected via EPIModeSet().
7. Select the address mapping for the peripherals or memory attached using a call to the EPIAddressMapSet() function.

This configuration is handled in the [PinoutSet\(\)](#) function which can be found in the file `set_pinout.c` in directory `boards/dk-lm3s9b96/drivers`. Two different implementations of this function are provided. The default implementation, used in all the example applications for the board, makes use of information from an I2C-connected EEPROM device found on some expansion boards to determine the EPI configuration at runtime. If no EEPROM device is found, the

configuration is set assuming that the SDRAM expansion board is connected (since this simple expansion board does not have the identification EEPROM).

In a typical end-user application, this dynamic determination of the EPI configuration is unlikely to be used and, for this reason, a secondary implementation of the [PinoutSet\(\)](#) function is provided which sets a static EPI configuration. By default, the example provided sets the EPI into HostBus-8 mode with timings appropriate for the Flash and SRAM expansion board. To compile this implementation of the function into your project, ensure that the `SIMPLE_PINOUT_SET` label is defined in your makefile or toolchain project and passed to the compiler. To set a static initialization for the FPGA expansion board, set `SIMPLE_PINOUT_SET` and also `EPI_CONFIG_FPGA` before compiling your application.

For further information on the functions mentioned here, see the *Stellaris Peripheral Driver Library User's Guide*.

2.3 Stellaris LM3S9B96 SDRAM Expansion Board

A expansion board containing 8 MB of SDRAM is included in the basic dk-lm3s9b96 package and is used by the `qs-checkout` and `showjpeg` example applications which make use of the memory for image decompression workspace and, in the `qs-checkout` case, to store a file system image that is used by the application's embedded web server.

This expansion board does not have an EEPROM containing EPI configuration information so [PinoutSet\(\)](#) defaults to configuring EPI for this board if no I2C EEPROM is found. The [SDRAMInit\(\)](#) function may also be called after [PinoutSet\(\)](#), if SDRAM timing parameters other than the defaults are required since this function reconfigures the EPI peripheral based on parameters defining the required clock rate and refresh interval for the SDRAM.

After configuration, the SDRAM is managed as a heap via the [ExtRAMAlloc\(\)](#) and [ExtRAMFree\(\)](#) functions which, along with [SDRAMInit\(\)](#), can be found in the file `extram.c` under the `boards/dk-lm3s9b96/drivers` directory.

2.4 Stellaris LM3S9B96 Flash and SRAM Memory Expansion Board

This complex expansion board, available separately from the basic dk-lm3s9b96 board, connects three devices via EPI:

- 8 MB of x8 Flash,
- 1 MB of x8 SRAM and
- the QVGA touchscreen (via a Customer Programmable Logic Device (CPLD)).

An I2C EEPROM on this board contains configuration and timing information allowing the [PinoutSet\(\)](#) function to configure the EPI correctly and allow all the devices to be accessed.

The expansion board connects to the jumper block near the QVGA touchscreen in addition to the EPI connector and intercepts control and data signals to the panel. The touchscreen display, which is normally accessed via GPIOs when this expansion board is absent, is driven via a CPLD which allows control and data access to the device via particular EPI addresses.

When this expansion board is detected in `PinoutSet()`, the `g_eDaughterType` global variable is set allowing the display driver (`kitronix320x240x16_ssd2119_8bit.c`) and touchscreen driver (`touch.c`) to dynamically reconfigure themselves to operate with the new expansion board. This variable is also used in the `extram.c` driver to configure the external SRAM as the heap which can be managed using the `ExtRAMAlloc()` and `ExtRAMFree()` calls (following a call to `ExtRAMHeapInit()`).

The `qs-checkout` example application detects this board and serves a web site from the external Flash if one is found. The external Flash is factory-programmed with the same "photo gallery" web site image that is found in the serial Flash on the base `dk-lm3s9b96` board. Both the `qs-checkout` and `showjpeg` examples make use of the SRAM on this expansion board for image decompression workspace.

2.5 Execution from External Memories

External, EPI-connected Flash memory is intended primarily for data storage but it is possible to execute code from Flash, SRAM, or SDRAM attached to the external peripheral interface. Note that execution from external Flash or RAM results in lower performance than executing from internal Flash. A program running from off-chip memory will typically run at approximately 5-10% of the speed of the same program in internal Flash. This results from the combined effects of the 8-bit or 16-bit wide external interface compared to the fast 32-bit wide access to internal Flash and the multiple clock cycles required for an access to the external Flash. SDRAM is recommended for the best performance using external memory.

Three example applications are provided illustrating execution from external Flash when using the optional Flash and SRAM Memory Expansion Board:

boot_eth_ext This boot loader variant is configured to download applications sent from the LM-Flash utility to external Flash and boot them directly from external memory. To force a download of a new binary while this boot loader is present in internal Flash, reboot the board with the "User Switch" pressed. Applications used with this boot loader must be compiled and linked to run from address `0x60000000` and must not program the system clock or reconfigure the EPI.

ext_demo_1 This simple application prints some status to UART0 then transfers control back to the boot loader in preparation for the download of a new firmware image. It must be used with the `boot_eth_ext` example boot loader and will not run in isolation.

ext_demo_2 This example is similar to `uart_echo` with the exception that it runs from external Flash memory and is configured to look for a particular string ("swupd") in the user input and transfer control back to the boot loader when this is detected. Like `ext_demo_1`, it must be used with the `boot_eth_ext` example boot loader and will not run in isolation.

2.5.1 Building and Running from External Memory

Building an application to run either partially or wholly from external memory requires a few changes compared to an application intended for internal Flash execution.

- A new linker script file is required which defines the external memory area and determines which sections of your code and data are to be placed there. The scripts used for the `ext_demo_1` and `ext_demo_2` applications may be used as references to develop your own versions.

- The exception vector table must reside in either internal Flash or SRAM since the Cortex M3 Nested Vectored Interrupt Controller (NVIC) is unable to access the EPI memory region.
- All EPI configuration must be performed before any code or data in external memory is accessed. Similarly, no system clock changes which would require EPI reconfiguration may be made in the code executing from external memory.

The `boot_eth_ext` example boot loader configures the system clock and EPI on behalf of the application it will ultimately boot. It also copies the application vector table from the start of the binary in external Flash to the bottom of internal SRAM and sets the NVIC Vector Table Offset Register to point to the new table. The application linker scripts are set up to reserve this area of SRAM for the vector table.

2.5.2 Downloading an Image to External Flash

A binary image may be downloaded to external Flash using one of two methods. With the `boot_eth_ext` boot loader image in internal Flash, the LM Flash Programmer utility (LMFlash) may be used to write a binary file to the base of the Flash found on the Flash and SRAM Memory Expansion Board via Ethernet.

Other applications running in internal memory may also support download to external Flash by making use of the general purpose TFTP server found in `utils/tftp.c` along with a suitable support file which provides Flash write support. An example of such a file is `boards/dk-lm3s9b96/qs-checkout/tftp_qs.c` which supports reading and writing files either from or to the external Flash device on an Flash and SRAM Memory Expansion Board or the serial Flash device found on the base dk-lm3s9b96 board. With this server running as part of the Stellaris application, files can be written to the external Flash device using command-line TFTP from an Ethernet-connected host system.

2.5.3 Debugging Code in External Memory

Third-party debugger support for the EPI address space and external Flash is not currently included in the StellarisWare release, however, you can still debug code or view data in this memory region. Once the system clock and EPI configuration has been set, debuggers can read and display the content of EPI memory as usual.

Debugging code running in external memories can pose problems, however. The Cortex-M3 Flash Patch and Breakpoint (FPB) unit is unable to access the EPI address range so hardware breakpoints cannot be set in this region. This does not cause problems when debugging code from external SRAM or SDRAM since software breakpoints can be used as normal, but when debugging from external Flash, this is problematic and requires a different approach to debugging in this situation.

The Cortex-M3 Data Watchpoint and Trace (DWT) unit has visibility into EPI address space and, depending upon the debugger, this can allow read watchpoints to be used as code breakpoints in external Flash. If this is not possible on a particular debugger, code can be recompiled with an explicit breakpoint (BKPT) instruction or a "while(1)" at the desired position to force the processor to stop at that point. Execution can be resumed by editing the PC register value to skip to the next instruction.

Note: Assembler-level (but not source-level) single stepping is supported from external Flash since this does not require the use of a hardware breakpoint unit.

2.6 Stellaris LM3S9B96 FPGA Expansion Board

The FPGA Expansion Board is available separately from the basic dk-lm3s9b96 board and illustrates connection of an FPGA device to the lm3s9b96 Stellaris microcontroller using the General Purpose (or Machine-to-Machine) mode of the External Peripheral Interface (EPI). The expansion board contains following major components:

- Xilinx XC3S100E FPGA
- 1 MB of SRAM dedicated to the FPGA
- Omnivision OV7690/OV7191 VGA CMOS camera module

An I2C EEPROM on this board contains configuration and timing information allowing the [PinoutSet\(\)](#) function to configure the EPI correctly and allow all the devices to be accessed. I2C is also used to initialize and control the VGA camera on the board.

The expansion board connects to the jumper block near the QVGA touchscreen in addition to the EPI connector and intercepts control and data signals to the panel. The touchscreen display, which is normally accessed via GPIOs when this expansion board is absent, is driven directly from the FPGA which mixes graphics and video to generate the display. Note that the thumbwheel potentiometer must be disabled by removing the “PB4/POT” jumper when the FPGA Expansion Board is in use since this board requires the EPI signal which is found on pin PB4.

When this expansion board is detected during a call to [PinoutSet\(\)](#), the `g_eDaughterType` global variable is set allowing the display driver (`kitronix320x240x16_ssd2119_8bit.c`) and touchscreen driver (`touch.c`) to dynamically reconfigure themselves to operate with the new expansion board.

After a reset, the FPGA operates in a legacy mode where the LCD display controller’s control and data registers are mapped to locations in the FPGA address space. This mode is used by the `kitronix320x240x16_ssd2119_8bit.c` display driver and allows existing applications to continue working with the new expansion board present. To allow use of the motion video features of the FPGA, however, a second display handling mode is supported. This mode stores a copy of the current graphics frame buffer in FPGA SRAM and allows mixing of this image with video from the camera using chromakeying. To access this mode, a new display driver, `kitronix320x240x16_fpga.c` must be used. This driver will work only when the FPGA Expansion Board is present and requires the use of a different `tDisplay` pointer when initializing the graphics library and widgets. Otherwise it is functionally equivalent to the previous driver from an application’s perspective.

In addition to the new display driver, two other software modules are provided to offer support for the video features of the expansion board. These are both found in the `boards/dk-lm3s9b96/drivers` directory. File `camera.c` and its associated header `camera.h` provide a high level API to initialize and control the camera on the expansion board. Functions allow capture to be started or stopped, picture attributes such as mirror, flip, brightness and contrast to be set, and capture and display position and size to be controlled.

To allow motion video to be added easily to widget-based applications, another module, `vidwidget.c` may be used. This is a graphics library widget class which supports video display. A single widget of this class may be created and the widget’s area will be filled with motion video according to the widget’s current style and parameters passed to its various APIs. This widget class acts as a wrapper over the API provided by `camera.c`. Any application using it should not make calls to the camera API in addition to the video widget API since all camera control functions are supported at the widget interface level. Example application `videocap` illustrates the use of the video widget.

2.7 Stellaris LM3S9B96 EM2 Expansion Board

The EM2 Expansion Board is available separately from the basic dk-lm3s9b96 board and offers a simple interface between the dk-lm3s9b96 EPI connector and up to two Texas Instruments RF Evaluation Modules (EMs). The board provides no function other than routing SSI, UART, I2C, and GPIO signals to the appropriate module connectors. It contains a 1Kb I2C EEPROM which is used for board identification purposes. The content of the EEPROM causes the `PinoutSet` function to set the `g_eDaughterType` global variable to the `DAUGHTER_EM2` value and prevents the function from configuring the dk-lm3s9b96 EPI peripheral which is not used by the EM2 Expansion Board.

This expansion board enables wireless application development using Low Power RF and RFID evaluation modules on the dk-lm3s9b96 platform. Supported protocols include ZigBee and Texas Instruments' SimpliciTI.

3 Example Applications

The example applications show how to utilize features of the Cortex-M3 microprocessor, the peripherals on the Stellaris microcontroller, and the various libraries provided in the StellarisWare software release - the peripheral driver library, graphics library and USB library. These applications are intended for demonstration and as a starting point for new applications.

There is an IAR workspace file (`dk-lm3s9b96.eww`) that contains the peripheral driver library project, graphics library project, USB library project, and all of the board example projects, in a single, easy to use workspace for use with Embedded Workbench version 5.

There is a Keil multi-project workspace file (`dk-lm3s9b96.mpw`) that contains the peripheral driver library project, graphics library project, USB library project, and all of the board example projects, in a single, easy to use workspace for use with uVision.

All of these examples reside in the `boards/dk-lm3s9b96` subdirectory of the firmware development package source distribution.

3.1 IQmath Demonstration (IQmath_demo)

This application demonstrates the use of the IQmath library, allowing the performance relative to floating-point to be easily seen. A dodecahedron is rotated in 3-space and displayed on the screen. Each face of the dodecahedron has a Stellaris logo on it, bring the total vertex count to 704 (i.e. the number of points that are rotated in 3-space for every frame that is displayed).

When first started, the IQmath library will be used to rotate the dodecahedron (as indicated by the status line at the bottom of the display). The user push button on the lower right corner of the board will switch between using IQmath and floating-point math.

3.2 AES Pre-expanded Key (aes_expanded_key)

This example shows how to use pre-expanded keys to encrypt some plaintext, and then decrypt it back to the original message. Using pre-expanded keys avoids the need to perform the expansion at run-time. This example also uses cipher block chaining (CBC) mode instead of the simpler ECB mode.

This example uses the AES tables present in the Stellaris ROM, which makes the overall program smaller.

3.3 AES Normal Key (aes_set_key)

This example shows how to set an encryption key and then use that key to encrypt some plaintext. It then sets the decryption key and decrypts the previously encrypted block back to plaintext.

This example uses the AES tables present in the Stellaris ROM, which makes the overall program smaller.

3.4 Bit-Banding (bitband)

This example application demonstrates the use of the bit-banding capabilities of the Cortex-M3 microprocessor. All of SRAM and all of the peripherals reside within bit-band regions, meaning that bit-banding operations can be applied to any of them. In this example, a variable in SRAM is set to a particular value one bit at a time using bit-banding operations (it would be more efficient to do a single non-bit-banded write; this simply demonstrates the operation of bit-banding).

3.5 Blinky (blinky)

A very simple example that blinks the on-board LED.

3.6 Boot Loader Demo 1 (boot_demo1)

An example to demonstrate the use of a flash-based boot loader. At startup, the application will configure the UART, USB and Ethernet peripherals, and then branch to the boot loader to await the start of an update. If using the serial boot loader (boot_serial), the UART will always be configured at 115,200 baud and does not require the use of auto-bauding.

This application is intended for use with any of the three flash-based boot loader flavors (boot_eth, boot_serial or boot_usb) included in the software release. To accommodate the largest of these (boot_usb), the link address is set to 0x1800. If you are using serial or Ethernet boot loader, you may change this address to a 1KB boundary higher than the last address occupied by the boot loader binary as long as you also rebuild the boot loader itself after modifying its bl_config.h file to set APP_START_ADDRESS to the same value.

The boot_demo2 application can be used along with this application to easily demonstrate that the boot loader is actually updating the on-chip flash.

Note that the LM3S9B96 and other Tempest-class Stellaris devices also support serial and ethernet boot loaders in ROM in silicon revisions B1 or later. To make use of this function, link your application to run at address 0x0000 in flash and enter the bootloader using either the ROM_UpdateEthernet or ROM_UpdateSerial functions (defined in rom.h). This mechanism is used in the utils/swupdate.c module when built specifically targeting a suitable Tempest-class device.

3.7 Boot Loader Demo 2 (boot_demo2)

An example to demonstrate the use of a flash-based boot loader. At startup, the application will configure the UART, USB and Ethernet peripherals, wait for a widget on the screen to be pressed, and then branch to the boot loader to await the start of an update. If using the serial boot loader (boot_serial), the UART will always be configured at 115,200 baud and does not require the use of auto-bauding.

This application is intended for use with any of the three flash-based boot loader flavors (boot_eth, boot_serial or boot_usb) included in the software release. To accommodate the largest of these (boot_usb), the link address is set to 0x1800. If you are using serial or Ethernet boot loader, you may change this address to a 1KB boundary higher than the last address occupied by the boot

loader binary as long as you also rebuild the boot loader itself after modifying its `bl_config.h` file to set `APP_START_ADDRESS` to the same value.

The `boot_demo1` application can be used along with this application to easily demonstrate that the boot loader is actually updating the on-chip flash.

Note that the LM3S9B96 and other Tempest-class Stellaris devices also support serial and ethernet boot loaders in ROM in silicon revisions B1 or later. To make use of this function, link your application to run at address 0x0000 in flash and enter the bootloader using either the `ROM_UpdateEthernet` or `ROM_UpdateSerial` functions (defined in `rom.h`). This mechanism is used in the `utils/swupdate.c` module when built specifically targeting a suitable Tempest-class device.

3.8 Ethernet Boot Loader Demo (`boot_demo_eth`)

An example to demonstrate the use of remote update signaling with the flash-based Ethernet boot loader. This application configures the Ethernet controller and acquires an IP address which is displayed on the screen along with the board's MAC address. It then listens for a "magic packet" telling it that a firmware upgrade request is being made and, when this packet is received, transfers control into the boot loader to perform the upgrade.

Although there are three flavors of flash-based boot loader provided with this software release (`boot_serial`, `boot_eth` and `boot_usb`), this example is specific to the Ethernet boot loader since the magic packet used to trigger entry into the boot loader from the application is only sent via Ethernet.

The `boot_demo1` and `boot_demo2` applications do not make use of the Ethernet magic packet and can be used along with this application to easily demonstrate that the boot loader is actually updating the on-chip flash.

Note that the LM3S9B96 and other Tempest-class Stellaris devices also support serial and ethernet boot loaders in ROM in silicon revisions B1 or later. To make use of this function, link your application to run at address 0x0000 in flash and enter the bootloader using either the `ROM_UpdateEthernet` or `ROM_UpdateSerial` functions (defined in `rom.h`). This mechanism is used in the `utils/swupdate.c` module when built specifically targeting a suitable Tempest-class device.

3.9 USB Boot Loader Example (`boot_demo_usb`)

This example application is used in conjunction with the USB boot loader (`boot_usb`) and turns the evaluation board into a composite device supporting a mouse via the Human Interface Device class and also publishing runtime Device Firmware Upgrade (DFU) capability. Dragging a finger or stylus over the touchscreen translates into mouse movement and presses on marked areas at the bottom of the screen indicate mouse button press. This input is used to generate messages in HID reports sent to the USB host allowing the evaluation board to control the mouse pointer on the host system.

Since the device also publishes a DFU interface, host software such as the `dfuprog` tool can determine that the device is capable of receiving software updates over USB. The runtime DFU protocol allows such tools to signal the device to switch into DFU mode and prepare to receive a new software image.

Runtime DFU functionality requires only that the device listen for a particular request (`DETACH`) from the host and, when this is received, transfer control to the USB boot loader via the normal means to reenumerate as a pure DFU device capable of uploading and downloading firmware

images.

Windows device drivers for both the runtime and DFU mode of operation can be found in C:/StellarisWare/windows_drivers assuming you installed StellarisWare in the default directory.

To illustrate runtime DFU capability, use the `dfuprog` tool which is part of the Stellaris Windows USB Examples package (SW-USB-win-xxxx.msi). Assuming this package is installed in the default location, the `dfuprog` executable can be found in the C:/Program Files/Texas Instruments/Stellaris/usb_examples directory.

With the device connected to your PC and the device driver installed, enter the following command to enumerate DFU devices:

```
dfuprog -e
```

This will list all DFU-capable devices found and you should see that you have one device available which is in “Runtime” mode. Entering the following command will switch this device into DFU mode and leave it ready to receive a new firmware image:

```
dfuprog -m
```

After entering this command, you should notice that the device disconnects from the USB bus and reconnects again. Running “`dfuprog -e`” a second time will show that the device is now in DFU mode and ready to receive downloads. At this point, either LM Flash Programmer or `dfuprog` may be used to send a new application binary to the device.

3.10 Ethernet Boot Loader (boot_eth)

The boot loader is a small piece of code that can be programmed at the beginning of flash to act as an application loader as well as an update mechanism for an application running on a Stellaris microcontroller, utilizing either UART0, I2C0, SSIO, or Ethernet. The capabilities of the boot loader are configured via the `bl_config.h` include file. For this example, the boot loader uses Ethernet to load an application.

The configuration is set to boot applications which are linked to run from address 0x1800 in flash. This is higher than strictly necessary but is intended to allow the example boot loader-aware applications provided in the release to be used with any of the three boot loader example configurations supplied (serial, USB or ethernet) without having to adjust their link addresses.

Note that the LM3S9B96 and other Tempest-class Stellaris devices also support serial and ethernet boot loaders in ROM in silicon revisions B1 or later.

3.11 Ethernet Boot Loader for External Flash(boot_eth_ext)

The boot loader is a piece of code that can be programmed at the beginning of internal flash to act as an application loader as well as an update mechanism for an application running on a Stellaris microcontroller, utilizing either UART0, I2C0, SSIO, or Ethernet. The capabilities of the boot loader are configured via the `bl_config.h` include file. For this example, the boot loader uses Ethernet to load an application into external flash and runs it from there. The boot loader itself is the only application running in internal flash in this example.

The configuration is set to boot applications which are linked to run from address 0x60000000 (EPI-connected external flash) and requires that the DK-LM3S9B96 Flash/SRAM Daughter Board be installed. If the daughter board is not present, the boot loader will warn the user via a message on the display.

Note that execution from external flash should be avoided if at all possible due to significantly lower performance than achievable from internal flash. Using an 8-bit wide interface to flash as found on the Flash/SRAM/LCD daughter board and remembering that an external memory access via EPI takes 8 or 9 system clock cycles, a program running from off-chip memory will typically run at approximately 5% of the speed of the same program in internal flash.

3.12 Boot Loader (boot_serial)

The boot loader is a small piece of code that can be programmed at the beginning of flash to act as an application loader as well as an update mechanism for an application running on a Stellaris microcontroller, utilizing either UART0, I2C0, SSI0, Ethernet or USB. The capabilities of the boot loader are configured via the `bl_config.h` include file. For this example, the boot loader uses UART0 to load an application.

The configuration is set to boot applications which are linked to run from address 0x1800 in flash. This is higher than strictly necessary but is intended to allow the example boot loader-aware applications provided in the release to be used with any of the three boot loader example configurations supplied (serial, USB or ethernet) without having to adjust their link addresses.

Note that the LM3S9B96 and other Tempest-class Stellaris devices also support serial and ethernet boot loaders in ROM in silicon revisions B1 or later.

3.13 USB Boot Loader (boot_usb)

The boot loader is a small piece of code that can be programmed at the beginning of flash to act as an application loader as well as an update mechanism for an application running on a Stellaris microcontroller, utilizing either UART0, I2C0, SSI0, Ethernet or USB. The capabilities of the boot loader are configured via the `bl_config.h` include file. For this example, the boot loader uses the USB Device Firmware Upgrade (DFU) class to download an application.

Applications intended for use with this version of the boot loader should be linked to run from address 0x1800 in flash (rather than the default run address of 0). This address is chosen to ensure that boot loader images built with all supported compilers may be used without modifying the application start address. Depending upon the compiler and optimization level you are using, however, you may find that you can reclaim some space by lowering this address and rebuilding both the application and boot loader. To do this, modify the makefile or project you use to build the application to show the new run address and also change the `APP_START_ADDRESS` value defined in `bl_config.h` before rebuilding the boot loader.

The USB boot loader may be demonstrated using the `boot_demo1` and `boot_demo2` example applications in addition to the `boot_usb` boot loader binary itself. Note that these are the only two example applications currently configured to run alongside the USB boot loader but making any of the other applications boot loader compatible is simply a matter of relinking them with the new start address and adding a mechanism to transfer control to the boot loader when required.

The Windows device driver required to communicate with the USB boot loader can be

found on the software and documentation CD from the development kit package. It can also be found in the Windows driver package which can be downloaded via a link from <http://www.ti.com/stellaris>.

A Windows command-line application, `dfuprog`, is also provided which illustrates how to perform uploads and downloads via the USB DFU protocol. The source for this application can be found in the “C:” directory and the prebuilt executable is available in the package “Windows-side examples for USB kits” available for download via a link from <http://www.ti.com/stellarisware>.

3.14 Calibration for the Touch Screen (calibrate)

The raw sample interface of the touch screen driver is used to compute the calibration matrix required to convert raw samples into screen X/Y positions. The produced calibration matrix can be inserted into the touch screen driver to map the raw samples into screen coordinates.

The touch screen calibration is performed according to the algorithm described by Carlos E. Videles in the June 2002 issue of *Embedded Systems Design*. It can be found online at <http://www.embedded.com/story/OEG20020529S0046>.

3.15 Daughter Board EEPROM Read/Write Example (dbeeprom)

This application may be used to read and write the ID structures written into the 128 byte EEPROMs found on the optional Flash/SRAM and FPGA daughter boards for `dk-lm3s9b96`. A command line interface is provided via UART 0 and commands allow the existing ID EEPROM content to be read and one of the standard structures identifying the available daughter boards to be written to the device.

The ID EEPROM is read in function `PinoutSet()` and used to configure the EPI interface appropriately for the attached daughter board. If the EEPROM content is incorrect, this auto-configuration will not be possible and example applications will typically show merely a blank display when run.

3.16 Ethernet-based I/O Control (enet_io)

This example application demonstrates web-based I/O control using the Stellaris Ethernet controller and the lwIP TCP/IP Stack. DHCP is used to obtain an Ethernet address. If DHCP times out without obtaining an address, a static IP address will be chosen using AutoIP. The address that is selected will be shown on the QVGA display allowing you to access the internal web pages served by the application via your normal web browser.

Two different methods of controlling board peripherals via web pages are illustrated via pages labeled “IO Control Demo 1” and “IO Control Demo 2” in the navigation menu on the left of the application’s home page. In both cases, the example allows you to toggle the state of the user LED on the board and set the speed of a graphical animation on the display.

“IO Control Demo 1” uses JavaScript running in the web browser to send HTTP requests for particular special URLs. These special URLs are intercepted in the file system support layer (`lmi_fs.c`)

and used to control the LED and animation. Responses generated by the board are returned to the browser and inserted into the page HTML dynamically by more JavaScript code.

“IO Control Demo 2” uses standard HTML forms to pass parameters to CGI (Common Gateway Interface) handlers running on the board. These handlers process the form data and control the animation and LED as requested before sending a response page (in this case, the original form) back to the browser. The application registers the names and handlers for each of its CGIs with the HTTPD server during initialization and the server calls these handlers after parsing URL parameters each time one of the CGI URLs is requested.

Information on the state of the various controls in the second demo is inserted into the served HTML using SSI (Server Side Include) tags which are parsed by the HTTPD server in the application. As with the CGI handlers, the application registers its list of SSI tags and a handler function with the web server during initialization and this handler is called whenever any registered tag is found in a .shtml, .ssi, .shtm or .xml file being served to the browser.

In addition to LED and animation speed control, the second example also allows a line of text to be sent to the board for display on the LCD panel. This is included to illustrate the decoding of HTTP text strings.

Note that the web server used by this example has been modified from the example shipped with the basic lwIP package. Additions include SSI and CGI support along with the ability to have the server automatically insert the HTTP headers rather than having these built in to the files in the file system image.

For additional details on lwIP, refer to the lwIP web page at:
<http://savannah.nongnu.org/projects/lwip/>

3.17 Ethernet with lwIP (enet_lwip)

This example application demonstrates the operation of the Stellaris Ethernet controller using the lwIP TCP/IP Stack. DHCP is used to obtain an Ethernet address. If DHCP times out without obtaining an address, AUTOIP will be used to obtain a link-local address. The address that is selected will be shown on the QVGA display.

The file system code will first check to see if an SD card has been plugged into the microSD slot. If so, all file requests from the web server will be directed to the SD card. Otherwise, a default set of pages served up by an internal file system will be used.

For additional details on lwIP, refer to the lwIP web page at:
<http://savannah.nongnu.org/projects/lwip/>

3.18 Ethernet with PTP (enet_ptpd)

This example application demonstrates the operation of the Stellaris Ethernet controller using the lwIP TCP/IP Stack. DHCP is used to obtain an Ethernet address. If DHCP times out without obtaining an address, AutoIP will be used to obtain a link-local address. The address that is selected will be shown on the QVGA display and output to the UART.

A default set of pages will be served up by an internal file system and the httpd server.

The IEEE 1588 (PTP) software has been enabled in this code to synchronize the internal clock to

a network master clock source.

UART0, connected to the FTDI virtual COM port and running at 115,200, 8-N-1, is used to display messages from this application.

For additional details on lwIP, refer to the lwIP web page at: <http://savannah.nongnu.org/projects/lwip/>

For additional details on the PTPd software, refer to the PTPd web page at: <http://ptpd.sourceforge.net>

3.19 Ethernet with uIP (enet_uip)

This example application demonstrates the operation of the Stellaris Ethernet controller using the uIP TCP/IP Stack. DHCP is used to obtain an Ethernet address. A basic web site is served over the Ethernet port. The web site displays a few lines of text, and a counter that increments each time the page is sent.

For additional details on uIP, refer to the uIP web page at: <http://www.sics.se/~adam/uip/>

3.20 External flash execution demonstration (ext_demo_1)

This example application illustrates execution out of external flash attached via the Im3s9b96 Extended Peripheral Interface (EPI). It utilizes the UART to display a simple message before immediately transferring control back to the boot loader in preparation for the download of a new application image. The first UART (connected to the FTDI virtual serial port on the development kit board) will be configured in 115200 baud, 8-n-1 mode.

This application is configured specifically for execution from external flash and relies upon the external flash version of the Ethernet boot loader, boot_eth_ext, being present in internal flash. It will not run with any other boot loader version. The boot_eth_ext boot loader configures the system clock and EPI to allow access to the external flash address space then relocates the application exception vectors into internal SRAM before branching to the main application in daughter-board flash.

Note that execution from external flash should be avoided if at all possible due to significantly lower performance than achievable from internal flash. Using an 8-bit wide interface to flash as found on the Flash/SRAM/LCD daughter board and remembering that an external memory access via EPI takes 8 or 9 system clock cycles, a program running from off-chip memory will typically run at approximately 5% of the speed of the same program in internal flash.

3.21 UART Echo running in external flash (ext_demo_2)

This example application is equivalent to uart_echo but has been reworked to run out of external flash attached via the Im3s9b96 Extended Peripheral Interface (EPI). It utilizes the UART to echo text. The first UART (connected to the FTDI virtual serial port on the evaluation board) will be configured in 115,200 baud, 8-n-1 mode. All characters received on the UART are transmitted back

to the UART and this continues until "swupd" followed by a carriage return is entered, at which point the application transfers control back to the boot loader to initiate a firmware update.

This application is configured specifically for execution from external flash and relies upon the external flash version of the Ethernet boot loader, `boot_eth_ext`, being present in internal flash. It will not run with any other boot loader version. The `boot_eth_ext` boot loader configures the system clock and EPI to allow access to the external flash address space then relocates the application exception vectors into internal SRAM before branching to the main application in daughter-board flash.

Note that execution from external flash should be avoided if at all possible due to significantly lower performance than achievable from internal flash. Using an 8-bit wide interface to flash as found on the Flash/SRAM/LCD daughter board and remembering that an external memory access via EPI takes 8 or 9 system clock cycles, a program running from off-chip memory will typically run at approximately 5% of the speed of the same program in internal flash.

3.22 GPIO JTAG Recovery (`gpio_jtag`)

This example demonstrates changing the JTAG pins into GPIOs, along with a mechanism to revert them to JTAG pins. When first run, the pins remain in JTAG mode. Pressing the touchscreen will toggle the pins between JTAG and GPIO modes.

In this example, four pins (PC0, PC1, PC2, and PC3) are switched.

3.23 Graphics Library Demonstration (`glib_demo`)

This application provides a demonstration of the capabilities of the Stellaris Graphics Library. A series of panels show different features of the library. For each panel, the bottom provides a forward and back button (when appropriate), along with a brief description of the contents of the panel.

The first panel provides some introductory text and basic instructions for operation of the application.

The second panel shows the available drawing primitives: lines, circles, rectangles, strings, and images.

The third panel shows the canvas widget, which provides a general drawing surface within the widget hierarchy. A text, image, and application-drawn canvas are displayed.

The fourth panel shows the check box widget, which provides a means of toggling the state of an item. Four check boxes are provided, with each having a red "LED" to the right. The state of the LED tracks the state of the check box via an application callback.

The fifth panel shows the container widget, which provides a grouping construct typically used for radio buttons. Containers with a title, a centered title, and no title are displayed.

The sixth panel shows the push button widget. Two columns of push buttons are provided; the appearance of each column is the same but the left column does not utilize auto-repeat while the right column does. Each push button has a red "LED" to its left, which is toggled via an application callback each time the push button is pressed.

The seventh panel shows the radio button widget. Two groups of radio buttons are displayed, the first using text and the second using images for the selection value. Each radio button has a red

“LED” to its right, which tracks the selection state of the radio buttons via an application callback. Only one radio button from each group can be selected at a time, though the radio buttons in each group operate independently.

The eighth and final panel shows the slider widget. Six sliders constructed using the various supported style options are shown. The slider value callback is used to update two widgets to reflect the values reported by sliders. A canvas widget near the top right of the display tracks the value of the red and green image-based slider to its left and the text of the grey slider on the left side of the panel is update to show its own value. The slider on the right is configured as an indicator which tracks the state of the upper slider and ignores user input.

3.24 Hello World (hello)

A very simple “hello world” example. It simply displays “Hello World!” on the display and is a starting point for more complicated applications. This example uses calls to the Stellaris Graphics Library graphics primitives functions to update the display. For a similar example using widgets, please see “hello_widget”.

3.25 Hello using Widgets (hello_widget)

A very simple “hello world” example written using the Stellaris Graphics Library widgets. It displays a button which, when pressed, toggles display of the words “Hello World!” on the screen. This may be used as a starting point for more complex widget-based applications.

3.26 I2S example application using SD Card FAT file system (i2s_demo)

This example application demonstrates playing wav files from an SD card that is formatted with a FAT file system. The application will only look in the root directory of the SD card and display all files that are found. Files can be selected to show their format and then played if the application determines that they are a valid .wav file. Only PCM format (uncompressed) files may be played.

For additional details about FatFs, see the following site:
http://elm-chan.org/fsw/ff/00index_e.html

3.27 I2S Record and Playback (i2s_filter)

This example application demonstrates recording audio from the codec’s ADC, transferring the audio over the I2S receive interface to the microcontroller, and then sending the audio back to the codec via the I2S transmit interface. A line level audio source should be fed into the LINE IN audio jack which will be recorded with the codec’s ADC and then played back through both the HEADPHONE and LINE OUT jacks. This application requires some modifications to the default jumpers on the board in order for the audio record path to function correctly. The PD4/LD4 jumper

should be removed and placed on the PD4/RXSD jumper to receive I2S data. The PD4/LD4 is located in the row of jumpers near the LCD connector while the PD4/RXSD jumper is in the row near the audio jacks.

Note:

Moving this jumper will cause the LCD to not function for other applications so the jumper should be moved back to run other applications.

3.28 I2S Record and Playback with Speex codec (i2s_speex_enc)

This example application demonstrates recording audio from the codec's ADC, transferring the audio over the I2S receive interface to the microcontroller, encoding the audio using a Speex encoder and then decoding the audio and sending the audio back to the codec via the I2S transmit interface. A line level audio source should be fed into the LINE IN audio jack which will be recorded with the codec's ADC and then played back through both the HEADPHONE and LINE OUT jacks. The application provides a simple command line interface via the virtual COM port. To get the list of supported commands, connect a serial communication program to the virtual COM port at 115200, no parity, 8 data bits, one stop bit. The "help" command will provide a list of valid commands. The current commands are "bypass" which will disable the Speex encoder and will pass the audio directly from input to output unmodified but still using the I2S record data. This is useful for hearing the audio at the current audio resolution without any encoding and decoding. The "speex" command takes an integer quality parameter that ranges from 0-4. The larger the number the higher the quality setting for the encoder.

This application requires some modifications to the default jumpers on the board in order for the audio record path to function correctly. The PD4/LD4 jumper should be removed and placed on the PD4/RXSD jumper to receive I2S data. The PD4/LD4 is located in the row of jumpers near the LCD connector while the PD4/RXSD jumper is in the row near the audio jacks.

Note:

Moving this jumper will cause the LCD to not function for other applications so the jumper should be moved back to run other applications.

3.29 Interrupts (interrupts)

This example application demonstrates the interrupt preemption and tail-chaining capabilities of Cortex-M3 microprocessor and NVIC. Nested interrupts are synthesized when the interrupts have the same priority, increasing priorities, and decreasing priorities. With increasing priorities, preemption will occur; in the other two cases tail-chaining will occur. The currently pending interrupts and the currently executing interrupt will be displayed on the display; GPIO pins B3, F2 and F3 (the GPIO on jumper JP40 and the two Ethernet LEDs) will be asserted upon interrupt handler entry and de-asserted before interrupt handler exit so that the off-to-on time can be observed with a scope or logic analyzer to see the speed of tail-chaining (for the two cases where tail-chaining is occurring).

3.30 Graphics Library String Table Demonstration (lang_demo)

This application provides a demonstration of the capabilities of the Stellaris Graphics Library's string table functions. Two panels show different implementations of features of the string table functions. For each panel, the bottom provides a forward and back button (when appropriate).

The first panel provides a large string with introductory text and basic instructions for operation of the application.

The second panel shows the available languages and allows them to be switched between English, German, Spanish and Italian.

3.31 MPU (mpu_fault)

This example application demonstrates the use of the MPU to protect a region of memory from access, and to generate a memory management fault when there is an access violation.

3.32 USB OTG HID Mouse Example (otg_detect)

This example application demonstrates the use of USB On-The-Go (OTG) to offer both USB host and device operation. When the EK board is connected to a USB host, it acts as a BIOS-compatible USB mouse. The user button on the board (nearest the USB OTG connector) acts as mouse button 1 and the mouse pointer may be moved by dragging your finger or a stylus across the touchscreen in the desired direction.

If a USB mouse is connected to the USB OTG port, the board operates as a USB host and draws dots on the display to track the mouse movement. The states of up to three mouse buttons are shown at the bottom right of the display.

3.33 Quickstart Checkout Application (qs-checkout)

This widget-based application exercises many of the peripherals found on the dk-lm3s9b96 development kit board. It offers the following features:

- USB mouse support. The application will show the state of up to three mouse buttons and a cursor position when a USB mouse is connected to the board. If the board is connected by USB to a host, it will operate as a USB mouse. In this mode, the host mouse pointer may be moved by dragging a finger across the touchscreen and the user button on the development board will act as mouse button 1.
- TFTP server. This server allows the image stored in the 1MB serial flash device to be written and read from a remote Ethernet-connected system. The image in the serial flash is copied to SDRAM on startup and used as the source for the external web server file system. Suitable images can be created using the makefsfile utility with the -b and -h switches. If an Flash/SRAM/LCD daughter board is detected, the external file system is served directly

from the flash on this board assuming an image is found there. To upload a binary image to the serial flash, use the TFTP command line `tftp -i <board IP> PUT <binary file> eeprom`. To read the current image out of serial flash, use command line `tftp -i <board IP> GET eeprom <binary file>`. To read or write the file system image on the Flash/SRAM/LCD daughter board (if present), use the same commands but replace “eeprom” with “exflash”. When shipped, the serial flash on the board contains file `ramfs_data.bin` which contains a web photo gallery.

- **Web server.** The lwIP TCP/IP stack is used to implement a web server which can serve files from an internal file system, a FAT file system on an installed microSD card or USB flash drive, or a file system image stored in serial flash and copied to SDRAM during initialization. These file systems may be accessed from a web browser using URLs `http://<board IP>`, `http://<board IP>/sdcard/<filename>`, `http://<board IP>/usb/<filename>` and `http://<board ID>/ram/<filename>` respectively where `<board IP>` is the dotted decimal IP address assigned to the board and `<filename>` indicates the particular file being requested. Note that the web server does not open default filenames (such as `index.htm`) in any directory other than the root so the full path and filename must be specified in all other cases.
- **Touch screen.** The current touch coordinates are displayed whenever the screen is pressed.
- **LED control.** A GUI widget allows control of the user LED on the board.
- **Serial command line.** A simple command line is implemented via UART0. Connect a terminal emulator set to 115200/8/N/1 and enter “help” for information on supported commands.
- **JPEG image viewer.** The QVGA display is used to display JPEG images from the “images” directory in the web server’s external file system image. The user can scroll the image on the display using the touchscreen.
- **Audio player.** Uncompressed WAV files stored on the microSD card or USB flash drive may be played back via the headphone jack on the I2S daughter board. Available wave audio files are shown in a listbox on the left side of the display. Click the desired file then press “Play” to play it back. Volume control is provided via a touchscreen slider shown on the right side of the display.

3.34 SafeRTOS Example (safertos_demo)

This application utilizes SafeRTOS to perform a variety of tasks in a concurrent fashion. The following tasks are created:

An lwIP task, which serves up web pages via the Ethernet interface. This is actually two tasks, one which runs the lwIP stack and one which manages the Ethernet interface (sending and receiving raw packets).

An LED task, which simply blinks the on-board LED at a user-controllable rate (changed via the web interface).

A set of spider tasks, each of which controls a spider that crawls around the LCD. The speed at which the spiders move is controllable via the web interface. Up to thirty-two spider tasks can be run concurrently (an application-imposed limit).

A spider control task, which manages presses on the touch screen and determines if a spider task should be terminated (if the spider is “squished”) or if a new spider task should be created (if no spider is “squished”).

There is an automatically created idle task, which monitors changes in the board's IP address and sends those changes to the user via a UART message.

Across the bottom of the LCD, several status items are displayed: the amount of time the application has been running, the number of tasks that are running, the IP address of the board, the number of Ethernet packets that have been transmitted, and the number of Ethernet packets that have been received.

The finder application (in tools/finder) can also be used to discover the IP address of the board. The finder application will search the network for all boards that respond to its requests and display information about them.

The web site served by lwIP includes the ability to adjust the toggle rate of the LED task and the update speed of the spiders (all spiders move at the same speed).

For additional details on SafeRTOS, refer to the SafeRTOS web page at:
<http://www.highintegritysystems.com/safertos/>

For additional details on lwIP, refer to the lwIP web page at:
<http://savannah.nongnu.org/projects/lwip/>

3.35 Scribble Pad (scribble)

The scribble pad provides a drawing area on the screen. Touching the screen will draw onto the drawing area using a selection of fundamental colors (in other words, the seven colors produced by the three color channels being either fully on or fully off). Each time the screen is touched to start a new drawing, the drawing area is erased and the next color is selected.

3.36 SD card using FAT file system (sd_card)

This example application demonstrates reading a file system from an SD card. It makes use of FatFs, a FAT file system driver. It provides a simple widget-based console on the display and also a UART-based command line for viewing and navigating the file system on the SD card.

For additional details about FatFs, see the following site:
http://elm-chan.org/fsw/ff/00index_e.html

The application may also be operated via a serial terminal attached to UART0. The RS232 communication parameters should be set to 115,200 bits per second, and 8-n-1 mode. When the program is started a message will be printed to the terminal. Type "help" for command help.

3.37 JPEG Image Decompression (showjpeg)

This example application decompresses a JPEG image which is linked into the application and shows it on the 320x240 display. External RAM is used for image storage and decompression workspace. The image may be scrolled in the display window by dragging a finger across the touchscreen.

JPEG decompression and display are handled using a custom graphics library widget, the source for which can be found in drivers/jpgwidget.c.

The JPEG library used by this application is release 6b of the Independent JPEG Group's reference decoder. For more information, see the README and various text file in the /third_party/jpeg directory or visit <http://www.ijg.org/>.

3.38 SoftUART Echo (softuart_echo)

This example application utilizes the SoftUART to echo text. The SoftUART is configured to use the same pins as the first UART (connected to the FTDI virtual serial port on the evaluation board), at 115,200 baud, 8-n-1 mode. All characters received on the SoftUART are transmitted back to the SoftUART.

3.39 Timer (timers)

This example application demonstrates the use of the timers to generate periodic interrupts. One timer is set up to interrupt once per second and the other to interrupt twice per second; each interrupt handler will toggle its own indicator on the display.

3.40 UART Echo (uart_echo)

This example application utilizes the UART to echo text. The first UART (connected to the FTDI virtual serial port on the evaluation board) will be configured in 115,200 baud, 8-n-1 mode. All characters received on the UART are transmitted back to the UART.

3.41 uDMA (udma_demo)

This example application demonstrates the use of the uDMA controller to transfer data between memory buffers, and to transfer data to and from a UART. The test runs for 10 seconds before exiting.

3.42 USB Audio Device (usb_dev_audio)

This example application makes the evaluation board a USB audio device that supports a single 16 bit stereo audio stream at 48 kHz sample rate. The application can also receive volume control and mute changes and apply them to the sound driver. These changes will only affect the headphone output and not the line output because the audio DAC used on this board only allows volume changes to the headphones.

The USB audio device example will work on any operating system that supports USB audio class devices natively. There should be no addition operating system specific drivers required to use the example. The application's main task is to pass buffers to the the USB library's audio device class, receive them back with audio data and pass the buffers on to the sound driver for this board.

3.43 USB Generic Bulk Device (`usb_dev_bulk`)

This example provides a generic USB device offering simple bulk data transfer to and from the host. The device uses a vendor-specific class ID and supports a single bulk IN endpoint and a single bulk OUT endpoint. Data received from the host is assumed to be ASCII text and it is echoed back with the case of all alphabetic characters swapped.

A Windows INF file for the device is provided on the installation CD. This INF contains information required to install the WinUSB subsystem on WindowsXP and Vista PCs. WinUSB is a Windows subsystem allowing user mode applications to access the USB device without the need for a vendor-specific kernel mode driver. The device driver may also be downloaded from http://www.luminarymicro.com/products/software_updates.html as part of the “Stellaris embedded USB drivers” package (SW-USB-windrivers).

A sample Windows command-line application, `usb_bulk_example`, illustrating how to connect to and communicate with the bulk device is also provided. The application binary is installed as part of the “Windows-side examples for USB kits” package (SW-USB-win) on the installation CD or via download from http://www.luminarymicro.com/products/software_updates.html. Project files are included to allow the examples to be built using Microsoft VisualStudio. Source code for this application can be found in directory `StellarisWare/tools/usb_bulk_example`.

3.44 USB Audio Device (`usb_dev_caudiohid`)

This example application turns the evaluation board into a composite USB device supporting the Human Interface Device keyboard class and a USB audio device that supports playback of a single 16 bit stereo audio stream at 48 kHz sample rate.

The audio device supports only playback and will respond to volume control and mute changes and apply them to the sound driver. These volume control changes will only affect the headphone output and not the line output because the audio DAC used on this board only allows volume changes to the headphones. The USB audio device example will work on any operating system that supports USB audio class devices natively. There should be no additional operating system specific drivers required to use the example. The application's main task is to pass buffers to the USB library's audio device class, receive them back with audio data and pass the buffers on to the sound driver for this board.

This keyboard device supports the Human Interface Device class and the color LCD display shows a virtual keyboard and taps on the touchscreen will send appropriate key usage codes back to the USB host. Modifier keys (Shift, Ctrl and Alt) are “sticky” and tapping them toggles their state. The board status LED is used to indicate the current Caps Lock state and is updated in response to pressing the “Caps” key on the virtual keyboard or any other keyboard attached to the same USB host system. The keyboard device implemented by this application also supports USB remote wakeup allowing it to request the host to reactivate a suspended bus. If the bus is suspended (as indicated on the application display), touching the display will request a remote wakeup assuming the host has not specifically disabled such requests.

3.45 USB HID Keyboard Device (usb_dev_keyboard)

This example application turns the evaluation board into a USB keyboard supporting the Human Interface Device class. The color LCD display shows a virtual keyboard and taps on the touchscreen will send appropriate key usage codes back to the USB host. Modifier keys (Shift, Ctrl and Alt) are “sticky” and tapping them toggles their state. The board status LED is used to indicate the current Caps Lock state and is updated in response to pressing the “Caps” key on the virtual keyboard or any other keyboard attached to the same USB host system.

The device implemented by this application also supports USB remote wakeup allowing it to request the host to reactivate a suspended bus. If the bus is suspended (as indicated on the application display), touching the display will request a remote wakeup assuming the host has not specifically disabled such requests.

3.46 USB HID Mouse Device (usb_dev_mouse)

This example application turns the evaluation board into a USB mouse supporting the Human Interface Device class. Dragging a finger or stylus over the touchscreen translates into mouse movement and presses on marked areas at the bottom of the screen indicate mouse button press. This input is used to generate messages in HID reports sent to the USB host allowing the evaluation board to control the mouse pointer on the host system.

3.47 USB MSC Device (usb_dev_msc)

This example application turns the evaluation board into a USB mass storage class device. The application will use the microSD card for the storage media for the mass storage device. The screen will display the current action occurring on the device ranging from disconnected, no media, reading, writing and idle.

3.48 USB Serial Device (usb_dev_serial)

This example application turns the evaluation kit into a virtual serial port when connected to the USB host system. The application supports the USB Communication Device Class, Abstract Control Model to redirect UART0 traffic to and from the USB host system.

Assuming you installed StellarisWare in the default directory, a driver information (INF) file for use with Windows XP, Windows Vista and Windows7 can be found in C:/StellarisWare/windows_drivers. For Windows 2000, the required INF file is in C:/StellarisWare/windows_drivers/win2K.

3.49 USB host audio example application using SD Card FAT file system (usb_host_audio)

This example application demonstrates playing .wav files from an SD card that is formatted with a FAT file system using USB host audio class. The application will only look in the root directory of the SD card and display all files that are found. Files can be selected to show their format and then played if the application determines that they are a valid .wav file. Only PCM format (uncompressed) files may be played.

For additional details about FatFs, see the following site:
http://elm-chan.org/fsw/ff/00index_e.html

3.50 USB host audio example application using a USB audio device for input and I2S for output. (usb_host_audioin)

This example application demonstrates streaming audio from a USB audio device that supports recording an audio source at 48000 16 bit stereo. The application will start recording audio from the USB audio device when the "Play" button is pressed and stream it to the I2S output on the board. Because some audio devices require more power, you may need to use an external 5 volt supply to provide enough power to the USB audio device.

3.51 USB HID Keyboard Host (usb_host_keyboard)

This example application demonstrates how to support a USB keyboard attached to the evaluation kit board. The display will show if a keyboard is currently connected and the current state of the Caps Lock key on the keyboard that is connected on the bottom status area of the screen. Pressing any keys on the keyboard will cause them to be printed on the screen and to be sent out the UART at 115200 baud with no parity, 8 bits and 1 stop bit. Any keyboard that supports the USB HID BIOS protocol should work with this demo application.

3.52 USB Mass Storage Class Host Example (usb_host_msc)

This example application demonstrates reading a file system from a USB flash disk. It makes use of FatFs, a FAT file system driver. It provides a simple widget-based console on the display and also a UART-based command line for viewing and navigating the file system on the flash disk.

For additional details about FatFs, see the following site:
http://elm-chan.org/fsw/ff/00index_e.html

The application may also be operated via a serial terminal attached to UART0. The RS232 communication parameters should be set to 115,200 bits per second, and 8-n-1 mode. When the program

is started a message will be printed to the terminal. Type “help” for command help.

3.53 USB Stick Update Demo (usb_stick_demo)

An example to demonstrate the use of the flash-based USB stick update program. This example is meant to be loaded into flash memory from a USB memory stick, using the USB stick update program (usb_stick_update), running on the microcontroller.

After this program is built, the binary file (usb_stick_demo.bin), should be renamed to the filename expected by usb_stick_update ("FIRMWARE.BIN" by default) and copied to the root directory of a USB memory stick. Then, when the memory stick is plugged into the eval board that is running the usb_stick_update program, this example program will be loaded into flash and then run on the microcontroller.

This program simply displays a message on the screen and prompts the user to press the select button. Once the button is pressed, control is passed back to the usb_stick_update program which is still in flash, and it will attempt to load another program from the memory stick. This shows how a user application can force a new firmware update from the memory stick.

3.54 USB Memory Stick Updater (usb_stick_update)

This example application behaves the same way as a boot loader. It resides at the beginning of flash, and will read a binary file from a USB memory stick and program it into another location in flash. Once the user application has been programmed into flash, this program will always start the user application until requested to load a new application.

When this application starts, if there is a user application already in flash (at **APP_START_ADDRESS**), then it will just run the user application. It will attempt to load a new application from a USB memory stick under the following conditions:

- no user application is present at **APP_START_ADDRESS**
- the user application has requested an update by transferring control to the updater
- the user holds down the eval board push button when the board is reset

When this application is attempting to perform an update, it will wait forever for a USB memory stick to be plugged in. Once a USB memory stick is found, it will search the root directory for a specific file name, which is *FIRMWARE.BIN* by default. This file must be a binary image of the program you want to load (the .bin file), linked to run from the correct address, at **APP_START_ADDRESS**.

The USB memory stick must be formatted as a FAT16 or FAT32 file system (the normal case), and the binary file must be located in the root directory. Other files can exist on the memory stick but they will be ignored.

3.55 Video Capture (videocap)

This example application makes use of the optional FPGA daughter board to capture and display motion video on the LCD display. VGA resolution (640x480) video is captured from the daughter

board camera and shown either scaled to the QVGA (320x240) resolution of the display or full size, in which case 25% of the image is visible and the user may scroll over the full image by dragging a finger on the video area of the touchscreen.

The main screen of the application offers the following controls:

Scale/Zoom This button toggles the video display between scaled and zoomed modes. In scaled mode, the 640x480 VGA video captured from the camera is downscaled by a factor of two in each dimension making it fit on the 320x240 QVGA display. In zoomed mode, the video image is shown without scaling and is clipped before being placed onto the display. The user can drag a finger or stylus over the touchscreen to scroll the area of the video which is visible.

Freeze/Unfreeze Use this button to freeze and unfreeze the video on the display. When the video is frozen, a copy of the image may be saved to SDCard as a Windows bitmap file by pressing the "Save" button.

Controls/Save When motion video is being displayed, this button displays "Controls" and allows you to adjust picture brightness, saturation and contrast by means of three slider controls which are shown when the button is pressed. Once you are finished with image adjustments, pressing the "Main" button will return you to the main controls screen. When video is frozen, this button shows "Save" and pressing it will save the currently displayed video image onto a microSD card if one is installed.

Hide This button hides all user interface elements to offer a clearer view of the video. To show the buttons again, press the small, red "Show" button displayed in the bottom right corner of the screen.

Note that jumper "PB4/POT" on the main development kit board must be removed when using the FPGA/Camera/LCD daughter board since the EPI signal available on this pin is required for correct operation of the board.

3.56 Watchdog (watchdog)

This example application demonstrates the use of the watchdog as a simple heartbeat for the system. If the watchdog is not periodically fed, it will reset the system. Each time the watchdog is fed, the LED is inverted so that it is easy to see that it is being fed, which occurs once every second. To stop the watchdog being fed and, hence, cause a system reset, tap the screen.

4 Development System Utilities

These are tools that run on the development system, not on the embedded target. They are provided to assist in the development of firmware for Stellaris microcontrollers.

These tools reside in the `tools` subdirectory of the firmware development package source distribution.

USB DFU Programmer

Usage:

```
dfuprog [OPTION]...
```

Description:

Downloads images to a Texas Instruments Stellaris microcontroller running the USB Device Firmware Upgrade boot loader. Additionally, this utility may be used to read back the existing application image or a subsection of flash and store it either as raw binary data or as a DFU-downloadable image file.

The source code for this utility is contained in `tools/dfuprog`. The binary for this utility is installed as part of the “Windows-side examples for USB kits” package (SW-USB-win) shipped on the release CD and downloadable from http://www.luminarymicro.com/products/software_updates.html. A Microsoft Visual Studio project file is provided to allow the application to be built.

Arguments:

- e specifies the address of the binary.
- u specifies that an image is to be uploaded from the board into the target file. If absent, the file will be downloaded to the board.
- c specifies that a section of flash memory is to be cleared. The address and size of the block may be specified using the -a and -l parameters. If these are absent, the entire writable area of flash is erased.
- f **FILE** specifies the name of the file to download or, if -u is given, to upload.
- b specifies that an uploaded file is to be stored as raw binary data without the DFU file wrapper. This option is only valid if used alongside -u.
- d specifies that the VID and PID in the DFU file wrapper should be ignored for a download operation.
- s specifies that image verification should be skipped following a download operation.
- a **ADDR** specifies the address at which the binary file will be downloaded or from which an uploaded file will be read. If a download operation is taking place and the source file provided is DFU-wrapped, this parameter will be ignored.
- l **SIZE** specifies the number of bytes to be uploaded when used in conjunction with -i or the number of bytes of flash to erase if used in conjunction with -c.
- i **NUM** specifies the zero-based index of the USB DFU device to access if more than one is currently attached to the system. If absent, the first device found is used.
- x specifies that destination file for an upload operation should be overwritten without prompting if it already exists.
- w specifies that the utility should wait for the user to press a key before it exits.
- v displays verbose output during the requested operation.

- h displays this help information.
- ? displays this help information.

Example:

The following example writes binary file program.bin to the device flash memory at address 0x1800:

```
dfuprog -f program.bin -a 0x1800
```

The following example writes DFU-wrapped file program.dfu to the flash memory of the second connected USB DFU device at the address found in the DFU file prefix:

```
dfuprog -i 1 -f program.dfu
```

The following example uploads (reads) the current application image into a DFU-formatted file appimage.dfu:

```
dfuprog -u -f appimage.dfu
```

USB DFU Wrapper

Usage:

```
dfuwrap [OPTION]...
```

Description:

Prepares binary images for download to a particular position in device flash via the USB device firmware upgrade protocol. A Stellaris-specific prefix and a DFU standard suffix are added to the binary.

The source code for this utility is contained in `tools/dfuwrap`, with a pre-built binary contained in `tools/bin`.

Arguments:

- a **ADDR** specifies the address of the binary.
- c specifies that the validity of the DFU wrapper on the input file should be checked.
- d **ID** specifies the USB device ID to place into the DFU wrapper. If not specified, the default of 0x0000 will be used.
- e enables verbose output.
- f specifies that a DFU wrapper should be added to the file even if one already exists.
- h displays usage information.
- i **FILE** specifies the name of the input file.
- o **FILE** specifies the name of the output file. If not specified, the default of image.dfu will be used.
- p **ID** specifies the USB product ID to place into the DFU wrapper. If not specified, the default of 0x00ff will be used.
- q specifies that only error information should be output.
- r specifies that the DFU header should be removed from the input file.
- v **ID** specifies the USB vendor ID to place into the DFU wrapper. If not specified, the default of 0x1cbe will be used.
- x specifies that the output file should be overwritten without prompting.

Example:

The following example adds a DFU wrapper which will cause the image to be programmed to address 0x1800:

```
dfuwrap -i program.bin -o program.dfu -a 0x1800
```

Ethernet Flash Downloader

Usage:

```
eflash [OPTION]... [INPUT FILE]
```

Description:

Downloads a firmware image to a Stellaris board using an Ethernet connection to the Stellaris Boot Loader. This has the same capabilities as the Ethernet download portion of the Stellaris Flash Programmer.

The source code for this utility is contained in `tools/eflash`, with a pre-built binary contained in `tools/bin`.

Arguments:

- help** displays usage information.
- h** is an alias for **--help**.
- ip=IP** specifies the IP address to be provided by the BOOTP server.
- i IP** is an alias for **--ip**.
- mac=MAC** specifies the MAC address
- m MAC** is an alias for **--mac**.
- quiet** specifies that only error information should be output.
- silent** is an alias for **--quiet**.
- verbose** specifies that verbose output should be output.
- version** displays the version of the utility and exits.
- INPUT FILE** specifies the name of the firmware image file.

Example:

The following will download a firmware image to the board over Ethernet, where the target board has a MAC address of 00:11:22:33:44:55 and is given an IP address of 169.254.19.70:

```
eflash -m 00:11:22:33:44:55 -i 169.254.19.70 image.bin
```

Finder

Usage:

```
finder
```

Description:

This program locates Stellaris boards on the local network that are running an lwIP-based application that includes the locator service. It will display the IP address, MAC address, client address, and application description for each board that it finds. This is useful for easily finding the IP address that has been assigned to a board via DHCP or AutoIP without needing to display it from the application (which is difficult on boards that do not have a builtin display).

The source code for this utility is contained in `tools/finder`, with a pre-built binary contained in `tools/bin`.

Example:

This utility can be run by clicking on the application in a filesystem browser or by invoking it from the command line as follows:

```
finder
```

FreeType Rasterizer

Usage:

```
ftrasterize [OPTION]... [INPUT FILE]
```

Description:

Uses the FreeType font rendering package to convert a font into the format that is recognized by the graphics library. Any font that is recognized by FreeType can be used, which includes TrueType®, OpenType®, PostScript® Type 1, and Windows® FNT fonts. A complete list of supported font formats can be found on the FreeType web site at <http://www.freetype.org>.

FreeType is used to render the glyphs of a font at a specific size in monochrome, using the result as the bitmap images for the font. These bitmaps are compressed and the results are written as a C source file that provides a tFont structure describing the font.

The source code for this utility is contained in `tools/ftrasterize`, with a pre-built binary contained in `tools/bin`.

Arguments:

- b** specifies that this is a bold font. This does not affect the rendering of the font, it only changes the name of the file and the name of the font structure that are produced.
 - f FILENAME** specifies the base name for this font, which is used to create the output file name and the name of the font structure. The default value is "font" if not specified.
 - i** specifies that this is an italic font. This does not affect the rendering of the font, it only changes the name of the file and the name of the font structure that are produced.
 - m** specifies that this is a monospaced font. This causes the glyphs to be horizontally centered in a box whose width is the width of the widest glyph. For best visual results, this option should only be used for font faces that are designed to be monospaced (such as Computer Modern TeleType).
 - s SIZE** specifies the size of this font, in points. The default value is 20 if not specified.
- INPUT FILE** specifies the name of the input font file.

Example:

The following example produces a 24-point font called test from test.ttf:

```
ftrasterize -f test -s 24 test.ttf
```

The result will be written to `fonttest24.c`, and will contain a structure called `g_sFontTest24` that describes the font.

USB DFU Library

Description:

LMDFU is a Windows dynamic link library offering a high level interface to the USB Device Firmware Upgrade functionality provided by the Stellaris USB boot loader (`boot_usb`). This DLL is used by the `dfuprog` utility and also by the LMFlash application to allow download and upload of application images to or from a Stellaris-based board via USB.

The source code for this DLL is contained in `tools/lmdfu`. The DLL binary is installed as part of the “Stellaris embedded USB drivers” package (SW-USB-windrivers) shipped on the release CD and downloadable from http://www.ti.com/software_updates.html. A Microsoft Visual Studio 2008 project file is provided to allow the application to be built.

GIMP Script For Texas Instruments Stellaris Button

Description:

This is a script-fu plugin for GIMP (<http://www.gimp.org>) that produces push button images that can be used by the push button widget. When installed into `${HOME}/.gimp-2.4/scripts`, this will be available under Xtns->Buttons->LMI Button. When run, a dialog will be displayed allowing the width and height of the button, the radius of the corners, the thickness of the 3D effect, the color of the button, and the pressed state of the button to be selected. Once the desired configuration is selected, pressing OK will create the push button image in a new GIMP image. The image should be saved as a raw PPM file so that it can be converted to a C array by `pnmtoc`.

This script is provided as a convenience to easily produce a particular push button appearance; the push button images can be of any desired appearance.

This script is located in `tools/lmi-button/lmi-button.scm`.

USB Dynamic Link Library

Description:

LMUSBDLL is a simple Windows dynamic link library offering low level packet read and write functions for some USB-connected Stellaris example applications. The DLL is written above the Microsoft WinUSB interface and is intended solely to ensure that various Windows-side example applications can be built without having to use WinUSB header files. These header files are not included in the Visual Studio tools and are only shipped in the Windows Device Driver Kit (DDK). By providing this simple mapping DLL which links to WinUSB, the user avoids the need for a multi-gigabyte download to build the examples.

The source code for this DLL is contained in `tools/lmusbdll`. The DLL binary is installed as part of the “Stellaris embedded USB drivers” package (SW-USB-windrivers) shipped on the release CD and downloadable from http://www.ti.com/software_updates.html. A Microsoft Visual Studio 2008 project file is provided to allow the DLL to be built on a PC which has the Windows Device Driver Kit installed.

Web Filesystem Generator

Usage:

```
makefsfile [OPTION]...
```

Description:

Generates a file system image for the lwIP web server. This is loosely based upon the `makefsdata` Perl script that is provided with lwIP, but does not require Perl and has several enhancements. The file system image is produced as a C source file that contains an image of all the files contained within a subtree of the development system's directory structure. This source file is then built into the application and served via HTTP by the lwIP web server.

By default, the file system image embeds the HTTP headers associated with each file in the file system image data itself. This is the default assumption of the lwIP web server implementation and is sensible if using an internal file system image containing a small number of files. If also serving files from a file system which does not embed the headers (for example the FAT file system on a microSD card) dynamic header generation must be used and internal file system images should be built using the `-h` option. In these cases, ensure that `DYNAMIC_HTTP_HEADERS` is also defined in the `lwipopts.h` file to correctly configure the web server.

The `-x` option allows an "exclude file" to be specified. This exclude file contains the names of files and directories within the input directory tree that are to be skipped in the conversion process. If this option is not present, a default set of file excludes is used. This list contains typical source code control metadata directory names (".svn" and "CVS") and system files such as "thumbs.db". To see the default exclude list, run the tool with the `-v` option and look in the output.

Each file or directory name in the exclude file must be on a separate line within the file. The exclude list must contain individual file or directory names and may not include partial paths. For example `images_old` or `.svn` would be acceptable but `images_old/.svn` would not.

In addition to generating multi-file images, the tool can also be used to dump a single file in the form of a C-style array of unsigned characters. This mode of operation is chosen using the `-f` command line option.

The source code for this utility is contained in `tools/makefsfile`, with a pre-built binary contained in `tools/bin`.

Arguments:

- b** generates a position-independent binary image.
- f** dumps a single file as a C-style hex character array.
- h** excludes HTTP headers from files. By default, HTTP headers are added to each file in the output.
- i NAME** specifies the name of the directory containing the files to be included in the image or the name of the single file to be dumped if `-f` is used.
- o FILE** specifies the name of the output file. If not specified, the default of `fsdata.c` will be used.
- q** enables quiet mode.
- r** overwrites the the output file without prompting.
- v** enables verbose output.
- x FILE** specifies a file containing a list of filenames and directory names to be excluded from the generated image.
- ?** displays usage information.

Example:

The following will generate a file system image using all the files in the `html` directory and place the results into `fsdata.h`:

```
makefsfile -i html -o fsdata.h
```

String Table Generator

Usage:

```
mkstringtable [INPUT FILE] [OUTPUT FILE]
```

Description:

Converts a comma separated file (.csv) to a table of strings that can be used by the Stellaris Graphics Library. The source .csv file has a simple fixed format that supports multiple strings in multiple languages. A .c and .h file will be created that can be compiled in with an application and used with the graphics library's string table handling functions. The strings will also be compressed in order to reduce the space required to store them.

The format of the input .csv file is simple and easily edited in any plain text editor or a spreadsheet editor capable of reading and editing a .csv file. The .csv file format has a header row where the first entry in the row can be any string as it is ignored. The remaining entries in the row must be one of the GrLang* language definitions defined by the graphics library in `grlib.h` or they must have a `#define` definition that is valid for the application as this text is used directly in the C output file that is produced. Adding additional languages only requires that the value is unique in the table and that the name used is defined by the application.

The strings are specified one per line in the .csv file. The first entry in any line is the value that is used as the actual text for the definition for the given string. The remaining entries should be the strings for each language specified in the header. Single words with no special characters do not require quotations, however any strings with a "," character must be quoted as the "\"" character is the delimiter for each item in the line. If the string has a quote character "\"" it must be preceded by another quote character.

The following is an example .csv file containing string in English (US), German, Spanish (SP), and Italian:

```
LanguageIDs,GrLangEnUS,GrLangDE,GrLangEsSP,GrLangIt
STR_CONFIG,Configuration,Konfigurieren,Configuracion,Configurazione
STR_INTRO,Introduction,Einfuhrung,Introduccion,Introduzione
STR_QUOTE,Introduction in "English","Einfuhrung, in Deutch",Prueba,Verifica
...
```

In this example, `STR_QUOTE` would result in the following strings in the various languages:

- `GrLangEnUs` – Introduction in "English"
- `GrLangDE` – Einfuhrung, in Deutch
- `GrLangEsSP` – Prueba
- `GrLangIt` – Verifica

The resulting .c file contains the string table that must be included with the application that is using the string table. While the contents of this .c file are readable, the string table itself may be unintelligible due to the compression used on the strings themselves. The .h file that is created has the definition for the string table as well as an enumerated type `enum SCOMP_STR_INDEX` that contains all of the string indexes that were present in the original .csv file.

The code that uses the string table produced by this utility must refer to the strings by their identifier in the original .csv file. In the example above, this means that the value `STR_CONFIG` would refer to the “Configuration” string in English (GrLangEnUS) or “Konfigurieren” in German (GrLangDE).

This utility is contained in `tools/bin`.

Arguments:

INPUT FILE specifies the input .csv file to use to create a string table.

OUTPUT FILE specifies the root name of the output files as `<OUTPUT FILE>.c` and `<OUTPUT FILE>.h`. The value is also used in the naming of the string table variable.

Example:

The following will create a string table in `str.c`, with prototypes in `str.h`, based on the input file `str.csv`:

```
mkstringtable str.csv str
```

In the produced `str.c`, there will be a string table in `g_pucTablestr`.

NetPNM Converter

Usage:

```
pnmtoc [OPTION]... [INPUT FILE]
```

Description:

Converts a NetPBM image file into the format that is recognized by the Stellaris Graphics Library. The input image must be in the raw PPM format (in other words, with the `P6` tag). The NetPBM image format can be produced using GIMP, NetPBM (<http://netpbm.sourceforge.net>), ImageMagick (<http://www.imagemagick.org>), or numerous other open source and proprietary image manipulation packages.

The resulting C image array definition is written to standard output; this follows the convention of the NetPBM toolkit after which the application was modeled (both in behavior and naming). The output should be redirected into a file so that it can then be used by the application.

To take a JPEG and convert it for use by the graphics library (using GIMP; a similar technique would be used in other graphics programs):

1. Load the file (File->Open).
2. Convert the image to indexed mode (Image->Mode->Indexed). Select “Generate optimum palette” and select either 2, 16, or 256 as the maximum number of colors (for a 1 BPP, 4 BPP, or 8 BPP image respectively). If the image is already in indexed mode, it can be converted to RGB mode (Image->Mode->RGB) and then back to indexed mode.
3. Save the file as a PNM image (File->Save As). Select raw format when prompted.
4. Use `pnmtoc` to convert the PNM image into a C array.

This sequence will be the same for any source image type (GIF, BMP, TIFF, and so on); once loaded into GIMP, it will treat all image types equally. For some source images, such as a GIF which is naturally an indexed format with 256 colors, the second step could be skipped if an 8 BPP image is desired in the application.

The source code for this utility is contained in `tools/pnmtoc`, with a pre-built binary contained in `tools/bin`.

Arguments:

- c** specifies that the image should be compressed. Compression is bypassed if it would result in a larger C array.

Example:

The following will produce a compressed image in `foo.c` from `foo.ppm`:

```
pnmtoc -c foo.ppm > foo.c
```

This will result in an array called `g_pucImage` that contains the image data from `foo.ppm`.

Serial Flash Downloader

Usage:

```
sflash [OPTION]... [INPUT FILE]
```

Description:

Downloads a firmware image to a Stellaris board using a UART connection to the Stellaris Serial Flash Loader or the Stellaris Boot Loader. This has the same capabilities as the serial download portion of the Stellaris Flash Programmer.

The source code for this utility is contained in `tools/sflash`, with a pre-built binary contained in `tools/bin`.

Arguments:

- b BAUD** specifies the baud rate. If not specified, the default of 115,200 will be used.
- c PORT** specifies the COM port. If not specified, the default of COM1 will be used.
- d** disables auto-baud.
- h** displays usage information.
- l FILENAME** specifies the name of the boot loader image file.
- p ADDR** specifies the address at which to program the firmware. If not specified, the default of 0 will be used.
- r ADDR** specifies the address at which to start processor execution after the firmware has been downloaded. If not specified, the processor will be reset after the firmware has been downloaded.
- s SIZE** specifies the size of the data packets used to download the firmware data. This must be a multiple of four between 8 and 252, inclusive. If using the Serial Flash Loader, the maximum value that can be used is 76. If using the Boot Loader, the maximum value that can be used is dependent upon the configuration of the Boot Loader. If not specified, the default of 8 will be used.

INPUT FILE specifies the name of the firmware image file.

Example:

The following will download a firmware image to the board over COM2 without auto-baud support:

```
sflash -c 2 -d image.bin
```

USB Bulk Data Transfer Example

Description:

usb_bulk_example is a Windows command line application which communicates with the StellarisWare usb_dev_bulk example. The application finds the Stellaris device on the USB bus then, if found, prompts the user to enter strings which are sent to the application running on the Stellaris board. This application then inverts the case of the alphabetic characters in the string and returns the data back to the USB host where it is displayed.

The source code for this application is contained in `tools/usb_bulk_example`. The binary is installed as part of the “Windows-side examples for USB kits” package (SW-USB-win) shipped on the release CD and downloadable from http://www.luminarymicro.com/products/software_updates.html. A Microsoft Visual Studio project file is provided to allow the application to be built.

5 Display Driver

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5.1 Introduction

The display driver offers a standard interface to access display functions on the Kitronix 320x240 QVGA screen and is used by the Stellaris Graphics Library and widget manager. In addition to providing the `tDisplay` structure required by the graphics library, the display driver also provides an API for initializing the display.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `kitronix320x240x16_ssd2119_8bit.c` containing the source code and `kitronix320x240x16_ssd2119_8bit.h` containing the API definitions for use by applications.

5.2 API Functions

Functions

- void `Kitronix320x240x16_SSD2119Init` (void)

Variables

- const tDisplay `g_sKitronix320x240x16_SSD2119`

5.2.1 Function Documentation

5.2.1.1 Kitronix320x240x16_SSD2119Init

Initializes the display driver.

Prototype:

```
void  
Kitronix320x240x16_SSD2119Init(void)
```

Description:

This function initializes the SSD2119 display controller on the panel, preparing it to display data.

Returns:

None.

5.2.2 Variable Documentation

5.2.2.1 g_sKitronix320x240x16_SSD2119

Definition:

```
const tDisplay g_sKitronix320x240x16_SSD2119
```

Description:

The display structure that describes the driver for the Kitronix K350QVG-V1-F TFT panel with an SSD2119 controller.

5.3 Programming Example

The following example shows how to initialize the display and prepare to draw on it using the graphics library.

```
tContext sContext;

//
// Initialize the display.
//
Kitronix320x240x16_SSD2119Init();

//
// Turn the backlight on at full brightness.
//
Kitronix320x240x16_SSD2119BacklightOn(0xFF);

//
// Initialize a graphics library drawing context.
//
GrContextInit(&sContext, &g_sKitronix320x240x16_SSD2119);
```

6 External Flash Driver

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6.1 Introduction

The external flash driver offers functions to query, erase and program the AMD-compatible flash found on the Flash/SRAM/LCD daughter board.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `extflash.c` containing the source code and `extflash.h` containing the API definitions for use by applications.

6.2 API Functions

Functions

- tBoolean [ExtFlashBlockErase](#) (unsigned long ulAddress, tBoolean bSync)
- unsigned long [ExtFlashBlockSizeGet](#) (unsigned long ulAddress, unsigned long *pulBlockAddr)
- tBoolean [ExtFlashChipErase](#) (tBoolean bSync)
- unsigned long [ExtFlashChipSizeGet](#) (void)
- tBoolean [ExtFlashEraselsComplete](#) (unsigned long ulAddress, tBoolean *pbError)
- tBoolean [ExtFlashPresent](#) (void)
- unsigned long [ExtFlashWrite](#) (unsigned long ulAddress, unsigned long ulLength, unsigned char *pucSrc)

6.2.1 Function Documentation

6.2.1.1 ExtFlashBlockErase

Erases a single block of the flash device.

Prototype:

```
tBoolean  
ExtFlashBlockErase(unsigned long ulAddress,  
                   tBoolean bSync)
```

Parameters:

ulAddress is an address within the block to be erased.

bSync indicates whether to return immediately or poll until the erase operation has completed.

Description:

This function erases a single block of the flash device. The block erased is identified by parameter *ulAddress*. If this is not a block start address, the block containing *ulAddress* is erased.

Applications may call [ExtFlashBlockSizeGet\(\)](#) to determine the size and start address of the block containing a particular flash address.

If called with *bAsync* set to **false**, the function will poll until the erase operation completes and only return at this point. If, however, *bAsync* is **true**, the function will return immediately and the caller can determine when the erase operation completes by polling function [ExtFlashErasesComplete\(\)](#), passing it the same *ulAddress* parameter as was passed to this function.

It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

Note:

A block erase will typically take 0.8 seconds to complete and may take up to 6 seconds in the worst case.

Returns:

Returns **true** to indicate success or **false** to indicate that an error occurred.

6.2.1.2 ExtFlashBlockSizeGet

Returns the size of a given flash block.

Prototype:

```
unsigned long  
ExtFlashBlockSizeGet(unsigned long ulAddress,  
                     unsigned long *pulBlockAddr)
```

Parameters:

ulAddress is the flash address whose block information is being queried.

pulBlockAddr is storage for the returned block start address.

Description:

This function determines the start address and size of the flash block which contains the supplied address. The block information is determined by parsing the flash CFI query data. If an invalid address is passed, the block address and size are both returned as 0.

It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

Returns:

Returns the size of the flash block which contains *ulAddress* or 0 if *ulAddress* is invalid.

6.2.1.3 ExtFlashChipErase

Erases the entire flash device.

Prototype:

```
tBoolean  
ExtFlashChipErase(tBoolean bSync)
```

Parameters:

bSync indicates whether to return immediately or poll until the erase operation has completed.

Description:

This function erases the entire flash device. If called with *bAsync* set to **false**, the function will poll until the erase operation completes and only return at this point. If, however, *bAsync* is **true**, the function will return immediately and the caller can determine when the erase operation completes by polling function [ExtFlashEraseIsComplete\(\)](#), passing *EXT_FLASH_BASE* as the *ulAddress* parameter.

It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

Note:

A chip erase will typically take 80 seconds to complete and may take up to 400 seconds in the worst case.

Returns:

Returns **true** to indicate success or **false** to indicate that an error occurred.

6.2.1.4 ExtFlashChipSizeGet

Queries the total size of the attached flash device.

Prototype:

```
unsigned long  
ExtFlashChipSizeGet(void)
```

Description:

This function read the flash CFI query block to determine the total size of the device in bytes and returns this value to the caller. It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

Returns:

Returns the total size of the attached flash device in bytes.

6.2.1.5 ExtFlashEraseIsComplete

Determines whether the last erase operation has completed.

Prototype:

```
tBoolean  
ExtFlashEraseIsComplete(unsigned long ulAddress,  
                        tBoolean *pbError)
```

Parameters:

ulAddress is an address within the area of flash which was last erased using a call to [ExtFlashBlockErase\(\)](#) or any valid flash address if [ExtFlashChipErase\(\)](#) was last issued.

pbError is storage for the returned error indicator.

Description:

This function determines whether the last flash erase operation has completed. The address passed in parameter *ulAddress* must correspond to an address in the region which was last erased. When the operation completes, **true** is returned and the caller should check the value

of **pbError* to determine whether or not the operation was successful. If **pbError* is **true**, an error was reported by the device and the flash may not have been correctly erased. If **pbError* is **false**, the operation completed successfully.

It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

Returns:

Returns **true** if the erase operation completed or **false** if it is still ongoing.

6.2.1.6 ExtFlashPresent

Determines whether the external flash on the Flash/SRAM/LCD daughter board is accessible.

Prototype:

```
tBoolean  
ExtFlashPresent(void)
```

Description:

This function checks to ensure that the external flash is accessible by reading back the "QRY" tag at the beginning of the CFI query block. If this is read correctly, **true** is returned indicating that the flash is now accessible, otherwise **false** is returned. It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

On return, the flash device is in read array mode.

Returns:

Returns **true** if the flash is found and accessible or **false** if the flash device is not found.

6.2.1.7 ExtFlashWrite

Writes data to the flash device.

Prototype:

```
unsigned long  
ExtFlashWrite(unsigned long ulAddress,  
               unsigned long ulLength,  
               unsigned char *pucSrc)
```

Parameters:

ulAddress is the address that the data is to be written to.

ulLength is the number of bytes of data to write.

pucSrc points to the first byte of data to write.

Description:

This function writes data to the flash device. Callers must ensure that the area of flash being written has previously been erased or, at least, that writing the data will not require any bits which are already stored as 0s in the flash to revert to 1s. The function returns either when an error is detected or all data has been successfully written to the device.

It is assumed that the EPI configuration has previously been set correctly using a call to [PinoutSet\(\)](#).

Note:

Programming data may take as long as 200 microseconds per byte.

Returns:

Returns the number of bytes successfully written.

6.3 Programming Example

The following example shows how to check for the presence of the external flash, erase the first block and program some data to it.

```
tBoolean bRetcode;

//
// Make sure that the flash is accessible.
//
if(!ExtFlashPresent())
{
    //
    // Handle a fatal error here - the flash could not be accessed.
    //
}

//
// Erase the first block of the flash device. This call is synchronous
// and only returns once the erase operation has completed.
//
bRetcode = ExtFlashBlockErase(EXT_FLASH_BASE, true);

if(bRetcode)
{
    //
    // Now write some data to the first block of the flash. We assume that
    // the array g_pucData contains at least MY_DATA_SIZE bytes and that
    // this contains the data to be written. This call is also synchronous
    // and only returns once the write has completed.
    //
    bRetcode = ExtFlashWrite(EXT_FLASH_BASE, MY_DATA_SIZE, g_pucData);

    //
    // Did we write the data successfully?
    //
    if(!bRetcode)
    {
        //
        // An error occurred while trying to write the data. Handle this
        // here.
        //
    }
}
else
{
    //
    // Oops - we couldn't erase the flash block!. Handle the error
    // condition here.
    //
}
```


7 External RAM Driver

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7.1 Introduction

The external RAM driver offers a convenient API for initializing and using RAM attached to the External Peripheral Interface. Functions are provided to configure the EPI module and I/O pins, to initialize the SDRAM daughter board and also to initialize and manage external RAM as a heap, allocating and freeing blocks.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `extram.c` containing the source code and `extram.h` containing the API definitions for use by applications. Applications using it must also ensure that they include source file `bget.c` which can be found in the `third_party/bget` directory.

7.2 API Functions

Functions

- void * [ExtRAMAlloc](#) (unsigned long ulSize)
- void [ExtRAMFree](#) (void *pvBlock)
- tBoolean [ExtRAMHeapInit](#) (void)
- unsigned long [ExtRAMMaxFree](#) (unsigned long *pulTotalFree)
- tBoolean [SDRAMInit](#) (unsigned long ulEPIDivider, unsigned long ulConfig, unsigned long ulRefresh)

7.2.1 Function Documentation

7.2.1.1 ExtRAMAlloc

Allocates a block of memory from the SDRAM heap.

Prototype:

```
void *
ExtRAMAlloc(unsigned long ulSize)
```

Parameters:

ulSize is the size in bytes of the block of external RAM to allocate.

Description:

This function allocates a block of external RAM and returns its pointer. If insufficient space exists to fulfill the request, a NULL pointer is returned.

Returns:

Returns a non-zero pointer on success or NULL if it is not possible to allocate the required memory.

7.2.1.2 ExtRAMFree

Frees a block of memory in the SDRAM heap.

Prototype:

```
void  
ExtRAMFree(void *pvBlock)
```

Parameters:

pvBlock is the pointer to the block of SDRAM to free.

Description:

This function frees a block of memory that had previously been allocated using a call to [ExtRAMAlloc\(\)](#);

Returns:

None.

7.2.1.3 ExtRAMHeapInit

Initializes any daughter-board SRAM as the external RAM heap.

Prototype:

```
tBoolean  
ExtRAMHeapInit(void)
```

Description:

When an SRAM/Flash daughter board is installed, this function may be used to configure the memory manager to use the SRAM on this board rather than the SDRAM as its heap. This allows software to call the [ExtRAMAlloc\(\)](#) and [ExtRAMFree\(\)](#) functions to manage the SRAM on the daughter board.

Function [PinoutSet\(\)](#) must be called before this function.

Returns:

Returns **true** on success of **false** if no SRAM is found or any other error occurs.

7.2.1.4 ExtRAMMaxFree

Reports information on the current heap usage.

Prototype:

```
unsigned long  
ExtRAMMaxFree(unsigned long *pulTotalFree)
```

Parameters:

pulTotalFree points to storage which will be written with the total number of bytes unallocated in the heap.

Description:

This function reports the total amount of memory free in the SDRAM heap and the size of the largest available block. It is included in the build only if label `INCLUDE_BGET_STATS` is defined.

Returns:

Returns the size of the largest available free block in the SDRAM heap.

7.2.1.5 SDRAMInit

Initializes the SDRAM.

Prototype:

```
tBoolean
SDRAMInit(unsigned long ulEPIDivider,
           unsigned long ulConfig,
           unsigned long ulRefresh)
```

Parameters:

ulEPIDivider is the EPI clock divider to use.

ulConfig is the SDRAM interface configuration.

ulRefresh is the refresh count in core clocks (0-2047).

Description:

This function must be called prior to `SDRAMAlloc()` or `SDRAMFree()` and after `PinoutSet()`. It configures the Stellaris microcontroller EPI block for SDRAM access and initializes the SDRAM heap (if SDRAM is found). The parameter *ulConfig* is the logical OR of several sets of choices:

The processor core frequency must be specified with one of the following:

- **EPI_SDRAM_CORE_FREQ_0_15** - core clock is 0 MHz < clk ≤ 15 MHz
- **EPI_SDRAM_CORE_FREQ_15_30** - core clock is 15 MHz < clk ≤ 30 MHz
- **EPI_SDRAM_CORE_FREQ_30_50** - core clock is 30 MHz < clk ≤ 50 MHz
- **EPI_SDRAM_CORE_FREQ_50_100** - core clock is 50 MHz < clk ≤ 100 MHz

The low power mode is specified with one of the following:

- **EPI_SDRAM_LOW_POWER** - enter low power, self-refresh state
- **EPI_SDRAM_FULL_POWER** - normal operating state

The SDRAM device size is specified with one of the following:

- **EPI_SDRAM_SIZE_64MBIT** - 64 Mbit device (8 MB)
- **EPI_SDRAM_SIZE_128MBIT** - 128 Mbit device (16 MB)
- **EPI_SDRAM_SIZE_256MBIT** - 256 Mbit device (32 MB)
- **EPI_SDRAM_SIZE_512MBIT** - 512 Mbit device (64 MB)

The parameter *ulRefresh* sets the refresh counter in units of core clock ticks. It is an 11-bit value with a range of 0 - 2047 counts.

Returns:

Returns **true** on success of **false** if no SDRAM is found or any other error occurs.

7.3 Programming Example

The following example shows how to initialize SDRAM and allocate a block for application use.

```
tBoolean bRetcode;
unsigned char *pucBlock;

//
// Initialize the SDRAM. This assumes that our system clock is running at
// somewhere between 50MHz and 100MHz and that the board is populated with
// a 64Mb SDRAM device. It sets the EPI clock divider to 1 (which divides
// the system clock by 2) and refreshes every 1024 cycles.
//
bRetcode = SDRAMInit(1, (EPI_SDRAM_CORE_FREQ_50_100 |
                        EPI_SDRAM_FULL_POWER | EPI_SDRAM_SIZE_64MBIT), 1024);

//
// Did we initialize the SDRAM correctly?
//
if(!bRetcode)
{
    //
    // Handle a fatal error here - the SDRAM could not be initialized.
    //
}

//
// Allocate an SDRAM buffer.
//
pucBlock = ExtrAMAlloc(MY_BLOCK_SIZE);

if(pucBlock)
{
    //
    // Go ahead and use the memory allocation.
    //

    ...

    //
    // Free the block once we are finished with it.
    //
    ExtrAMFree(pucBlock);
}
else
{
    //
    // Oops - we couldn't allocate memory. Handle the error condition here.
    //
}
```


8 JPEG Display Widget

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8.1 Introduction

The `jpgwidget` module contains a custom control widget for use with the Stellaris Graphics Library. This widget supports two modes of operation, both of which display a single JPEG image within the bounds of the control.

A “JPEGButton” widget offers simple pushbutton functionality with a callback function provided by the client being called to indicate that the button has either been pressed or released. This type of control centers the provided JPEG image within the area of the widget, clipping the image if necessary to fit the available area.

A “JPEGCanvas” widget is intended for image display. It does not provide an `OnClick` callback but does offer image scrolling function using the touchscreen input to control positioning of the image within the widget area. An `OnScroll` callback is provided which will be called whenever the position of the image is changed by the user. An application may use this to pace the redraw rate for the control or, alternatively, set the widget to redraw automatically on any scroll input.

To use this widget, an application must also include the source for the JPEG decoder which can be found in `third_party/jpeg` along with the SDRAM driver from `boards/dk-lm3s9b96/drivers` and the `bget` heap manager from `third_party/bget`. The application makefile or project must also ensure that label `INCLUDE_BGET_STATS` is defined to enable optional heap manager functionality that is required by the JPEG decoder.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `jpgwidget.c` containing the source code and `jpgwidget.h` containing the API definitions for use by applications.

8.2 API Functions

Data Structures

- `tJPEGInst`
- `tJPEGWidget`

Defines

- `JPEGButton`(`sName`, `pParent`, `pNext`, `pChild`, `pDisplay`, `IX`, `IY`, `IWidth`, `IHeight`, `ulStyle`, `ulFillColor`, `ulOutlineColor`, `ulTextColor`, `pFont`, `pcText`, `puclImage`, `ullmgLen`, `ucBorderWidth`, `pfnOnClick`, `psInst`)
- `JPEGCanvas`(`sName`, `pParent`, `pNext`, `pChild`, `pDisplay`, `IX`, `IY`, `IWidth`, `IHeight`, `ulStyle`, `ulFillColor`, `ulOutlineColor`, `ulTextColor`, `pFont`, `pcText`, `puclImage`, `ullmgLen`, `ucBorderWidth`, `pfnOnScroll`, `psInst`)

- [JPEGWidgetClickCallbackSet](#)(pWidget, pfnOnClick)
- [JPEGWidgetFillColorSet](#)(pWidget, ulColor)
- [JPEGWidgetFillOff](#)(pWidget)
- [JPEGWidgetFillOn](#)(pWidget)
- [JPEGWidgetFontSet](#)(pWidget, pFnt)
- [JPEGWidgetImageOff](#)(pWidget)
- [JPEGWidgetLock](#)(pWidget)
- [JPEGWidgetOutlineColorSet](#)(pWidget, ulColor)
- [JPEGWidgetOutlineOff](#)(pWidget)
- [JPEGWidgetOutlineOn](#)(pWidget)
- [JPEGWidgetOutlineWidthSet](#)(pWidget, ucWidth)
- [JPEGWidgetScrollCallbackSet](#)(pWidget, pfnOnScroll)
- [JPEGWidgetStruct](#)(pParent, pNext, pChild, pDisplay, IX, IY, IWidth, IHeight, ulStyle, ulFillColor, ulOutlineColor, ulTextColor, pFont, pcText, puclmage, ullmgLen, ucBorderWidth, pfnOnClick, pfnOnScroll, psInst)
- [JPEGWidgetTextColorSet](#)(pWidget, ulColor)
- [JPEGWidgetTextOff](#)(pWidget)
- [JPEGWidgetTextOn](#)(pWidget)
- [JPEGWidgetTextSet](#)(pWidget, pcTtxt)
- [JPEGWidgetUnlock](#)(pWidget)
- [JW_STYLE_BUTTON](#)
- [JW_STYLE_FILL](#)
- [JW_STYLE_LOCKED](#)
- [JW_STYLE_OUTLINE](#)
- [JW_STYLE_PRESSED](#)
- [JW_STYLE_RELEASE_NOTIFY](#)
- [JW_STYLE_SCROLL](#)
- [JW_STYLE_TEXT](#)

Functions

- long [JPEGWidgetImageDecompress](#) (tWidget *pWidget)
- void [JPEGWidgetImageDiscard](#) (tWidget *pWidget)
- long [JPEGWidgetImageSet](#) (tWidget *pWidget, const unsigned char *plmg, unsigned long ullmgLen)
- void [JPEGWidgetInit](#) (tJPEGWidget *pWidget, const tDisplay *pDisplay, long IX, long IY, long IWidth, long IHeight)
- long [JPEGWidgetMsgProc](#) (tWidget *pWidget, unsigned long ulMsg, unsigned long ulParam1, unsigned long ulParam2)

8.2.1 Data Structure Documentation

8.2.1.1 tJPEGInst

Definition:

```
typedef struct
{
```

```
    unsigned short usWidth;
    unsigned short usHeight;
    short sXOffset;
    short sYOffset;
    short sXStart;
    short sYStart;
    unsigned short *pusImage;
}
tJPEGInst
```

Members:

usWidth The width of the decompressed JPEG image in pixels.

usHeight The height of the decompressed JPEG image in lines.

sXOffset The current X image display offset (pan).

sYOffset The current Y image display offset (scan).

sXStart The x coordinate of the screen position corresponding to the last scrolling calculation check for a JPEGCanvas type widget.

sYStart The y coordinate of the screen position corresponding to the last scrolling calculation check for a JPEGCanvas type widget.

pusImage A pointer to the SDRAM buffer containing the decompressed JPEG image.

Description:

The structure containing workspace fields used by the JPEG widget in decompressing and displaying the JPEG image. This structure must not be modified by the application using the widget.

8.2.1.2 tJPEGWidget

Definition:

```
typedef struct
{
    tWidget sBase;
    unsigned long ulStyle;
    unsigned long ulFillColor;
    unsigned long ulOutlineColor;
    unsigned long ulTextColor;
    const tFont *pFont;
    const char *pcText;
    const unsigned char *pucImage;
    unsigned long ulImageLen;
    unsigned char ucBorderWidth;
    void (*pfnOnClick)(tWidget *pWidget);
    void (*pfnOnScroll)(tWidget *pWidget,
                        short sX,
                        short sY);
    tJPEGInst *psJPEGInst;
}
tJPEGWidget
```

Members:

sBase The generic widget information.

ulStyle The style for this widget. This is a set of flags defined by JW_STYLE_xxx.

ulFillColor The 24-bit RGB color used to fill this JPEG widget, if JW_STYLE_FILL is selected.

ulOutlineColor The 24-bit RGB color used to outline this JPEG widget, if JW_STYLE_OUTLINE is selected.

ulTextColor The 24-bit RGB color used to draw text on this JPEG widget, if JW_STYLE_TEXT is selected.

pFont A pointer to the font used to render the JPEG widget text, if JW_STYLE_TEXT is selected.

pcText A pointer to the text to draw on this JPEG widget, if JW_STYLE_TEXT is selected.

pucImage A pointer to the compressed JPEG image to be drawn onto this widget. If NULL, the widget will be filled with the provided background color if painted.

ulImageLen The number of bytes of compressed data in the image pointed to by pucImage.

ucBorderWidth The width of the border to be drawn around the widget. This is ignored if JW_STYLE_OUTLINE is not set.

pfnOnClick A pointer to the function to be called when the button is pressed. This is ignored if JW_STYLE_BUTTON is not set.

pfnOnScroll A pointer to the function to be called if the user scrolls the displayed image. This is ignored if JW_STYLE_BUTTON is set.

psJPEGInst The following structure contains all the workspace fields required by the widget. The client must initialize this with a valid pointer to a read/write structure.

Description:

The structure that describes a JPEG widget.

8.2.2 Define Documentation

8.2.2.1 JPEGButton

Declares an initialized variable containing a JPEG button data structure.

Definition:

```
#define JPEGButton(sName,  
                  pParent,  
                  pNext,  
                  pChild,  
                  pDisplay,  
                  lX,  
                  lY,  
                  lWidth,  
                  lHeight,  
                  ulStyle,  
                  ulFillColor,  
                  ulOutlineColor,  
                  ulTextColor,  
                  pFont,  
                  pcText,  
                  pucImage,  
                  ulImgLen,  
                  ucBorderWidth,
```

```
pfnOnClick,  
psInst)
```

Parameters:

- sName** is the name of the variable to be declared.
- pParent** is a pointer to the parent widget.
- pNext** is a pointer to the sibling widget.
- pChild** is a pointer to the first child widget.
- pDisplay** is a pointer to the display on which to draw the push button.
- IX** is the X coordinate of the upper left corner of the JPEG widget.
- IY** is the Y coordinate of the upper left corner of the JPEG widget.
- IWidth** is the width of the JPEG widget.
- IHeight** is the height of the JPEG widget.
- ulStyle** is the style to be applied to the JPEG widget.
- ulFillColor** is the color used to fill in the JPEG widget.
- ulOutlineColor** is the color used to outline the JPEG widget.
- ulTextColor** is the color used to draw text on the JPEG widget.
- pFont** is a pointer to the font to be used to draw text on the push button.
- pcText** is a pointer to the text to draw on this JPEG widget.
- puclmage** is a pointer to the compressed image to draw on this JPEG widget.
- ullmgLen** is the length of the data pointed to by puclmage.
- ucBorderWidth** is the width of the border to paint if `JW_STYLE_OUTLINE` is specified.
- pfnOnClick** is a pointer to the function that is called when the JPEG button is pressed or released.
- psInst** is a pointer to a read/write `tJPEGInst` structure that the widget can use for workspace.

Description:

This macro provides an initialized JPEG button widget data structure, which can be used to construct the widget tree at compile time in global variables (as opposed to run-time via function calls).

A JPEG button displays an image centered within the widget area and sends `OnClick` messages to the client whenever a user presses or releases the touchscreen within the widget area (depending upon the state of the `JW_STYLE_RELEASE_NOTIFY` style flag). A JPEG button does not support image scrolling.

ulStyle is the logical OR of the following:

- **JW_STYLE_OUTLINE** to indicate that the JPEG widget should be outlined.
- **JW_STYLE_TEXT** to indicate that the JPEG widget should have text drawn on it (using **pFont** and **pcText**).
- **JW_STYLE_FILL** to indicate that the JPEG widget should have its background filled with color (specified by **ulFillColor**).
- **JW_STYLE_SCROLL** to indicate that the JPEG widget should be redrawn automatically each time the pointer is moved (touchscreen is dragged) rather than waiting for the gesture to end then redrawing once. A client may chose to omit this style flag and call `WidgetPaint()` from within the `pfnOnScroll` callback at a rate deemed acceptable for the application.
- **JW_STYLE_LOCKED** to indicate that the JPEG widget should ignore all user input and merely display the image. If this flag is set, **JW_STYLE_SCROLL** is ignored.
- **JW_STYLE_RELEASE_NOTIFY** to indicate that the callback should be made when the button-style widget is released. If absent, the callback is called when the widget is initially pressed. This style flag is ignored unless **JW_STYLE_BUTTON** is specified.

Returns:

Nothing; this is not a function.

8.2.2.2 JPEGCanvas

Declares an initialized variable containing a JPEG canvas data structure.

Definition:

```
#define JPEGCanvas(sName,  
                  pParent,  
                  pNext,  
                  pChild,  
                  pDisplay,  
                  lX,  
                  lY,  
                  lWidth,  
                  lHeight,  
                  ulStyle,  
                  ulFillColor,  
                  ulOutlineColor,  
                  ulTextColor,  
                  pFont,  
                  pcText,  
                  pucImage,  
                  ulImgLen,  
                  ucBorderWidth,  
                  pfnOnScroll,  
                  psInst)
```

Parameters:

sName is the name of the variable to be declared.

pParent is a pointer to the parent widget.

pNext is a pointer to the sibling widget.

pChild is a pointer to the first child widget.

pDisplay is a pointer to the display on which to draw the push button.

lX is the X coordinate of the upper left corner of the JPEG widget.

lY is the Y coordinate of the upper left corner of the JPEG widget.

lWidth is the width of the JPEG widget.

lHeight is the height of the JPEG widget.

ulStyle is the style to be applied to the JPEG widget.

ulFillColor is the color used to fill in the JPEG widget.

ulOutlineColor is the color used to outline the JPEG widget.

ulTextColor is the color used to draw text on the JPEG widget.

pFont is a pointer to the font to be used to draw text on the push button.

pcText is a pointer to the text to draw on this JPEG widget.

pucImage is a pointer to the compressed image to draw on this JPEG widget.

ulImgLen is the length of the data pointed to by pucImage.

ucBorderWidth is the width of the border to paint if JW_STYLE_OUTLINE is specified.

pfnOnScroll is a pointer to the function that is called when the user drags a finger or stylus across the widget area. The values reported as parameters to the callback indicate the number of pixels of offset from center that will be applied to the image next time it is redrawn.

psInst is a pointer to a read/write [tJPEGInst](#) structure that the widget can use for workspace.

Description:

This macro provides an initialized JPEG canvas widget data structure, which can be used to construct the widget tree at compile time in global variables (as opposed to run-time via function calls).

A JPEG canvas widget acts as an image display surface. User input via the touch screen controls the image positioning, allowing scrolling of a large image within a smaller area of the display. Image redraw can either be carried out automatically whenever scrolling is required or can be delegated to the application via the OnScroll callback which is called whenever the user requests an image position change.

ulStyle is the logical OR of the following:

- **[JW_STYLE_OUTLINE](#)** to indicate that the JPEG widget should be outlined.
- **[JW_STYLE_TEXT](#)** to indicate that the JPEG widget should have text drawn on it (using *pFont* and *pcText*).
- **[JW_STYLE_FILL](#)** to indicate that the JPEG widget should have its background filled with color (specified by *ulFillColor*).
- **[JW_STYLE_SCROLL](#)** to indicate that the JPEG widget should be redrawn automatically each time the pointer is moved (touchscreen is dragged) rather than waiting for the gesture to end then redrawing once. A client may chose to omit this style flag and call `WidgetPaint()` from within the *pfnOnScroll* callback at a rate deemed acceptable for the application.
- **[JW_STYLE_LOCKED](#)** to indicate that the JPEG widget should ignore all user input and merely display the image. If this flag is set, **[JW_STYLE_SCROLL](#)** is ignored.

Returns:

Nothing; this is not a function.

8.2.2.3 JPEGWidgetClickCallbackSet

Sets the function to call when the button-style widget is pressed.

Definition:

```
#define JPEGWidgetClickCallbackSet (pWidget,  
                                   pfnOnClick)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

pfnOnClick is a pointer to the function to call.

Description:

This function sets the function to be called when this widget is pressed (assuming **[JW_STYLE_BUTTON](#)** is set). The supplied function is called when the button is pressed if **[JW_STYLE_RELEASE_NOTIFY](#)** is clear or when the button is released if this style flag is set.

Returns:

None.

8.2.2.4 JPEGWidgetFillColorSet

Sets the fill color of a JPEG widget.

Definition:

```
#define JPEGWidgetFillColorSet (pWidget,  
                               ulColor)
```

Parameters:

pWidget is a pointer to the JPEG widget to be modified.

ulColor is the 24-bit RGB color to use to fill the JPEG widget.

Description:

This function changes the color used to fill the JPEG widget on the display. The display is not updated until the next paint request.

Returns:

None.

8.2.2.5 JPEGWidgetFillOff

Disables background color fill for JPEG widget.

Definition:

```
#define JPEGWidgetFillOff (pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function disables background color fill for a JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.6 JPEGWidgetFillOn

Enables background color fill for a JPEG widget.

Definition:

```
#define JPEGWidgetFillOn (pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function enables background color fill for JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.7 JPEGWidgetFontSet

Sets the font for a JPEG widget.

Definition:

```
#define JPEGWidgetFontSet (pWidget,  
                           pFnt)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

pFnt is a pointer to the font to use to draw text on the push button.

Description:

This function changes the font used to draw text on the JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.8 JPEGWidgetImageOff

Disables the image on a JPEG widget.

Definition:

```
#define JPEGWidgetImageOff (pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function disables the drawing of an image on a JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.9 JPEGWidgetLock

Locks a JPEG widget making it ignore pointer input.

Definition:

```
#define JPEGWidgetLock (pWidget)
```

Parameters:

pWidget is a pointer to the widget to modify.

Description:

This function locks a JPEG widget and makes it ignore all pointer input. When locked, a widget acts as a passive canvas.

Returns:

None.

8.2.2.10 JPEGWidgetOutlineColorSet

Sets the outline color of a JPEG widget.

Definition:

```
#define JPEGWidgetOutlineColorSet (pWidget,  
                                   ulColor)
```

Parameters:

pWidget is a pointer to the JPEG widget to be modified.
ulColor is the 24-bit RGB color to use to outline the widget.

Description:

This function changes the color used to outline the JPEG widget on the display. The display is not updated until the next paint request.

Returns:

None.

8.2.2.11 JPEGWidgetOutlineOff

Disables outlining of a JPEG widget.

Definition:

```
#define JPEGWidgetOutlineOff (pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function disables the outlining of a JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.12 JPEGWidgetOutlineOn

Enables outlining of a JPEG widget.

Definition:

```
#define JPEGWidgetOutlineOn (pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function enables the outlining of a JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.13 JPEGWidgetOutlineWidthSet

Sets the outline width of a JPEG widget.

Definition:

```
#define JPEGWidgetOutlineWidthSet (pWidget,  
                                   ucWidth)
```

Parameters:

pWidget is a pointer to the JPEG widget to be modified.

ucWidth This function changes the width of the border around the JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.14 #define JPEGWidgetScrollCallbackSet(pWidget, pfnOnScrl)

Sets the function to call when the JPEG image is scrolled.

Parameters:

pWidget is a pointer to the JPEG widget to modify.

pfnOnScrl is a pointer to the function to call.

Description:

This function sets the function to be called when this widget is scrolled by dragging a finger or stylus over the image area (assuming that **JW_STYLE_BUTTON** is clear).

Returns:

None.

8.2.2.15 JPEGWidgetStruct

Declares an initialized JPEG image widget data structure.

Definition:

```
#define JPEGWidgetStruct (pParent,  
                          pNext,  
                          pChild,  
                          pDisplay,  
                          lX,  
                          lY,  
                          lWidth,  
                          lHeight,  
                          ulStyle,  
                          ulFillColor,  
                          ulOutlineColor,  
                          ulTextColor,  
                          pFont,  
                          pcText,
```

```
pucImage,  
ulImgLen,  
ucBorderWidth,  
pfnOnClick,  
pfnOnScroll,  
psInst)
```

Parameters:

pParent is a pointer to the parent widget.
pNext is a pointer to the sibling widget.
pChild is a pointer to the first child widget.
pDisplay is a pointer to the display on which to draw the push button.
IX is the X coordinate of the upper left corner of the JPEG widget.
IY is the Y coordinate of the upper left corner of the JPEG widget.
IWidth is the width of the JPEG widget.
IHeight is the height of the JPEG widget.
ulStyle is the style to be applied to the JPEG widget.
ulFillColor is the color used to fill in the JPEG widget.
ulOutlineColor is the color used to outline the JPEG widget.
ulTextColor is the color used to draw text on the JPEG widget.
pFont is a pointer to the font to be used to draw text on the push button.
pcText is a pointer to the text to draw on this JPEG widget.
pucImage is a pointer to the compressed image to draw on this JPEG widget.
ulImgLen is the length of the data pointed to by pucImage.
ucBorderWidth is the width of the border to paint if **JW_STYLE_OUTLINE** is specified.
pfnOnClick is a pointer to the function that is called when the JPEG button is pressed assuming **JW_STYLE_BUTTON** is specified.
pfnOnScroll is a pointer to the function that is called when the image is scrolled assuming **JW_STYLE_BUTTON** is not specified.
psInst is a pointer to a read/write **tJPEGInst** structure that the widget can use for workspace.

Description:

This macro provides an initialized jpeg image widget data structure, which can be used to construct the widget tree at compile time in global variables (as opposed to run-time via function calls). This must be assigned to a variable, such as:

```
tJPEGWidget g_sImageButton = JPEGWidgetStruct(...);
```

Or, in an array of variables:

```
tJPEGWidget g_psImageButtons[] =  
{  
    JPEGWidgetStruct(...),  
    JPEGWidgetStruct(...)  
};
```

ulStyle is the logical OR of the following:

- **JW_STYLE_OUTLINE** to indicate that the JPEG widget should be outlined.

- **JW_STYLE_BUTTON** to indicate that the JPEG widget should act as a button and that calls should be made to `pfnOnClick` when it is pressed or released (depending upon the state of **JW_STYLE_RELEASE_NOTIFY**). If absent, the widget acts as a canvas which allows the image, if larger than the widget display area to be scrolled by dragging a finger on the touchscreen. In this case, the `pfnOnScroll` callback will be called when any scrolling is needed.
- **JW_STYLE_TEXT** to indicate that the JPEG widget should have text drawn on it (using `pFont` and `pcText`).
- **JW_STYLE_FILL** to indicate that the JPEG widget should have its background filled with color (specified by `ulFillColor`).
- **JW_STYLE_SCROLL** to indicate that the JPEG widget should be redrawn automatically each time the pointer is moved (touchscreen is dragged) rather than waiting for the gesture to end then redrawing once. A client may chose to omit this style flag and call `WidgetPaint()` from within the `pfnOnScroll` callback at a rate deemed acceptable for the application.
- **JW_STYLE_LOCKED** to indicate that the JPEG widget should ignore all user input and merely display the image. If this flag is set, **JW_STYLE_SCROLL** is ignored.
- **JW_STYLE_RELEASE_NOTIFY** to indicate that the callback should be made when the button-style widget is released. If absent, the callback is called when the widget is initially pressed. This style flag is ignored unless **JW_STYLE_BUTTON** is specified.

Returns:

Nothing; this is not a function.

8.2.2.16 JPEGWidgetTextColorSet

Sets the text color of a JPEG widget.

Definition:

```
#define JPEGWidgetTextColorSet (pWidget,  
                               ulColor)
```

Parameters:

pWidget is a pointer to the JPEG widget to be modified.

ulColor is the 24-bit RGB color to use to draw text on the push button.

Description:

This function changes the color used to draw text on the JPEG widget on the display. The display is not updated until the next paint request.

Returns:

None.

8.2.2.17 JPEGWidgetTextOff

Disables the text on a JPEG widget.

Definition:

```
#define JPEGWidgetTextOff (pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function disables the drawing of text on a JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.18 JPEGWidgetTextOn

Enables the text on a JPEG widget.

Definition:

```
#define JPEGWidgetTextOn(pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget to modify.

Description:

This function enables the drawing of text on a JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.19 JPEGWidgetTextSet

Changes the text drawn on a JPEG widget.

Definition:

```
#define JPEGWidgetTextSet(pWidget,  
                          pcTxt)
```

Parameters:

pWidget is a pointer to the JPEG widget to be modified.

pcTxt is a pointer to the text to draw onto the JPEG widget.

Description:

This function changes the text that is drawn onto the JPEG widget. The display is not updated until the next paint request.

Returns:

None.

8.2.2.20 JPEGWidgetUnlock

Unlocks a JPEG widget making it pay attention to pointer input.

Definition:

```
#define JPEGWidgetUnlock (pWidget)
```

Parameters:

pWidget is a pointer to the widget to modify.

Description:

This function unlocks a JPEG widget. When unlocked, a JPEG widget will respond to pointer input by scrolling or making press callbacks depending upon the style flags and the image it is currently displaying.

Returns:

None.

8.2.2.21 JW_STYLE_BUTTON

Definition:

```
#define JW_STYLE_BUTTON
```

Description:

This flag indicates that the widget should act as a button rather than as a display surface.

8.2.2.22 JW_STYLE_FILL

Definition:

```
#define JW_STYLE_FILL
```

Description:

This flag indicates that the JPEG widget's background area should be filled with color even when there is an image to display.

8.2.2.23 JW_STYLE_LOCKED

Definition:

```
#define JW_STYLE_LOCKED
```

Description:

This flag indicates that the JPEG widget should ignore all touchscreen activity.

8.2.2.24 JW_STYLE_OUTLINE

Definition:

```
#define JW_STYLE_OUTLINE
```

Description:

This flag indicates that the widget should be outlined.

8.2.2.25 JW_STYLE_PRESSED

Definition:

```
#define JW_STYLE_PRESSED
```

Description:

This flag indicates that the JPEG widget is pressed.

8.2.2.26 JW_STYLE_RELEASE_NOTIFY

Definition:

```
#define JW_STYLE_RELEASE_NOTIFY
```

Description:

This flag indicates that the JPEG widget callback should be made when the widget is released rather than when it is pressed. This style flag is ignored if JW_STYLE_BUTTON is not set.

8.2.2.27 JW_STYLE_SCROLL

Definition:

```
#define JW_STYLE_SCROLL
```

Description:

This flag indicates that the JPEG widget's image should be repainted as the user scrolls over it. This is CPU intensive but looks better than the alternative which only repaints the image when the user ends their touchscreen drag.

8.2.2.28 JW_STYLE_TEXT

Definition:

```
#define JW_STYLE_TEXT
```

Description:

This flag indicates that the JPEG widget should have text drawn on it.

8.2.3 Function Documentation

8.2.3.1 JPEGWidgetImageDecompress

Decompresses the image associated with a JPEG widget.

Prototype:

```
long  
JPEGWidgetImageDecompress(tWidget *pWidget)
```


Parameters:

pWidget is a pointer to the JPEG widget whose image is to be decompressed.

Description:

This function must be called by the client for any JPEG widget whose compressed data pointer is initialized using the JPEGCanvas, JPEGButton or JPEGWidgetStruct macros. It decompresses the image and readies it for display.

This function must NOT be used if the widget already holds a decompressed image (i.e. if this function has been called before or if a prior call has been made to [JPEGWidgetImageSet\(\)](#) without a later call to [JPEGWidgetImageDiscard\(\)](#)) since this will result in a serious memory leak.

The client is responsible for repainting the widget after this call is made.

Returns:

Returns 0 on success. Any other return code indicates failure.

8.2.3.2 JPEGWidgetImageDiscard

Frees any decompressed image held by the widget.

Prototype:

```
void
JPEGWidgetImageDiscard(tWidget *pWidget)
```

Parameters:

pWidget is a pointer to the JPEG widget whose image is to be discarded.

Description:

This function frees any decompressed image that is currently held by the widget and returns the memory it was occupying to the RAM heap. After this call, [JPEGWidgetImageDecompress\(\)](#) may be called to re-decompress the same image or [JPEGWidgetImageSet\(\)](#) can be called to have the widget decompress a new image.

Returns:

None.

8.2.3.3 JPEGWidgetImageSet

Pass a new compressed image to the widget.

Prototype:

```
long
JPEGWidgetImageSet(tWidget *pWidget,
                   const unsigned char *pImg,
                   unsigned long ulImgLen)
```

Parameters:

pWidget is a pointer to the JPEG widget whose image is to be set.

pImg is a pointer to the compressed JPEG data for the image.

ulImgLen is the number of bytes of data pointed to by pImg.

Description:

This function is used to change the image displayed by a JPEG widget. It is safe to call it when the widget is already displaying an image since it will free any existing image before decompressing the new one. The client is responsible for repainting the widget after this call is made.

Returns:

Returns 0 on success. Any other return code indicates failure.

8.2.3.4 JPEGWidgetInit

Initializes a JPEG widget.

Prototype:

```
void
JPEGWidgetInit (tJPEGWidget *pWidget,
                const tDisplay *pDisplay,
                long lX,
                long lY,
                long lWidth,
                long lHeight)
```

Parameters:

pWidget is a pointer to the JPEG widget to initialize.

pDisplay is a pointer to the display on which to draw the push button.

lX is the X coordinate of the upper left corner of the JPEG widget.

lY is the Y coordinate of the upper left corner of the JPEG widget.

lWidth is the width of the JPEG widget.

lHeight is the height of the JPEG widget.

Description:

This function initializes the provided JPEG widget. The widget position is set and all styles and parameters set to 0. The caller must make use of the various widget functions to set any required parameters after making this call.

Returns:

None.

8.2.3.5 JPEGWidgetMsgProc

Handles messages for a JPEG widget.

Prototype:

```
long
JPEGWidgetMsgProc (tWidget *pWidget,
                  unsigned long ulMsg,
                  unsigned long ulParam1,
                  unsigned long ulParam2)
```

Parameters:

pWidget is a pointer to the JPEG widget.

ulMsg is the message.

ulParam1 is the first parameter to the message.

ulParam2 is the second parameter to the message.

Description:

This function receives messages intended for this JPEG widget and processes them accordingly. The processing of the message varies based on the message in question.

Unrecognized messages are handled by calling WidgetDefaultMsgProc().

Returns:

Returns a value appropriate to the supplied message.

8.3 Programming Example

The following example shows how to initialize a JPEGCanvas widget. The sample application boards/dk-lm3s9b96/showjpeg provides a working example of the use of this widget.

```
...

// *****
//
// Forward references
//
// *****
extern tCanvasWidget g_sBackground;

// *****
//
// Workspace for the JPEG canvas widget.
//
// *****
tJPEGInst g_sJPEGInst;

// *****
//
// The JPEG canvas widget used to hold the decompressed JPEG image and
// display it. This is a simple JPEG canvas which will decompress and display
// the image contained in buffer g_pucJPEGImage in a 320x215 pixel control with
// a single pixel white outline. Style flag JW_STYLE_SCROLL enables automatic
// redraw based on user touchscreen scrolling. This is rather CPU intensive
// so it may be better to remove this style flag and install an OnScroll
// callback that can be used to pace the redraws. This is demonstrated in the
// showjpeg example application.
//
// *****
#define IMAGE_LEFT      0
#define IMAGE_TOP       25
#define IMAGE_WIDTH     320
#define IMAGE_HEIGHT    215
JPEGCanvas(g_sImage, &g_sBackground, 0, 0,
           &g_sKitronix320x240x16_SSD2119, IMAGE_LEFT, IMAGE_TOP, IMAGE_WIDTH,
           IMAGE_HEIGHT, (JW_STYLE_OUTLINE | JW_STYLE_SCROLL), ClrBlack,
           ClrWhite, 0, 0, 0, g_pucJPEGImage, sizeof(g_pucJPEGImage), 1, 0,
           &g_sJPEGInst);

...

int
```

```
main(void)
{
    tBoolean bRetcode;
    int iRetcode;

    //
    // Set the system clock to run at 50MHz from the PLL.
    //
    SysCtlClockSet(SYSCTL_SYSDIV_4 | SYSCTL_USE_PLL | SYSCTL_OSC_MAIN |
        SYSCTL_XTAL_16MHZ);

    //
    // Enable Interrupts
    //
    IntMasterEnable();

    //
    // Initialize the SDRAM controller and heap.
    //
    SDRAMInit(1, (EPI_SDRAM_CORE_FREQ_50_100 | EPI_SDRAM_FULL_POWER |
        EPI_SDRAM_SIZE_64MBIT), 1024);

    //
    // Initialize the display driver.
    //
    Kitronix320x240x16_SSD2119Init();

    //
    // Initialize the touch screen driver.
    //
    TouchScreenInit();

    //
    // Set the touch screen event handler.
    //
    TouchScreenCallbackSet(WidgetPointerMessage);

    //
    // Add the compile-time defined widgets to the widget tree.
    //
    WidgetAdd(WIDGET_ROOT, (tWidget *)&g_sBackground);

    //
    // Decompress the image we linked to the JPEG canvas widget
    //
    iRetcode = JPEGWidgetImageDecompress((tWidget *)&g_sImage);

    //
    // Was the decompression successful?
    //
    if(iRetcode != 0)
    {
        while(1)
        {
            //
            // Something went wrong during the decompression of the JPEG
            // image. Hang here pending investigation.
            //
        }
    }

    //
    // Issue the initial paint request to the widgets.
    //
    WidgetPaint(WIDGET_ROOT);
}
```

```
//  
// Enter an infinite loop for reading and processing touchscreen input  
// from the user.  
//  
while(1)  
{  
    WidgetMessageQueueProcess();  
}
```


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9.1 Introduction

The `set_pinout` driver offers a convenient API for configuring the device pinout appropriately for the target board. It encapsulates all GPIO Port Control register configuration into a single, common function that all applications running on a particular hardware platform can call to initialize the pinout.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `set_pinout.c` containing the source code and `set_pinout.h` containing the API definition for use by applications.

9.2 API Functions

Functions

- void `PinoutSet` (void)

9.2.1 Function Documentation

9.2.1.1 PinoutSet

Configures the LM3S9B96 device pinout for the development board.

Prototype:

```
void  
PinoutSet (void)
```

Description:

This function configures each pin of the lm3s9b96 device to route the appropriate peripheral signal as required by the design of the dk-lm3s9b96 development board.

Note:

This module can be built in two ways. If the label `SIMPLE_PINOUT_SET` is not defined, the `PinoutSet()` function will attempt to read an I2C EEPROM to determine which daughter board is attached to the development kit board and use information from that EEPROM to dynamically configure the EPI appropriately. In this case, if no EEPROM is found, the EPI configuration will default to that required to use the SDRAM daughter board which is included with the base development kit.

If `SIMPLE_PINOUT_SET` is defined, however, all the dynamic configuration code is replaced with a very simple function which merely sets the pinout and EPI configuration statically. This is a better

representation of how a real- world application would likely initialize the pinout and EPI timing and takes significantly less code space than the dynamic, daughter-board detecting version. The example offered here sets the pinout and EPI configuration appropriately for the Flash/SRAM/LCD daughter board.

Returns:

None.

10 Serial Flash Driver

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10.1 Introduction

This driver provides a low level interface allowing data to be written to and read from the SSI-connected 1MB serial flash device found on the board.

This device shares the SSI bus with the SDCard so care must be taken when using these two devices to ensure no contention since this driver does not contain code to arbitrate for ownership of the SSI bus.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `ssiflash.c` containing the source code and `ssiflash.h` containing the API definitions for use by applications.

10.2 API Functions

Functions

- tBoolean [SSIFlashBlockErase](#) (unsigned long ulAddress, tBoolean bSync)
- unsigned long [SSIFlashBlockSizeGet](#) (void)
- tBoolean [SSIFlashChipErase](#) (tBoolean bSync)
- unsigned long [SSIFlashChipSizeGet](#) (void)
- tBoolean [SSIFlashIDGet](#) (unsigned char *pucManufacturer, unsigned char *pucDevice)
- tBoolean [SSIFlashInit](#) (void)
- tBoolean [SSIFlashIsBusy](#) (void)
- unsigned long [SSIFlashRead](#) (unsigned long ulAddress, unsigned long ulLength, unsigned char *pucDst)
- tBoolean [SSIFlashSectorErase](#) (unsigned long ulAddress, tBoolean bSync)
- unsigned long [SSIFlashSectorSizeGet](#) (void)
- unsigned long [SSIFlashWrite](#) (unsigned long ulAddress, unsigned long ulLength, unsigned char *pucSrc)

10.2.1 Function Documentation

10.2.1.1 SSIFlashBlockErase

Erases the contents of a single serial flash block.

Prototype:

```
tBoolean
```

```
SSIFlashBlockErase(unsigned long ulAddress,  
                   tBoolean bSync)
```

Parameters:

ulAddress is the start address of the block which is to be erased. This value must be an integer multiple of the block size returned by [SSIFlashBlockSizeGet\(\)](#).

bSync should be set to **true** if the function is to block until the erase operation is complete or **false** to return immediately after the operation is started.

Description:

This function erases a single block of the serial flash, returning all bytes in that block to their erased value of 0xFF. The block size and, hence, start address granularity can be determined by calling [SSIFlashBlockSizeGet\(\)](#).

The function may be synchronous (*bSync* set to **true**) or asynchronous (*bSync* set to **false**). If asynchronous, the caller is responsible for ensuring that no further serial flash operations are requested until the erase operation has completed. The state of the device may be queried by calling [SSIFlashIsBusy\(\)](#).

Three options for erasing are provided. Sectors provide the smallest erase granularity, blocks provide the option of erasing a larger section of the device in one operation and, finally, the whole device may be erased in a single operation via [SSIFlashChipErase\(\)](#).

Note:

This operation will take between 400mS and 1000mS to complete. If the *bSync* parameter is set to **true**, this function will, therefore, not return for a significant period of time.

Returns:

Returns **true** on success or **false** on failure.

10.2.1.2 SSIFlashBlockSizeGet

Returns the size of a block for this device.

Prototype:

```
unsigned long  
SSIFlashBlockSizeGet(void)
```

Description:

This function returns the size of an erasable block for the serial flash device. All addresses passed to [SSIFlashBlockErase\(\)](#) must be aligned on a block boundary.

Returns:

Returns the number of bytes in a block.

10.2.1.3 SSIFlashChipErase

Erases the entire serial flash device.

Prototype:

```
tBoolean  
SSIFlashChipErase(tBoolean bSync)
```

Parameters:

bSync should be set to **true** if the function is to block until the erase operation is complete or **false** to return immediately after the operation is started.

Description:

This function erases the entire serial flash device, returning all bytes in the device to their erased value of 0xFF.

The function may be synchronous (***bSync*** set to **true**) or asynchronous (***bSync*** set to **false**). If asynchronous, the caller is responsible for ensuring that no further serial flash operations are requested until the erase operation has completed. The state of the device may be queried by calling [SSIFlashIsBusy\(\)](#).

Note:

This operation will take between 6 and 10 seconds to complete. If the ***bSync*** parameter is set to **true**, this function will, therefore, not return for a significant period of time.

Returns:

Returns **true** on success or **false** on failure.

10.2.1.4 SSIFlashChipSizeGet

Returns the total amount of storage offered by this device.

Prototype:

```
unsigned long  
SSIFlashChipSizeGet(void)
```

Description:

This function returns the size of the programmable area provided by the attached SSI flash device.

Returns:

Returns the number of bytes in the device.

10.2.1.5 SSIFlashIDGet

Returns the manufacturer and device IDs for the attached serial flash.

Prototype:

```
tBoolean  
SSIFlashIDGet(unsigned char *pucManufacturer,  
              unsigned char *pucDevice)
```

Parameters:

pucManufacturer points to storage which will be written with the manufacturer ID of the attached serial flash device.

pucDevice points to storage which will be written with the device ID of the attached serial flash device.

Description:

This function may be used to determine the manufacturer and device IDs of the attached serial flash device.

Returns:

Returns **true** on success or **false** on failure.

10.2.1.6 SSIFlashInit

Initializes the SSI port and determines if the serial flash is available.

Prototype:

```
tBoolean  
SSIFlashInit(void)
```

Description:

This function must be called prior to any other function offered by the serial flash driver. It configures the SSI port to run in mode 0 at 10MHz and queries the ID of the serial flash device to ensure that it is available.

Note:

SSI0 is shared between the serial flash and the SDCard on the dk-lm3s9b96 boards. Two independent GPIOs are used to provide chip selects for these two devices but care must be taken when using both in a single application, especially during initialization of the SDCard when the SSI clock rate must initially be set to 400KHz and later increased. Since both the SSI flash and SDCard drivers initialize the SSI peripheral, application writers must be aware of the possible contention and ensure that they do not allow the possibility of two different interrupt handlers or execution threads from attempting to access both peripherals simultaneously.

This driver assumes that the application is aware of the possibility of contention and has been designed with this in mind. Other than disabling the SDCard when an attempt is made to access the serial flash, no code is included here to arbitrate for ownership of the SSI peripheral.

Returns:

Returns **true** on success or **false** if an error is reported or the expected serial flash device is not present.

10.2.1.7 SSIFlashIsBusy

Determines if the serial flash is able to accept a new instruction.

Prototype:

```
tBoolean  
SSIFlashIsBusy(void)
```

Description:

This function reads the serial flash status register and determines whether or not the device is currently busy. No new instruction may be issued to the device if it is busy.

Returns:

Returns **true** if the device is busy and unable to receive a new instruction or **false** if it is idle.

10.2.1.8 SSIFlashRead

Reads a block of serial flash into a buffer.

Prototype:

```
unsigned long
SSIFlashRead(unsigned long ulAddress,
              unsigned long ulLength,
              unsigned char *pucDst)
```

Parameters:

ulAddress is the serial flash address of the first byte to be read.

ulLength is the number of bytes of data to read.

pucDst is a pointer to storage for the data read.

Description:

This function reads a contiguous block of data from a given address in the serial flash device into a buffer supplied by the caller.

Returns:

Returns the number of bytes read.

10.2.1.9 SSIFlashSectorErase

Erases the contents of a single serial flash sector.

Prototype:

```
tBoolean
SSIFlashSectorErase(unsigned long ulAddress,
                    tBoolean bSync)
```

Parameters:

ulAddress is the start address of the sector which is to be erased. This value must be an integer multiple of the sector size returned by [SSIFlashSectorSizeGet\(\)](#).

bSync should be set to **true** if the function is to block until the erase operation is complete or **false** to return immediately after the operation is started.

Description:

This function erases a single sector of the serial flash, returning all bytes in that sector to their erased value of 0xFF. The sector size and, hence, start address granularity can be determined by calling [SSIFlashSectorSizeGet\(\)](#).

The function may be synchronous (*bSync* set to **true**) or asynchronous (*bSync* set to **false**). If asynchronous, the caller is responsible for ensuring that no further serial flash operations are requested until the erase operation has completed. The state of the device may be queried by calling [SSIFlashIsBusy\(\)](#).

Three options for erasing are provided. Sectors provide the smallest erase granularity, blocks provide the option of erasing a larger section of the device in one operation and, finally, the whole device may be erased in a single operation via [SSIFlashChipErase\(\)](#).

Note:

This operation will take between 120mS and 250mS to complete. If the *bSync* parameter is set to **true**, this function will, therefore, not return for a significant period of time.

Returns:

Returns **true** on success or **false** on failure.

10.2.1.10 SSIFlashSectorSizeGet

Returns the size of a sector for this device.

Prototype:

```
unsigned long  
SSIFlashSectorSizeGet(void)
```

Description:

This function returns the size of an erasable sector for the serial flash device. All addresses passed to [SSIFlashSectorErase\(\)](#) must be aligned on a sector boundary.

Returns:

Returns the number of bytes in a sector.

10.2.1.11 SSIFlashWrite

Writes a block of data to the serial flash device.

Prototype:

```
unsigned long  
SSIFlashWrite(unsigned long ulAddress,  
               unsigned long ulLength,  
               unsigned char *pucSrc)
```

Parameters:

ulAddress is the first serial flash address to be written.

ulLength is the number of bytes of data to write.

pucSrc is a pointer to the data which is to be written.

Description:

This function writes a block of data into the serial flash device at the given address. It is assumed that the area to be written has previously been erased.

Returns:

Returns the number of bytes written.

10.3 Programming Example

The following example shows how to use the SSIFlash API.

```
int main(void)  
{  
    tBoolean bRetcode;  
    unsigned long ulCount;
```

```
...

//
// Initialize the serial flash device driver.
//
bRetcode = SSIFlashInit();
if(!bRetcode)
{
    //
    // An error occurred! Handle it here.
    //
    while(1);
}

//
// Erase the first block of the flash device. We call this function with
// the bSync parameter set to false so it will return immediately and
// not wait for the operation to complete.
//
bRetcode = SSIFlashBlockErase(0, false);
if(!bRetcode)
{
    //
    // An error occurred! Handle it here.
    //
    while(1);
}

//
// Wait for the erase operation to complete. In "real" code, you would,
// of course, not use an infinite loop here.
//
while(SSIFlashIsBusy());

//
// Write data to the newly erased page.
//
ulCount = SSIFlashWrite(0, DATA_LENGTH, g_pcBuffer1);
if(ulCount != DATA_LENGTH)
{
    //
    // An error occurred! Handle it here.
    //
    while(1);
}

//
// Read the data back into a different buffer.
//
ulCount = SSIFlashRead(0, DATA_LENGTH, g_pcBuffer2);
if(ulCount != DATA_LENGTH)
{
    //
    // An error occurred! Handle it here.
    //
    while(1);
}

...
}
```


11 Sound Driver

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11.1 Introduction

The sound driver allows applications to play and record PCM sample buffers and play simple tones using the internal I2S peripheral communicating with an external audio codec. Functions are provided to initialize the codec, set the output volume, set the audio format and play or record a buffer of PCM audio samples. There is also a method for creating simple songs or sound effects by specifying a sequence of frequencies and the times at which they should be output.

Applications wishing to use the sound driver must ensure that they set function [SoundIntHandler\(\)](#) in the interrupt table for the I2S vector.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `sound.c` containing the source code and `sound.h` containing the API definitions for use by applications.

11.2 API Functions

Functions

- unsigned long [SoundBufferPlay](#) (const void *pvData, unsigned long ulLength, tBufferCallback pfnCallback)
- unsigned long [SoundBufferRead](#) (void *pvData, unsigned long ulSize, tBufferCallback pfnCallback)
- void [SoundInit](#) (unsigned long ulEnableReceive)
- void [SoundIntHandler](#) (void)
- void [SoundPlay](#) (const unsigned short *pusSong, unsigned long ulLength)
- unsigned long [SoundSampleRateGet](#) (void)
- void [SoundSetFormat](#) (unsigned long ulSampleRate, unsigned short usBitsPerSample, unsigned short usChannels)
- void [SoundVolumeDown](#) (unsigned long ulPercent)
- unsigned char [SoundVolumeGet](#) (void)
- void [SoundVolumeSet](#) (unsigned long ulPercent)
- void [SoundVolumeUp](#) (unsigned long ulPercent)

11.2.1 Function Documentation

11.2.1.1 SoundBufferPlay

Starts playback of a block of PCM audio samples.

Prototype:

```
unsigned long  
SoundBufferPlay(const void *pvData,  
                unsigned long ulLength,  
                tBufferCallback pfnCallback)
```

Parameters:

pvData is a pointer to the audio data to play.

ulLength is the length of the data in bytes.

pfnCallback is a function to call when this buffer has be played.

Description:

This function starts the playback of a block of PCM audio samples. If playback of another buffer is currently ongoing, its playback is canceled and the buffer starts playing immediately.

Returns:

0 if the buffer was accepted, returns non-zero if there was no space available for this buffer.

11.2.1.2 SoundBufferRead

Starts recording from the audio input.

Prototype:

```
unsigned long  
SoundBufferRead(void *pvData,  
                unsigned long ulSize,  
                tBufferCallback pfnCallback)
```

Parameters:

pvData is a pointer to store the data as it is received.

ulSize is the size of the buffer pointed to by *pvData* in bytes.

pfnCallback is a function to call when this buffer has been filled.

Description:

This function initiates a request for the I2S controller to receive data from an I2S codec.

Returns:

0 if the buffer was accepted, returns non-zero if there was no space available for this buffer.

11.2.1.3 SoundInit

Initializes the sound output.

Prototype:

```
void  
SoundInit(unsigned long ulEnableReceive)
```

Parameters:

ulEnableReceive is set to 1 to enable the receive portion of the I2S controller and 0 to leave the I2S controller not configured.

Description:

This function prepares the sound driver to play songs or sound effects. It must be called before any other sound function. The sound driver uses uDMA with the I2S controller so the caller must ensure that the uDMA peripheral is enabled and its control table configured prior to making this call. The GPIO peripheral and pins used by the I2S interface are controlled by the I2S0_*_PERIPH, I2S0_*_PORT and I2S0_*_PIN definitions.

Returns:

None

11.2.1.4 SoundIntHandler

Handles the I2S sound interrupt.

Prototype:

```
void  
SoundIntHandler(void)
```

Description:

This function services the I2S interrupt and will call the callback function provided with the buffer that was given to the [SoundBufferPlay\(\)](#) or [SoundBufferRead\(\)](#) functions to handle emptying or filling the buffers and starting up DMA transfers again. It is solely the responsibility of the callback functions to continuing sending or receiving data to or from the audio codec.

Returns:

None.

11.2.1.5 SoundPlay

Starts playback of a song.

Prototype:

```
void  
SoundPlay(const unsigned short *pusSong,  
           unsigned long ulLength)
```

Parameters:

pusSong is a pointer to the song data structure.

ulLength is the length of the song data structure in bytes.

Description:

This function starts the playback of a song or sound effect. If a song or sound effect is already being played, its playback is canceled and the new song is started.

Returns:

None.

11.2.1.6 SoundSampleRateGet

Returns the current sample rate.

Prototype:

```
unsigned long  
SoundSampleRateGet(void)
```

Description:

This function returns the sample rate that was set by a call to [SoundSetFormat\(\)](#). This is needed to retrieve the exact sample rate that is in use in case the requested rate could not be matched exactly.

Returns:

The current sample rate in samples/second.

11.2.1.7 SoundSetFormat

Configures the I2S peripheral for the given audio data format.

Prototype:

```
void  
SoundSetFormat(unsigned long ulSampleRate,  
               unsigned short usBitsPerSample,  
               unsigned short usChannels)
```

Parameters:

ulSampleRate is the sample rate of the audio to be played in samples per second.

usBitsPerSample is the number of bits in each audio sample.

usChannels is the number of audio channels, 1 for mono, 2 for stereo.

Description:

This function configures the I2S peripheral in preparation for playing and recording audio data of a particular format.

Note:

This function has a work around for the I2SMCLKCFG register errata. This errata limits the low end of the MCLK at some bit sizes. The absolute limit is a divide of the System PLL by 256 or an MCLK minimum of 400MHz/256 or 1.5625MHz. This is overcome by increasing the number of bits shifted out per sample and thus increasing the MCLK needed for a given sample rate. This uses the fact that the I2S codec used on the LM3S9B96 development board s that will toss away extra bits that go to or from the codec.

Returns:

None.

11.2.1.8 SoundVolumeDown

Decreases the volume.

Prototype:

```
void  
SoundVolumeDown(unsigned long ulPercent)
```

Parameters:

ulPercent is the amount to decrease the volume, specified as a percentage between 0% (silence) and 100% (full volume), inclusive.

Description:

This function adjusts the audio output down by the specified percentage. The adjusted volume will not go below 0% (silence).

Returns:

None.

11.2.1.9 SoundVolumeGet

Returns the current volume level.

Prototype:

```
unsigned char  
SoundVolumeGet(void)
```

Description:

This function returns the current volume, specified as a percentage between 0% (silence) and 100% (full volume), inclusive.

Returns:

Returns the current volume.

11.2.1.10 SoundVolumeSet

Sets the volume of the music/sound effect playback.

Prototype:

```
void  
SoundVolumeSet(unsigned long ulPercent)
```

Parameters:

ulPercent is the volume percentage, which must be between 0% (silence) and 100% (full volume), inclusive.

Description:

This function sets the volume of the sound output to a value between silence (0%) and full volume (100%).

Returns:

None.

11.2.1.11 SoundVolumeUp

Increases the volume.

Prototype:

```
void  
SoundVolumeUp(unsigned long ulPercent)
```

Parameters:

ulPercent is the amount to increase the volume, specified as a percentage between 0% (silence) and 100% (full volume), inclusive.

Description:

This function adjusts the audio output up by the specified percentage. The adjusted volume will not go above 100% (full volume).

Returns:

None.

11.3 Programming Example

The following example shows how to play a short tone at 1000 Hz.

```
//  
// Initialize the sound output.  
//  
SoundInit(0);  
  
//  
// Set the sound output frequency to 1000 Hz.  
//  
SoundFrequencySet(1000);  
  
//  
// Enable the sound output.  
//  
SoundEnable();  
  
//  
// Delay for a while.  
//  
SysCtlDelay(10000);  
  
//  
// Disable the sound output.  
//  
SoundDisable();
```

12 Thumbwheel Driver

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12.1 Introduction

The board contains a thumbwheel potentiometer attached to one of the analog-to-digital converter input channels. This driver offers a simple way to configure the ADC to sample the potentiometer and to request that samples be captured.

An application wishing to make use of the thumbwheel initializes the driver and provides a callback function which will be used to notify it when samples have been captured. Another function is provided to allow sample capture to be triggered.

The potentiometer sample captured is read from the ADC, converted from a raw value to millivolts then passed to the calling application via its callback function.

This driver makes use of ADC sequence 2.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `thumbwheel.c` containing the source code and `thumbwheel.h` containing the API definitions for use by applications.

12.2 API Functions

Functions

- void [ThumbwheelCallbackSet](#) (void (*pfnCallback)(unsigned short usThumbwheelmV))
- void [ThumbwheelInit](#) (void)
- void [ThumbwheelTrigger](#) (void)

12.2.1 Function Documentation

12.2.1.1 ThumbwheelCallbackSet

Sets the function that will be called when the thumbwheel has been sampled.

Prototype:

```
void  
ThumbwheelCallbackSet(void (*pfnCallback)(unsigned short usThumbwheelmV))
```

Parameters:

pfnCallback is a pointer to the function to be called when a thumbwheel sample is available.

Description:

This function sets the address of the function to be called when a new thumbwheel sample is available

Returns:

None.

12.2.1.2 ThumbwheelInit

Initializes the ADC to read the thumbwheel potentiometer.

Prototype:

```
void  
ThumbwheelInit(void)
```

Description:

This function configures ADC sequence 2 to sample the thumbwheel potentiometer under processor trigger control.

Returns:

None.

12.2.1.3 ThumbwheelTrigger

Triggers the ADC to capture a single sample from the thumbwheel.

Prototype:

```
void  
ThumbwheelTrigger(void)
```

Description:

This function triggers the ADC and starts the process of capturing a single sample from the thumbwheel potentiometer. Once the sample is available, the user-supplied callback function, provided via a call to `ThumbwheelCallbackSet{}`, will be called with the result.

Returns:

None.

12.3 Programming Example

The following example shows how to initialize the thumbwheel driver and capture one sample from the potentiometer.

```
volatile tBoolean g_bSampleRead = false;  
unsigned short g_usThumbwheelmV = 0;  
  
//  
// Application callback function which will be informed on the thumbwheel  
// potentiometer voltage when a new sample is available. The parameter  
// passed is scaled in millivolts.
```



```
//
void AppThumbwheelCallback(unsigned short usVoltageMV)
{
    //
    // Remember the voltage we were passed and signal the main loop that
    // the thumbwheen has been read.
    //
    g_usThumbwheelmV = usVoltageMV;
    g_bSampleRead = true;
}

int main(void)
{
    ...

    //
    // Initialize the thumbwheel driver and provide it our callback
    //
    ThumbwheelInit();
    ThumbwheelCallbackSet(AppThumbwheelCallback);

    //
    // Request a sample from the thumbwheel.
    //
    ThumbwheelTrigger();

    //
    // Wait until the sample is available.
    //
    while(!g_bSampleRead);

    //
    // Do something with the result.
    //

    ...
}
```


13 Touch Screen Driver

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13.1 Introduction

The touch screen is a pair of resistive layers on the surface of the display. One layer has connection points at the top and bottom of the screen, and the other layer has connection points at the left and right of the screen. When the screen is touched, the two layers make contact and electricity can flow between them.

The horizontal position of a touch can be found by applying positive voltage to the right side of the horizontal layer and negative voltage to the left side. When not driving the top and bottom of the vertical layer, the voltage potential on that layer will be proportional to the horizontal distance across the screen of the press, which can be measured with an ADC channel. By reversing these connections, the vertical position can also be measured. When the screen is not being touched, there will be no voltage on the non-powered layer.

By monitoring the voltage on each layer when the other layer is appropriately driven, touches and releases on the screen, as well as movements of the touch, can be detected and reported.

In order to read the current voltage on the two layers and also drive the appropriate voltages onto the layers, each side of each layer is connected to both a GPIO and an ADC channel. The GPIO is used to drive the node to a particular voltage, and when the GPIO is configured as an input, the corresponding ADC channel can be used to read the layer's voltage.

The touch screen is sampled every millisecond, with four samples required to properly read both the X and Y position. Therefore, 250 X/Y sample pairs are captured every second.

Like the display driver, the touch screen driver operates in the same four orientations (selected in the same manner). Default calibrations are provided for using the touch screen in each orientation; the calibrate application can be used to determine new calibration values if necessary.

The touch screen driver utilizes sample sequence 3 of the ADC and timer 1 subtimer A. The interrupt from the ADC sample sequence 3 is used to process the touch screen readings; the [TouchScreenIntHandler\(\)](#) function should be called when this interrupt occurs (which is typically accomplished by placing it in the vector table in the startup code for the application).

The touch screen driver makes use of calibration parameters determined using the “calibrate” example application. The theory behind these parameters is explained by Carlos E. Videles in the June 2002 issue of Embedded Systems Design. It can be found online at <http://www.embedded.com/story/OEG20020529S0046>.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `touch.c` containing the source code and `thumbwheel.h` containing the API definitions for use by applications.

13.2 API Functions

Functions

- void [TouchScreenCallbackSet](#) (long (*pfnCallback)(unsigned long ulMessage, long lX, long lY))
- void [TouchScreenInit](#) (void)
- void [TouchScreenIntHandler](#) (void)

13.2.1 Function Documentation

13.2.1.1 TouchScreenCallbackSet

Sets the callback function for touch screen events.

Prototype:

```
void  
TouchScreenCallbackSet (long (*long) (unsigned ulMessage, long lX, long  
lY) pfnCallback)
```

Parameters:

pfnCallback is a pointer to the function to be called when touch screen events occur.

Description:

This function sets the address of the function to be called when touch screen events occur. The events that are recognized are the screen being touched (“pen down”), the touch position moving while the screen is touched (“pen move”), and the screen no longer being touched (“pen up”).

Returns:

None.

13.2.1.2 TouchScreenInit

Initializes the touch screen driver.

Prototype:

```
void  
TouchScreenInit (void)
```

Description:

This function initializes the touch screen driver, beginning the process of reading from the touch screen. This driver uses the following hardware resources:

- ADC sample sequence 3
- Timer 1 subtimer A

Returns:

None.

13.2.1.3 TouchScreenIntHandler

Handles the ADC interrupt for the touch screen.

Prototype:

```
void
TouchScreenIntHandler(void)
```

Description:

This function is called when the ADC sequence that samples the touch screen has completed its acquisition. The touch screen state machine is advanced and the acquired ADC sample is processed appropriately.

It is the responsibility of the application using the touch screen driver to ensure that this function is installed in the interrupt vector table for the ADC3 interrupt.

Returns:

None.

13.3 Programming Example

The following example shows how to initialize the touchscreen driver and the callback function which receives notifications of touch and release events in cases where the StellarisWare Graphics Library widget manager is not being used by the application.

```

//*****
//
// Globals used to hold the current touch position.
//
//*****
long g_lTouchX, g_lTouchY;

//*****
//
// Globals used to hold flags indicating any touchscreen event received.
//
//*****
volatile unsigned long g_ulFlags;

//*****
//
// The touch screen driver calls this function to report all state changes.
//
//*****
static long
TouchTestCallback(unsigned long ulMessage, long lX, long lY)
{
    //
    // Save the new touch position.
    //
    g_lTouchX = lX;
    g_lTouchY = lY;

    //
    // Determine what to do now. In this case, we merely set flags that the
    // application main loop can deal with later.
    //
    switch(ulMessage)

```

```
    {
        case WIDGET_MSG_PTR_UP:
            g_ulFlags |= FLAG_PTR_UP;
            break;

        case WIDGET_MSG_PTR_DOWN:
            g_ulFlags |= FLAG_PTR_DOWN;
            break;

        case WIDGET_MSG_PTR_MOVE:
            g_ulFlags |= FLAG_PTR_MOVE;
            break;

        default:
            break;
    }

    return(0);
}

int main(void)
{
    ...

    //
    // Initialize the touch screen driver.
    //
    TouchScreenInit();
    TouchScreenCallbackSet(TouchTestCallback);

    ...

    //
    // Process touch events as signaled via g_ulFlags.
    //

    ...
}
```

If using the StellarisWare Graphics Library widget manager, touchscreen initialization code is as follows. In this case, the touchscreen callback is provided within the widget manager so no additional function is required in the application code.

```
int main(void)
{
    ...

    //
    // Initialize the touch screen driver when using the graphics library
    // widget manager.
    //
    TouchScreenInit();
    TouchScreenCallbackSet(WidgetPointerMessage);

    ...
}
```

14 Video Camera API

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14.1 Introduction

The camera module is a low level driver supporting the motion video capture and display functions provided by the optional FPGA/Camera/LCD daughter board. Applications making use of the graphics library may access these features via the “vidwidget” widget which wraps the camera API and offers a higher level interface. Applications must not mix calls to the camera and vidwidget APIs.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `camera.c` containing the source code and `camera.h` containing the API definitions for use by applications.

14.2 API Functions

Defines

- `BFROMPIXEL(usPix)`
- `BRIGHTNESS_NORMAL`
- `CAMERA_EVENT_CAPTURE_END`
- `CAMERA_EVENT_CAPTURE_MATCH`
- `CAMERA_EVENT_CAPTURE_START`
- `CAMERA_EVENT_DISPLAY_END`
- `CAMERA_EVENT_DISPLAY_MATCH`
- `CAMERA_EVENT_DISPLAY_START`
- `CONTRAST_NORMAL`
- `GFROMPIXEL(usPix)`
- `RFROMPIXEL(usPix)`
- `SATURATION_NORMAL`

Functions

- void `CameraBrightnessSet` (unsigned char ucBrightness)
- void `CameraCaptureBufferSet` (unsigned long ulAddr, unsigned short usStride, tBoolean bAsync)
- void `CameraCaptureMatchSet` (unsigned short usLine)
- void `CameraCaptureStart` (void)
- void `CameraCaptureStop` (tBoolean bAsync)
- void `CameraCaptureTypeSet` (unsigned long ulFlags)

- void [CameraColorBarsEnable](#) (tBoolean bEnable, tBoolean bMix)
- void [CameraContrastSet](#) (unsigned char ucContrast)
- void [CameraDisplayBufferSet](#) (unsigned long ulAddr, unsigned short usStride, tBoolean bAsync)
- void [CameraDisplayChromaKeyEnable](#) (tBoolean bEnable)
- void [CameraDisplayChromaKeySet](#) (unsigned long ulRGB)
- void [CameraDisplayDownscaleSet](#) (tBoolean bDownscale)
- void [CameraDisplayMatchSet](#) (unsigned short usLine)
- void [CameraDisplayStart](#) (void)
- void [CameraDisplayStop](#) (tBoolean bAsync)
- void [CameraEventsSet](#) (unsigned long ulEvents, unsigned long ulEventMask)
- void [CameraFlipSet](#) (tBoolean bFlip)
- void [CameraImageDataGet](#) (tBoolean bCapBuffer, unsigned short usX, unsigned short usY, unsigned long ulNumPixels, tBoolean b24bit, unsigned short *pusBuffer)
- tBoolean [CameraInit](#) (unsigned long ulFlags, unsigned long ulCaptureAddr, tCameraCallback pfnCallback)
- void [CameraMirrorSet](#) (tBoolean bMirror)
- void [CameraSaturationSet](#) (unsigned char ucSaturation)

14.2.1 Define Documentation

14.2.1.1 BFROMPIXEL

This macro can be used to extract an 8 green blue color component from a 16 bit RGB pixel read from the video camera.

Definition:

```
#define BFROMPIXEL(usPix)
```

Parameters:

usPix is a 16 bit pixel in the format captured by the video camera.

Returns:

Returns the 8 bit blue color component extracted from the supplied pixel.

14.2.1.2 #define BRIGHTNESS_NORMAL

The value to pass to the [CameraBrightnessSet\(\)](#) function to set normal brightness level in the video image.

14.2.1.3 #define CAMERA_EVENT_CAPTURE_END

This event is generated as each video frame capture ends.

14.2.1.4 #define CAMERA_EVENT_CAPTURE_MATCH

This event is generated during each captured video frame when the captured line number equals the value passed to [CameraCaptureMatchSet\(\)](#).

14.2.1.5 #define CAMERA_EVENT_CAPTURE_START

This event is generated as each video frame capture begins.

14.2.1.6 #define CAMERA_EVENT_DISPLAY_END

This event is generated as each frame sent to the LCD begins. Note that it is not possible to use this event to prevent display “tearing” since the FPGA has no access to the display’s internal frame and line sync signals.

14.2.1.7 #define CAMERA_EVENT_DISPLAY_MATCH

This event is generated during each frame sent to the LCD when the display line number equals the value passed to [CameraDisplayMatchSet\(\)](#).

14.2.1.8 #define CAMERA_EVENT_DISPLAY_START

This event is generated as each frame sent to the LCD ends. Note that it is not possible to use this event to prevent display “tearing” since the FPGA has no access to the display’s internal frame and line sync signals.

14.2.1.9 #define CONTRAST_NORMAL

The value to pass to the [CameraContrastSet\(\)](#) function to set normal contrast in the video image.

14.2.1.10 #define GFROMPIXEL(usPix)

This macro can be used to extract an 8 green red color component from a 16 bit RGB pixel read from the video camera.

Parameters:

usPix is a 16 bit pixel in the format captured by the video camera.

Returns:

Returns the 8 bit green color component extracted from the supplied pixel.

14.2.1.11 #define RFROMPIXEL(usPix)

This macro can be used to extract an 8 bit red color component from a 16 bit RGB pixel read from the video camera.

Parameters:

usPix is a 16 bit pixel in the format captured by the video camera.

Returns:

Returns the 8 bit red color component extracted from the supplied pixel.

14.2.1.12 #define SATURATION_NORMAL

The value to pass to the [CameraSaturationSet\(\)](#) function to set normal color saturation level in the video image.

14.2.2 Function Documentation

14.2.2.1 void CameraBrightnessSet (unsigned char *ucBrightness*)

Sets the brightness of the video image.

Parameters:

ucBrightness is an unsigned value representing the required brightness for the video image. Values in the range [0, 255] are scaled to represent exposure values between -4EV and +4EV with **BRIGHTNESS_NORMAL** (128) representing “normal” brightness or 0EV adjustment.

Description:

This function sets the brightness (exposure) of the captured video image. Values of *ucBrightness* above **BRIGHTNESS_NORMAL** cause the image to be brighter while values below this darken the image.

Returns:

None.

14.2.2.2 CameraCaptureBufferSet

Definition:

```
void CameraCaptureBufferSet (unsigned long ulAddr , unsigned short  
usStride , tBoolean bAsync )
```

Description:

Sets the video capture buffer.

Parameters:

ulAddr is the address of the video capture buffer in the FPGA SRAM. This address must be between 0x0 and (0x100000 - video frame size).

usStride is the width of the video capture buffer in bytes. This must be at least as wide as a single line of the configured video capture frame size (640 * 2 for VGA or 320 * 2 for QVGA) and must be even.

bAsync indicates whether to wait for the capture change to take effect before returning or not. If **true**, the call will return immediately. If **false**, the call will block until the new capture buffer parameters have been read and applied.

This function may be called to move or resize the video capture buffer. The initial buffer is configured during a call to `CaptureInit()` with stride 1280 for VGA resolution or 640 for QVGA at the location provided in the **ulCaptureAddr** parameter to that function.

The caller is responsible for ensuring that the video capture buffer is large enough to hold the currently configured video frame size and that the buffer does not overlap with any other buffer in FPGA SRAM (specifically the graphics buffer).

Returns:

None.

14.2.2.3 CameraCaptureMatchSet

Definition:

```
void CameraCaptureMatchSet (unsigned short usLine )
```

Description:

Sets the capture line on which **CAMERA_EVENT_CAPTURE_MATCH** is generated.

Parameters:

usLine is the video line number at which **CAMERA_EVENT_CAPTURE_MATCH** will be generated. Valid values are 0 to 479 if VGA resolution is being captured or 0 to 239 if QVGA is being captured.

This call sets the video line at which the **CAMERA_EVENT_CAPTURE_MATCH** event will be generated assuming this event has previously been enabled using a call to [CameraEventsSet\(\)](#).

Returns:

None.

14.2.2.4 CameraCaptureStart

Definition:

```
void CameraCaptureStart (void)
```

Description:

Starts video capture.

This call starts the process of capturing video frames. Frames from the camera are written to the buffer configured by a call to the [CameraInit\(\)](#) or [CameraCaptureBufferSet\(\)](#) functions. Following this call, events **CAMERA_EVENT_CAPTURE_START** and **CAMERA_EVENT_CAPTURE_END** will be notified to the application callback if they have been enabled via a call to the [CameraEventsSet\(\)](#) function.

Returns:

None.

14.2.2.5 CameraCaptureStop

Definition:

```
void CameraCaptureStop (tBoolean bAsync )
```

Description:

Stops video capture.

Parameters:

bAsync indicates whether the function call should return immediately (**true**) or wait until the current frame capture has completed (**false**) before returning.

This call stops the capture of video frames from the camera. Capture will stop at the end of the frame which is currently being read and the caller may choose to return immediately or wait until the end of the frame by setting the *bAsync* parameter appropriately. In cases where *bAsync* is set to **true**, capture will end following the next **CAMERA_EVENT_CAPTURE_END** event.

Returns:

None.

14.2.2.6 CameraCaptureTypeSet

Definition:

```
void CameraCaptureTypeSet (unsigned long ulFlags )
```

Description:

Sets the video capture size and pixel format.

Parameters:

ulFlags defines the video size and pixel format.

This function sets the size of video captured from the camera and the pixel format that is stored in the video buffer.

Parameter *ulFlags* is comprised of ORed flags indicating the desired video capture size and pixel format. It must contain two flags, one each from the following groups:

Video dimensions: **CAMERA_SIZE_VGA** for 640x480 capture or **CAMERA_SIZE_QVGA** for 320x240 capture.

Pixel format: **CAMERA_FORMAT_RGB565** to capture 16bpp RGB data with the red component in the most significant 5 bits of the half word pixel, **CAMERA_FORMAT_BGR565** to capture RGB data with the blue component in the most significant 5 bits of the pixel, **CAMERA_FORMAT_YUYV** to capture video data in YUV422 format with YUYV component ordering or **CAMERA_FORMAT_YVYU** to capture YUV422 data with YVYU component ordering. Note that direct display of the video data is only possible when using **CAMERA_FORMAT_RGB565**.

The caller is responsible for ensuring that the currently-defined video capture buffer is large enough to hold the video frame size defined here.

Returns:

None.

14.2.2.7 CameraColorBarsEnable

Definition:

```
void CameraColorBarsEnable (tBoolean bEnable , tBoolean bMix )
```

Description:

Enables or disables color bar output from the camera.

Parameters:

bEnable is set to **true** to enable color bar output from the camera or **false** to disable it and return to normal video capture.

bMix is set to **true** to mix the color bars with the image captured by the camera or **false** to replace the camera image completely with the color bars.

This function may be used to enable or disable color bars in the output from the camera. When **bMix** is **false**, the output from the camera is replaced with a standard pattern of vertical color bars. When **bMix** is **true**, the normal video output is mixed with the color bar image.

Returns:

None.

14.2.2.8 CameraContrastSet

Definition:

```
void CameraContrastSet (unsigned char ucContrast )
```

Description:

Sets the contrast of the video image.

Parameters:

ucContrast is a value representing the required contrast for the video image. Normal contrast is represented by value **CONTRAST_NORMAL** (128) with values above this increasing contrast and values below decreasing it.

This function sets the contrast of the captured video image. Higher values result in higher contrast images and lower values result in lower contrast images.

Returns:

None.

14.2.2.9 CameraDisplayBufferSet

Definition:

```
void CameraDisplayBufferSet (unsigned long ulAddr , unsigned short  
usStride , tBoolean bAsync )
```

Description:

Sets the address and size of the video display buffer.

Parameters:

ulAddr is the address of the video display buffer in the FPGA SRAM. This address must be between 0x0 and (0x100000 - video frame size).

usStride is the stride of the video display buffer in bytes. The stride is defined as the number of bytes between vertically adjacent pixels.

bAsync indicates whether the function call should return immediately (**true**) or wait until the new buffer parameters have taken effect (/b false) before returning.

This call sets the address and stride of the buffer from which video should be displayed. It is assumed that the image contained in this buffer is the size of the video currently being captured, VGA or QVGA.

When displaying live video, the buffer set here will generally be the same buffer as was passed to [CameraCaptureBufferSet\(\)](#) or [CameraInit\(\)](#). Decoupling the capture and display buffers, however, offers applications the option of capturing into one buffer while displaying from a different region of memory.

Returns:

None.

14.2.2.10 CameraDisplayChromaKeyEnable

Definition:

```
void CameraDisplayChromaKeyEnable (tBoolean bEnable )
```

Description:

Enables or disables chromakey mixing of graphics and video.

Parameters:

bEnable is **true** to enable graphics/video chromakeying and **false** to disable it.

This call enables or disables chromakeying. When disabled, the graphics plane will be displayed in preference to video whenever it is enabled by a call to [CameraDisplayStart\(\)](#), regardless of whether the video plane is also enabled.

With chromakey enabled and both graphics and video planes enabled, the graphics and video will be mixed on the display. At each pixel location, if the graphics plane contains the current chromakey color (as set using a call to [CameraDisplayChromaKeySet\(\)](#)), the corresponding video pixel will be shown instead of the graphics pixel.

Another way to think of this is that graphics plane sits above the video plane with the chromakey color transparent, allowing the video to “shine through” the graphics in areas where it exists.

Returns:

None.

14.2.2.11 CameraDisplayChromaKeySet

Definition:

```
void CameraDisplayChromaKeySet (unsigned long ulRGB )
```

Description:

Sets the chromakey color controlling graphics transparency.

Parameters:

ulRGB is the RGB888 color to use as the graphics chromakey.

This call sets the color which will be considered the chromakey. If this color appears in the graphics buffer and both graphics and video planes are enabled, the video pixel will be shown instead of the graphics pixel at this position.

Chromakeying offers an easy way to allow graphics to be displayed on top of the video with the video "shining through" the graphics in selected areas. To achieve this, paint areas of the graphics plane at which the video is to be visible using the chromakey color. Areas in which the graphics are to be seen should use colors other than the chromakey.

Note that only a simple compare is provided to select between graphics and video using the key - the video pixel is shown if the graphics color is exactly equal to the chromakey color. If you are preparing graphics which contain areas of chromakey, make sure that you do not anti-alias the edges between the visible portion of your graphic and the chromakey areas since these areas containing a blend of the chromakey and graphics colors will show as graphics pixels and result in a chromakey-colored fringe around your graphic.

Returns:

None.

14.2.2.12 CameraDisplayDownscaleSet

Definition:

```
void CameraDisplayDownscaleSet (tBoolean bDownscale )
```

Description:

Enables or disables video display downscaling.

Parameters:

bDownscale is **true** if the display should show half size video or **false** if full size video is to be displayed.

This call allows a client to display video either at the size it was captured or at quarter the capture size (skipping every other pixel and line). This may be used to show full screen video on the 320x240 display while capturing 640x480 VGA images into the video capture buffer.

Note that the downscaling uses a simple pixel skipping approach so video display quality will likely be somewhat lower when using this downscale option than would be achieved by capturing at the required resolution and displaying the native size without downscaling on display.

Returns:

None.

14.2.2.13 CameraDisplayMatchSet

Definition:

```
void CameraDisplayMatchSet (unsigned short usLine )
```

Description:

Sets the display line on which **CAMERA_EVENT_DISPLAY_MATCH** is generated.

Parameters:

usLine is the display line number at which **CAMERA_EVENT_DISPLAY_MATCH** will be generated. Valid values are 0 to 239.

This call sets the display line at which the **CAMERA_EVENT_DISPLAY_MATCH** event will be generated assuming this event has previously been enabled using a call to [CameraEventsSet\(\)](#). The event is generated as the FPGA scans out the data for the requested display row. Note, however, that the FPGA is merely writing data into the display controller's frame buffer so this event cannot be used to synchronize display updates to prevent "tearing."

Returns:

None.

14.2.2.14 CameraDisplayStart

Definition:

```
void CameraDisplayStart (void)
```

Description:

Enables the video display plane.

This call instructs the FPGA to enable the video display plane using the current size, position and buffer pointer settings.

Returns:

None.

14.2.2.15 CameraDisplayStop

Definition:

```
void CameraDisplayStop (tBoolean bAsync )
```

Description:

Disables the video display plane.

Parameters:

bAsync indicates whether the function call should return immediately (**true**) or wait until the current display frame has completed (/b false) before returning.

This call disables the video display plane on the LCD. The caller may choose to return immediately or wait until the end of the frame by setting the *bAsync* parameter appropriately. In cases where *bAsync* is set to **true**, display processing will end following the next **CAMERA_EVENT_DISPLAY_END** event.

Returns:

None.

14.2.2.16 CameraEventsSet

Definition:

```
void CameraEventsSet (unsigned long ulEvents , unsigned long  
ulEventMask )
```

Description:

Sets or clears notification of various video events.

Parameters:

ulEvents defines whether notification is enabled for a given event. Valid values are logical OR combinations of **CAMERA_EVENT_CAPTURE_START**, **CAMERA_EVENT_CAPTURE_END**, **CAMERA_EVENT_CAPTURE_MATCH**, **CAMERA_EVENT_DISPLAY_START**, **CAMERA_EVENT_DISPLAY_END** and **CAMERA_EVENT_DISPLAY_MATCH**.

ulEventMask defines which events are to be affected by this call. Valid values are as for *ulEvents*. A 1 in a given position in this parameter causes the respective event's notification state to be taken from *ulEvents*. A zero causes the associated bit in *ulEvents* to be ignored.

This function enables or disables client notification for one or more asynchronous video events. The use of a mask parameter, *ulEventMask*, allows notifications for groups of events to be configured without affecting the current states of other events.

When event notification is enabled, the callback function supplied on [CameraInit\(\)](#) will be called whenever that video event occurs.

Returns:

None.

14.2.2.17 CameraFlipSet

Definition:

```
void CameraFlipSet (tBoolean bFlip )
```

Description:

Sets the flip (vertical reflection) state of the video.

Parameters:

bFlip is **true** if the incoming motion video is to be flipped in the vertical direction or **false** to leave the image oriented normally.

Returns:

None.

14.2.2.18 CameraImageDataGet

Definition:

```
void CameraImageDataGet (tBoolean bCapBuffer , unsigned short usX  
, unsigned short usY , unsigned long ulNumPixels , tBoolean b24bit ,  
unsigned short *pusBuffer )
```

Description:

Reads pixel data from a given position in the video image.

Parameters:

bCapBuffer is **true** to read data from the image described by the current video capture buffer settings or **false** to read from the current video display buffer.

usX is the X coordinate of the start of the data to be read.

usY is the Y coordinate of the start of the data to be read.

ulNumPixels is the number of pixels to be read. Pixels are read starting at *usX*, *usY* and progressing to the right then downwards.

b24bit indicates whether to return raw (RGB565) data or extracted colors in RGB24 format.

pusBuffer points to the buffer into which the pixel data should be copied.

This function allows an application to read back a section of a captured image into a buffer in Stellaris internal SRAM. The data may be returned either in the native 16bpp format supported by the camera and LCD or in 24bpp RGB format. The 24bpp format returned places the blue component in the lowest memory location followed by the green component then the red component.

Returns:

None.

14.2.2.19 CameraInit

Definition:

```
tBoolean CameraInit (unsigned long ulFlags , unsigned long
ulCaptureAddr , tCameraCallback pfnCallback )
```

Description:

Initializes the camera and prepares for motion video capture.

Parameters:

ulFlags defines the initial video size and pixel format.

ulCaptureAddr is the address of the video capture buffer in daughter board SRAM where 0 indicates the bottom of the memory.

pfnCallback is a pointer to a function that will be called to inform the application of any requested asynchronous events related to video capture and display. If no callbacks are required, this parameter may be set to 0 to disable all notifications.

This function must be called after [PinoutSet\(\)](#) and before any other camera API to initialize the camera and set the initial capture buffer location. On exit, the camera is ready to start capturing motion video but capture has not been enabled.

Parameter *ulFlags* is comprised of ORed flags indicating the desired initial video capture size and pixel format. It must contain two flags, one each from the following groups:

Video dimensions: **CAMERA_SIZE_VGA** for 640x480 capture or **CAMERA_SIZE_QVGA** for 320x240 capture.

Pixel format: **CAMERA_FORMAT_RGB565** to capture 16bpp RGB data with the red component in the most significant 5 bits of the half word pixel, **CAMERA_FORMAT_BGR565** to capture RGB data with the blue component in the most significant 5 bits of the pixel, **CAMERA_FORMAT_YUYV** to capture video data in YUV422 format with YUYV component ordering or **CAMERA_FORMAT_VYU** to capture YUV422 data with VYU component ordering. Note that direct display of the video data is only possible when using **CAMERA_FORMAT_RGB565**.

The *ulCaptureAddr* parameter defines the start of the video capture buffer relative to the start of the daughter board SRAM. It is assumed that the buffer is sized to hold a single frame of video at the size requested in *ulFlags*. The caller is responsible for managing the SRAM on the daughter board and ensuring that this buffer is large enough to hold the captured video and that it does not overlap with the graphics display buffer, if used.

Returns:

Returns **true** if the camera was initialized successfully or **false** if the required hardware was not present or some other error occurred during initialization.

14.2.2.20 CameraMirrorSet

Definition:

```
void CameraMirrorSet (tBoolean bMirror )
```

Description:

Sets the mirror (horizontal reflection) state of the video.

Parameters:

bMirror is **true** if the incoming motion video is to be mirrored in the horizontal direction or **false** to leave the image oriented normally.

This function would typically be used for applications where a user is viewing their own video. Since people are more used to seeing a mirror image of themselves, this is often more comfortable than viewing the non- inverted image.

Returns:

None.

14.2.2.21 CameraSaturationSet

Definition:

```
void CameraSaturationSet (unsigned char ucSaturation )
```

Description:

Sets the color saturation of the video image.

Parameters:

ucSaturation is a value representing the required saturation for the video image. Normal saturation is represented by value **SATURATION_NORMAL** (128) with values above this increasing saturation and values below decreasing it.

This function sets the color saturation of the captured video image. Higher values result in more vivid images and lower values produce a muted or even monochrome effect.

Returns:

None.

14.3 Programming Example

The following example shows how to initialize the video camera on the daughter board and start capturing VGA video into a buffer in the daughter board's SRAM.

```
#include "inc/hw_types.h"
#include "driverlib/sysctl.h"
#include "drivers/set_pinout.h"
#include "drivers/camera.h"
```

```
int
main(void)
{
    //
    // Set the system clock to run at 50MHz from the PLL
    //
    SysCtlClockSet(SYSCTL_SYSDIV_4 | SYSCTL_USE_PLL | SYSCTL_OSC_MAIN |
                    SYSCTL_XTAL_16MHZ);

    //
    // Set the device pinout appropriately for this board.
    //
    PinoutSet();

    //
    // Make sure we detected the FPGA daughter board since this application
    // requires it.
    //
    if(g_eDaughterType != DAUGHTER_FPGA)
    {
        //
        // We can't run - the FPGA daughter board doesn't seem to be there.
        //
        while(1)
        {
            //
            // Hang here on error.
            //
        }
    }

    //
    // Initialize the camera module and configure for VGA-sized capture.
    //
    CameraInit((CAMERA_SIZE_VGA | CAMERA_FORMAT_RGB565), VIDEO_BUFF_BASE, 0);

    //
    // Start capturing motion video.
    //
    CameraCaptureStart();

    //
    // Set the video display buffer address.
    //
    CameraDisplayBufferSet(VIDEO_BUFF_BASE, VIDEO_BUFF_STRIDE, true);

    //
    // Downscale the 640x480 video to fit the 320x240 display.
    //
    CameraDisplayDownscaleSet(true);

    //
    // Start display of the video.
    //
    CameraDisplayStart(true);

    //
    // Drop into our main loop (which, in this case, doesn't do anything).
    //
    while(1)
    {
    }
}
```

15 Video Display Widget

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15.1 Introduction

The vidwidget module contains a custom control widget for use with the Stellaris Graphics Library and dk-lm3s9b96 hardware equipped with the optional FPGA/Camera/LCD daughter board. This widget supports display of motion or still video images on the LCD display. A “VidWidget” class object can be thought of as a video canvas.

When using the FPGA/Camera/LCD daughter board, motion video and graphics occupy two different display planes with graphics logically “on top” of video. Although the video widget may occupy any subrectangle of the display, the video image itself is always positioned at the top left of the display and covers the whole screen area. Areas of the graphics display which are painted with a chromakey color become transparent when video is enabled and allow the video image to shine through the graphics plane. The video widget automatically paints itself with the user’s chromakey color but note that any other areas of the display containing this color will also become transparent and show video. Applications should, therefore, chose a unique color that does not appear elsewhere in their user interface for the areas of the screen which are to show video. Typically a color such as bright magenta (ClrMagenta) is used.

Video may be captured in VGA (640x480) or QVGA (320x240) resolution and may be displayed full size or downscaled by a factor of 2 in both dimensions. In cases where VGA video is being capture and no downscaling is selected, dragging a finger or stylus over the video widget area will cause the video displayed to scroll within the widget.

To use this widget, an application must also include the source for the camera driver, `camera.c`, and must be using the special display driver intended for the FPGA daughter board, `kitronix_320x240x16_fpga.c`. Both of these files can be found in the same directory as the video widget source file. The underlying camera API is wrapped by vidwidget so applications using vidwidget must not call any camera APIs directly.

This driver is located in `boards/dk-lm3s9b96/drivers`, with `vidwidget.c` containing the source code and `vidwidget.h` containing the API definitions for use by applications.

15.2 API Functions

Data Structures

- `tVidInst`
- `tVidWidget`

Defines

- [VideoWidget](#)(sName, pParent, pNext, pChild, pDisplay, IX, IY, IWidth, IHeight, ulStyle, ulKeyColor, ulOutlineColor, ucBorderWidth, pfnOnScroll, psInst)
- [VideoWidgetLock](#)(pWidget)
- [VideoWidgetOutlineColorSet](#)(pWidget, ulColor)
- [VideoWidgetOutlineOff](#)(pWidget)
- [VideoWidgetOutlineOn](#)(pWidget)
- [VideoWidgetOutlineWidthSet](#)(pWidget, ucWidth)
- [VideoWidgetScrollCallbackSet](#)(pWidget, pfnOnScrl)
- [VideoWidgetStruct](#)(pParent, pNext, pChild, pDisplay, IX, IY, IWidth, IHeight, ulStyle, ulKeyColor, ulOutlineColor, ucBorderWidth, pfnOnScroll, psInst)
- [VideoWidgetUnlock](#)(pWidget)
- [VW_STYLE_BLANK](#)
- [VW_STYLE_DOWNSCALE](#)
- [VW_STYLE_FLIP](#)
- [VW_STYLE_FREEZE](#)
- [VW_STYLE_LOCKED](#)
- [VW_STYLE_MIRROR](#)
- [VW_STYLE_OUTLINE](#)
- [VW_STYLE_VGA](#)

Functions

- void [VideoWidgetBlankSet](#) (tWidget *pWidget, tBoolean bBlank)
- void [VideoWidgetBrightnessSet](#) (tWidget *pWidget, unsigned char ucBrightness)
- void [VideoWidgetCameraFlipSet](#) (tWidget *pWidget, tBoolean bFlip)
- void [VideoWidgetCameraInit](#) (tVideoWidget *pWidget, unsigned long ulBufAddr)
- void [VideoWidgetCameraMirrorSet](#) (tWidget *pWidget, tBoolean bMirror)
- void [VideoWidgetContrastSet](#) (tWidget *pWidget, unsigned char ucContrast)
- void [VideoWidgetDownscaleSet](#) (tWidget *pWidget, tBoolean bDownscale)
- void [VideoWidgetFreezeSet](#) (tWidget *pWidget, tBoolean bFreeze)
- void [VideoWidgetImageDataGet](#) (tWidget *pWidget, unsigned short usX, unsigned short usY, unsigned long ulPixels, unsigned short *pusBuffer, tBoolean b24bpp)
- void [VideoWidgetInit](#) (tVideoWidget *pWidget, const tDisplay *pDisplay, unsigned long ulBufAddr, tBoolean bVGA, long IX, long IY, long IWidth, long IHeight, tVideoInst *psInst)
- void [VideoWidgetKeyColorSet](#) (tWidget *pWidget, unsigned long ulColor)
- long [VideoWidgetMsgProc](#) (tWidget *pWidget, unsigned long ulMsg, unsigned long ulParam1, unsigned long ulParam2)
- void [VideoWidgetResolutionSet](#) (tWidget *pWidget, tBoolean bVGA)
- void [VideoWidgetSaturationSet](#) (tWidget *pWidget, unsigned char ucSaturation)

15.2.1 Data Structure Documentation

15.2.1.1 tVideoInst

Definition:

```
typedef struct
{
    unsigned long ulCapAddr;
    unsigned short sXStart;
    unsigned short sYStart;
    unsigned short usDispWidth;
    unsigned short usDispHeight;
    unsigned short usCapWidth;
    unsigned short usCapHeight;
    unsigned short usStride;
    short sXOffset;
    short sYOffset;
}
tVideoInst
```

Members:

ulCapAddr The address of the start of the video capture buffer. This address is relative to the start of the FPGA's 1MB SRAM memory.

sXStart The X coordinate of the saved start position used during dragging of the video image.

sYStart The Y coordinate of the saved start position used during dragging of the video image.

usDispWidth The width of the displayable video image in pixels. This value will take into account whether or not video display downscaling is in use.

usDispHeight The height of the displayable video image in pixels. This value will take into account whether or not video display downscaling is in use.

usCapWidth The width of the captured video image in pixels.

usCapHeight The height of the captured video image in pixels.

usStride The stride of the video capture buffer in bytes.

sXOffset The X coordinate of the video image pixel corresponding to the top left of the display.

sYOffset The Y coordinate of the video image pixel corresponding to the top left of the display.

Description:

This structure contains workspace fields used by the video widget. It is provided by the application when a widget is created but must not be modified by the application after this.

15.2.1.2 tVideoWidget

Definition:

```
typedef struct
{
    tWidget sBase;
    unsigned long ulStyle;
    unsigned long ulKeyColor;
    unsigned long ulOutlineColor;
    unsigned char ucBorderWidth;
    void (*pfnOnScroll)(tWidget *pWidget,
```

```
        short  sX,  
        short  sY);  
    tVideoInst *psVideoInst;  
}  
tVideoWidget
```

Members:

sBase The generic widget information. This structure is common to all widgets used by the Stellaris Graphics Library.

ulStyle The style for this widget. This is a set of flags defined by VW_STYLE_XXX labels.

ulKeyColor The 24-bit RGB color used as the video chromakey. The widget background will be filled with this color and video will appear on top of areas colored using this value. Note that the video plane occupies the whole screen regardless of the position and size of the video widget so video will show through areas of any other widgets which happen to use this color. A good rule of thumb is to pick some color which is highly unlikely to appear in the user interface, such as bright magenta (ClrMagenta).

ulOutlineColor The 24-bit RGB color used to outline this video widget, if VW_STYLE_OUTLINE is selected.

ucBorderWidth The width of the border to be drawn around the widget. This is ignored if VW_STYLE_OUTLINE is not set.

pfnOnScroll A pointer to the function to be called if the user scrolls the displayed video image.

psVideoInst A pointer to a structure in read/write memory that the widget can use to hold working variables. The application must not modify the contents of this structure.

Description:

This structure describes a video widget. It contains the standard widget fields in its sBase member followed by class-specific fields for the video widget.

15.2.2 Define Documentation

15.2.2.1 VideoWidget

Declares an initialized variable containing a video widget data structure.

Definition:

```
#define VideoWidget(sName,  
    pParent,  
    pNext,  
    pChild,  
    pDisplay,  
    lX,  
    lY,  
    lWidth,  
    lHeight,  
    ulStyle,  
    ulKeyColor,  
    ulOutlineColor,  
    ucBorderWidth,  
    pfnOnScroll,  
    psInst)
```


Parameters:

- sName** is the name of the variable to be declared.
- pParent** is a pointer to the parent widget.
- pNext** is a pointer to the sibling widget.
- pChild** is a pointer to the first child widget.
- pDisplay** is a pointer to the display on which to draw the video widget.
- IX** is the X coordinate of the upper left corner of the video widget.
- IY** is the Y coordinate of the upper left corner of the video widget.
- IWidth** is the width of the video widget.
- IHeight** is the height of the video widget.
- ulStyle** is the style to be applied to the video widget.
- ulKeyColor** is the color used to fill in the video widget and represents the color above which video image pixels will be shown.
- ulOutlineColor** is the color used to outline the video widget if **VW_STYLE_OUTLINE** is specified.
- ucBorderWidth** is the width of the border to paint if **VW_STYLE_OUTLINE** is specified.
- pfnOnScroll** is a pointer to the function that is called when the image is scrolled by the user. Scrolling is enabled if style flag **VW_STYLE_LOCKED** is not specified and the video capture size is larger than the display size.
- psInst** is a pointer to a structure in read/write memory that the widget can use to hold working variables.

Description:

This macro provides an initialized video widget data structure, which can be used to construct the widget tree at compile time in global variables (as opposed to run-time via function calls).

ulStyle is the logical OR of the following:

- **VW_STYLE_OUTLINE** to indicate that the video widget should be outlined.
- **VW_STYLE_LOCKED** to indicate that the video widget should ignore all user input and merely display the image. If this flag is clear, the user may scroll the video image within the widget by dragging a finger on the touchscreen.
- **VW_STYLE_BLANK** to indicate that video is not to be shown. If set, the widget will display the key color specified in **ulKeyColor** instead of any captured video image. This flag may be used to hide or show the video image.
- **VW_STYLE_FREEZE** to indicate that motion video capture is disabled. If set, the widget will show the last captured image, if any exists. This flag may be used to freeze and unfreeze motion video.
- **VW_STYLE_VGA** to indicate that 640x480 resolution video should be captured. If clear, QVGA (320x240) video will be captured.
- **VW_STYLE_DOWNSCALE** to indicate that the video should be downscaled by a factor of two in both width and height on display. This flag may be used to scale VGA video appropriately for display on a QVGA display or to show QVGA video in the top left quadrant of the display. This flag does not affect the image stored in the video capture buffer.
- **VW_STYLE_MIRROR** to indicate that the captured video must be flipped horizontally. This flag affects the image stored in the video capture buffer.
- **VW_STYLE_FLIP** to indicate that the captured video must be flipped vertically. This flag affects the image stored in the video capture buffer.

Note:

The FPGA on the daughter board does not support positioning of the video image origin anywhere on the display other than at the top left. As a result, if the video widget is placed

elsewhere on the screen, it will show the portion of the underlying video corresponding to its area but it will not be possible to reposition the video such that it is aligned with the top left corner of the widget. It is recommended, therefore, that the video widget be positioned in the top left corner of the display and take up the full area of the display. In this case, there are three video display possibilities:

1. QVGA capture with scaling disabled or VGA capture with scaling enabled results in the full image being visible on the display.
2. VGA capture with scaling disabled results in a quarter of the video occupying the full screen area. In this case, the visible portion of the video image may be chosen by scrolling.
3. QVGA capture with scaling enabled results in the full video image being visible in the top left quadrant of the display.

Returns:

Nothing; this is not a function.

15.2.2.2 VideoWidgetLock

Locks a video widget making it ignore pointer input.

Definition:

```
#define VideoWidgetLock(pWidget)
```

Parameters:

pWidget is a pointer to the widget to modify.

Description:

This function locks a video widget and makes it ignore all pointer input. When locked, a widget acts as a passive canvas and the video shown in the widget cannot be scrolled.

Returns:

None.

15.2.2.3 VideoWidgetOutlineColorSet

Sets the outline color of a video widget.

Definition:

```
#define VideoWidgetOutlineColorSet(pWidget,  
                                   ulColor)
```

Parameters:

pWidget is a pointer to the video widget to be modified.

ulColor is the 24-bit RGB color to use to outline the widget.

Description:

This function changes the color used to outline the video widget on the display. The display is not updated until the next paint request.

Returns:

None.

15.2.2.4 VideoWidgetOutlineOff

Disables outlining of a video widget.

Definition:

```
#define VideoWidgetOutlineOff (pWidget)
```

Parameters:

pWidget is a pointer to the video widget to modify.

Description:

This function disables the outlining of a video widget. The display is not updated until the next paint request.

Returns:

None.

15.2.2.5 VideoWidgetOutlineOn

Enables outlining of a video widget.

Definition:

```
#define VideoWidgetOutlineOn (pWidget)
```

Parameters:

pWidget is a pointer to the video widget to modify.

Description:

This function enables the outlining of a video widget. The display is not updated until the next paint request.

Returns:

None.

15.2.2.6 VideoWidgetOutlineWidthSet

Sets the outline width of a video widget.

Definition:

```
#define VideoWidgetOutlineWidthSet (pWidget,  
                                     ucWidth)
```

Parameters:

pWidget is a pointer to the video widget to be modified.

ucWidth This function changes the width of the border around the video widget. The display is not updated until the next paint request.

Returns:

None.

15.2.2.7 #define VideoWidgetScrollCallbackSet(pWidget, pfnOnScrl)

Sets the function to call when the video image is scrolled.

Parameters:

pWidget is a pointer to the video widget to modify.

pfnOnScrl is a pointer to the scroll callback function.

Description:

This macro sets the callback function to be notified when this widget's video image is scrolled by dragging a finger or stylus over the video area. This function will be called any time the video image is repositioned as a result of user touchscreen input.

Returns:

None.

15.2.2.8 VideoWidgetStruct

Declares an initialized video widget data structure.

Definition:

```
#define VideoWidgetStruct (pParent,  
                           pNext,  
                           pChild,  
                           pDisplay,  
                           lX,  
                           lY,  
                           lWidth,  
                           lHeight,  
                           ulStyle,  
                           ulKeyColor,  
                           ulOutlineColor,  
                           ucBorderWidth,  
                           pfnOnScroll,  
                           psInst)
```

Parameters:

pParent is a pointer to the parent widget.

pNext is a pointer to the sibling widget.

pChild is a pointer to the first child widget.

pDisplay is a pointer to the display on which to draw the video widget.

lX is the X coordinate of the upper left corner of the video widget.

lY is the Y coordinate of the upper left corner of the video widget.

lWidth is the width of the video widget.

lHeight is the height of the video widget.

ulStyle is the style to be applied to the video widget.

ulKeyColor is the color used to fill in the video widget and represents the color above which video image pixels will be shown.

ulOutlineColor is the color used to outline the video widget if **VW_STYLE_OUTLINE** is specified.

ucBorderWidth is the width of the border to paint if **VW_STYLE_OUTLINE** is specified.

pfnOnScroll is a pointer to the function that is called when the image is scrolled by the user.

Scrolling is enabled if style flag **VW_STYLE_LOCKED** is not specified and the video capture size is larger than the display size.

psInst is a pointer to a structure in read/write memory that the widget can use to hold working variables.

Description:

This macro provides an initialized video widget data structure, which can be used to construct the widget tree at compile time in global variables (as opposed to run-time via function calls). This must be assigned to a variable, such as:

```
tVideoWidget g_sVideoArea = VideoWidgetStruct(...);
```

ulStyle is the logical OR of the following:

- **VW_STYLE_OUTLINE** to indicate that the video widget should be outlined.
- **VW_STYLE_LOCKED** to indicate that the video widget should ignore all user input and merely display the image. If this flag is clear, the user may scroll the video image within the widget by dragging a finger on the touchscreen.
- **VW_STYLE_BLANK** to indicate that video is not to be shown. If set, the widget will display the key color specified in **ulKeyColor** instead of any captured video image. This flag may be used to hide or show the video image.
- **VW_STYLE_FREEZE** to indicate that motion video capture is disabled. If set, the widget will show the last captured image, if any exists. This flag may be used to freeze and unfreeze motion video.
- **VW_STYLE_VGA** to indicate that 640x480 resolution video should be captured. If clear, QVGA (320x240) video will be captured.
- **VW_STYLE_DOWNSCALE** to indicate that the video should be downscaled by a factor of two in both width and height on display. This flag may be used to scale VGA video appropriately for display on a QVGA display or to show QVGA video in the top left quadrant of the display. This flag does not affect the image stored in the video capture buffer.
- **VW_STYLE_MIRROR** to indicate that the captured video must be flipped horizontally. This flag affects the image stored in the video capture buffer.
- **VW_STYLE_FLIP** to indicate that the captured video must be flipped vertically. This flag affects the image stored in the video capture buffer.

Returns:

Nothing; this is not a function.

15.2.2.9 VideoWidgetUnlock

Unlocks a video widget making it respond to touchscreen input.

Definition:

```
#define VideoWidgetUnlock(pWidget)
```

Parameters:

pWidget is a pointer to the widget to modify.

Description:

This function unlocks a video widget. When unlocked, a video widget will respond to touch-screen input by scrolling the video it is currently displaying assuming that the video image being displayed is larger than the display dimensions.

Returns:

None.

15.2.2.10 VW_STYLE_BLANK

Definition:

```
#define VW_STYLE_BLANK
```

Description:

This flag indicates that the video widget should show the chromakey color rather than the captured video image. If absent, the widget will show the current contents of the video capture buffer, the last captured video image if [VW_STYLE_FREEZE](#) is set or motion video otherwise.

15.2.2.11 VW_STYLE_DOWNSCALE

Definition:

```
#define VW_STYLE_DOWNSCALE
```

Description:

This flag causes the captured video to be downscaled by a factor of 2 in both width and height on display. If [VW_STYLE_VGA](#) is set, this flag can be used to scale the video for display on the 320x240 display while maintaining the full resolution image in the video capture buffer. If [VW_STYLE_VGA](#) is not set and the system is capturing QVGA resolution video, this flag will allow the QVGA image to be downscaled and shown in the top left quadrant of the display.

This flag does not affect the size of the video image captured.

15.2.2.12 VW_STYLE_FLIP

Definition:

```
#define VW_STYLE_FLIP
```

Description:

This flag causes the captured video to be flipped vertically. Used in combination with [VW_STYLE_MIRROR](#), this flag allows video to be rotated 180 degrees. It affects the image stored in the video capture buffer.

15.2.2.13 VW_STYLE_FREEZE

Definition:

```
#define VW_STYLE_FREEZE
```

Description:

This flag allows motion video capture to be stopped or started. When set, motion video will be captured into the current capture buffer. When clear, no motion video will be captured. In this case, if [VW_STYLE_BLANK](#) is not also set, the display will show the current contents of the video capture buffer, usually the last video frame captured.

15.2.2.14 VW_STYLE_LOCKED

Definition:

```
#define VW_STYLE_LOCKED
```

Description:

This flag indicates that the video widget should ignore all touchscreen activity. If not specified, the user will be able to scroll over the captured video by dragging their finger over the touchscreen assuming that the video capture size is greater than the widget area.

15.2.2.15 VW_STYLE_MIRROR

Definition:

```
#define VW_STYLE_MIRROR
```

Description:

This flag causes the captured video to be mirrored (flipped horizontally) and is useful for video-conferencing-type applications where a user is viewing video of themselves. In these situations people are typically more comfortable viewing a mirror image. The flag affects the image stored in the video capture buffer.

15.2.2.16 VW_STYLE_OUTLINE

Definition:

```
#define VW_STYLE_OUTLINE
```

Description:

This flag indicates that the widget should be outlined. The outline width and color are controlled by the `ulOutlineColor` and `ucBorderWidth` fields of the [tVideoWidget](#) structure.

15.2.2.17 VW_STYLE_VGA

Definition:

```
#define VW_STYLE_VGA
```

Description:

This flag sets the camera to capture VGA (640x480) resolution images. If absent, QVGA (320x240) images are captured.

15.2.3 Function Documentation

15.2.3.1 VideoWidgetBlankSet

Enables or disables display of the video plane.

Prototype:

```
void  
VideoWidgetBlankSet (tWidget *pWidget,  
                    tBoolean bBlank)
```

Parameters:

pWidget is a pointer to the video widget.

bBlank is **true** if the video plane is to be enabled or **false** if it is to be disabled.

Description:

This function enables or disables display of the video plane. When enabled, the current contents of the video display buffer, either motion video or the last captured frame if video capture is disabled, will be shown on the screen above any pixels painted with the chromakey color. When disabled, the chromakey pixels will be shown on the screen rather than video.

Returns:

None.

15.2.3.2 VideoWidgetBrightnessSet

Sets the brightness of the video image.

Prototype:

```
void  
VideoWidgetBrightnessSet (tWidget *pWidget,  
                        unsigned char ucBrightness)
```

Parameters:

pWidget is a pointer to the video widget.

ucBrightness is an unsigned value representing the required brightness for the video image. Values in the range [0, 255] are scaled to represent exposure values between -4EV and +4EV with 128 representing "normal" brightness or 0EV adjustment.

Description:

This function sets the brightness (exposure) of the captured video image. Values of *ucBrightness* above 128 cause the image to be brighter while values below this darken the image.

This is a camera setting and affects the image stored in the capture buffer.

Returns:

None.

15.2.3.3 VideoWidgetCameraFlipSet

Flips the video image about its horizontal axis.

Prototype:

```
void  
VideoWidgetCameraFlipSet (tWidget *pWidget,  
                          tBoolean bFlip)
```

Parameters:

pWidget is a pointer to the video widget.

bFlip is **true** to flip the video image around its horizontal center or **false** to leave the video unaffected.

Description:

This function allows the captured video image to be flipped around its horizontal axis. When flipped, the bottom of the image appears at the top of the display. By using this function in conjunction with [VideoWidgetCameraMirrorSet\(\)](#), an application can rotate the video 180 degrees.

This is a camera setting and affects the image stored in the capture buffer.

Returns:

None.

15.2.3.4 VideoWidgetCameraInit

Initializes the camera associated with a statically allocated video widget.

Prototype:

```
void  
VideoWidgetCameraInit (tVideoWidget *pWidget,  
                      unsigned long ulBufAddr)
```

Parameters:

pWidget is a pointer to the video widget to initialize.

ulBufAddr is the FPGA SRAM address of the start of the video capture buffer. Valid values are from 0 through (1MB - (size of 1 video frame)).

Description:

This function must be called to correctly initialize a statically allocated video widget. It ensures that all the widget parameters which are relevant are passed to the low level camera driver and that the widget is ready to start capturing and displaying motion video.

Parameter *ulBufAddr* must point to an area of FPGA SRAM large enough to hold the largest video image that the application may ever capture. If only QVGA images are being used, the minimum buffer size is (320 * 240 * 2) bytes but if VGA resolution is ever selected using [VideoWidgetResolutionSet\(\)](#), the buffer must be at least (640 * 480 * 2) bytes in size.

A dynamically created video widget which makes calls to the various functions in the widget API to initialize each field of the widget structure need not call this function.

Returns:

None.

15.2.3.5 VideoWidgetCameraMirrorSet

Mirrors the video image about its vertical axis.

Prototype:

```
void  
VideoWidgetCameraMirrorSet (tWidget *pWidget,  
                           tBoolean bMirror)
```

Parameters:

pWidget is a pointer to the video widget.

bMirror is **true** to mirror the video image around its vertical axis or **false** to leave the video unaffected.

Description:

This function allows the captured video image to be mirrored around its vertical axis. When mirrored, the left of the image appears at the right of the display. By using this function in conjunction with [VideoWidgetCameraFlipSet\(\)](#), an application can rotate the video 180 degrees.

This operation is also useful in video-conferencing applications where a user is looking at their own image. In these cases, people often find that it feels more natural to view a mirror image of themselves.

This is a camera setting and affects the image stored in the capture buffer.

Returns:

None.

15.2.3.6 VideoWidgetContrastSet

Sets the contrast of the video image.

Prototype:

```
void  
VideoWidgetContrastSet (tWidget *pWidget,  
                      unsigned char ucContrast)
```

Parameters:

pWidget is a pointer to the video widget.

ucContrast is a value representing the required contrast for the video image. Normal contrast is represented by value 128 with values above this increasing contrast and values below decreasing it.

Description:

This function sets the contrast of the captured video image. Higher values result in higher contrast images and lower values result in lower contrast images.

This is a camera setting and affects the image stored in the capture buffer.

Returns:

None.

15.2.3.7 VideoWidgetDownscaleSet

Enables or disabled video display downscaling.

Prototype:

```
void  
VideoWidgetDownscaleSet (tWidget *pWidget,  
                          tBoolean bDownscale)
```

Parameters:

pWidget is a pointer to the video widget.

bDownscale is **true** to downscale the video image by 50% in each dimension or **false** to show video without scaling.

Description:

This function allows an application to downscale the motion or still video image by 50% in each dimension. This allows VGA resolution video to be captured and the full image to be displayed on the QVGA resolution LCD display. If QVGA video is being captured, downscaling will enable it to be shown in the top left quadrant of the display.

If VGA video is captured and downscaling is disabled, the user may select the section of the video image which is displayed by dragging a stylus or finger over the video widget to scroll the video.

Video scaling is a display operation and does not affect the image stored in the capture buffer.

Returns:

None.

15.2.3.8 VideoWidgetFreezeSet

Freezes or unfreezes motion video.

Prototype:

```
void  
VideoWidgetFreezeSet (tWidget *pWidget,  
                      tBoolean bFreeze)
```

Parameters:

pWidget is a pointer to the video widget.

bFreeze is **true** if video capture is to be frozen or **false** if it is to be started.

Description:

This function freezes or unfreezes motion video capture. When capture is frozen, a static image will be displayed in the video plane assuming that video display has not been disabled using a call to [VideoWidgetBlankSet\(\)](#).

Returns:

None.

15.2.3.9 VideoWidgetImageDataGet

Reads pixel data from a given position in the video image.

Prototype:

```
void  
VideoWidgetImageDataGet (tWidget *pWidget,  
                        unsigned short usX,  
                        unsigned short usY,  
                        unsigned long ulPixels,  
                        unsigned short *pusBuffer,  
                        tBoolean b24bpp)
```

Parameters:

pWidget is a pointer to the video widget.

usX is the X coordinate of the start of the data to be read. Valid values are [0,639] of VGA capture is configured or [0,319] if QVGA is being captured.

usY is the Y coordinate of the start of the data to be read. Valid values are [0,479] of VGA capture is configured or [0,239] if QVGA is being captured.

ulPixels is the number of pixels to be read. Pixels are read starting at *usX*, *usY* and progressing to the right then downwards.

pusBuffer points to the buffer into which the pixel data should be copied.

b24bpp indicates whether to return raw (RGB565) data or extracted colors in RGB24 format.

Description:

This function allows an application to read back a section of a captured image into a buffer in Stellaris internal SRAM. The data may be returned either in the native 16bpp format supported by the camera and LCD or in 24bpp RGB format. The 24bpp format returned places the blue component in the lowest memory location followed by the green component then the red component.

It is recommended that data only be read from the capture buffer when video capture is frozen. This ensures that the data returned will comprise a single video image. Reading while capture is enabled may result in pixels being returned from different images since the capture buffer content may change during the time the reading is going on.

Returns:

None.

15.2.3.10 VideoWidgetInit

Initializes a Video widget.

Prototype:

```
void  
VideoWidgetInit (tVideoWidget *pWidget,  
                const tDisplay *pDisplay,  
                unsigned long ulBufAddr,  
                tBoolean bVGA,  
                long lX,  
                long lY,
```

```
long lWidth,  
long lHeight,  
tVideoInst *psInst)
```

Parameters:

pWidget is a pointer to the video widget to initialize.

pDisplay is a pointer to the display on which to draw the widget.

ulBufAddr is the FPGA SRAM address of the start of the video capture buffer. Valid values are from 0 through (1MB - (size of 1 video frame)).

bVGA is **true** if the capture buffer is sized to accept a 640x480 (VGA) video frame or **false** if sized for 320x240 (QVGA).

IX is the X coordinate of the upper left corner of the video widget.

IY is the Y coordinate of the upper left corner of the video widget.

IWidth is the width of the video widget.

IHeight is the height of the video widget.

psInst is a pointer to a structure in RAM that the widget can use to hold working variables.

Description:

This function initializes the provided video widget. The widget position is set based on the parameters passed. All other widget parameters including style flags are set to 0. The caller must make use of the various widget functions to set any required parameters or styles after making this call.

Returns:

None.

15.2.3.11 VideoWidgetKeyColorSet

Sets the graphics plane color on top of which video pixels will be displayed.

Prototype:

```
void  
VideoWidgetKeyColorSet(tWidget *pWidget,  
                        unsigned long ulColor)
```

Parameters:

pWidget is a pointer to the video widget.

ulColor is the 24 bit RGB color identifier representing the chromakey color to be used.

Description:

This function sets or changes the video chromakey color. This color is used to indicate where on the display video pixels are to be shown. It is used to fill the video widget allowing the video to “shine through” the graphics from the layer underneath. If your application statically allocates the video widget and calls [VideoWidgetCameraInit\(\)](#), it is not necessary to also call [VideoWidgetKeyColorSet\(\)](#) since the chromakey is also set during the widget initialization function.

Note that video will be displayed in all areas of the screen where the chromakey color is drawn in the graphics plane regardless of whether those areas are within the bounds of the vidwidget object itself. Care should, therefore, be taken to ensure that the chromakey color chosen is not used elsewhere in the user interface unless, of course, you intend video to show at those positions too.

Returns:

None.

15.2.3.12 VideoWidgetMsgProc

Handles messages for a Video widget.

Prototype:

```
long  
VideoWidgetMsgProc (tWidget *pWidget,  
                    unsigned long ulMsg,  
                    unsigned long ulParam1,  
                    unsigned long ulParam2)
```

Parameters:

pWidget is a pointer to the Video widget.

ulMsg is the message.

ulParam1 is the first parameter to the message.

ulParam2 is the second parameter to the message.

Description:

This function receives messages intended for this Video widget and processes them accordingly. The processing of the message varies based on the message in question.

Unrecognized messages are handled by calling `WidgetDefaultMsgProc()`.

This function is called by the Stellaris Graphics Library Widget Manager. An application should not call this function but should interact with the video widget using the other functions and macros offered by the widget API.

Returns:

Returns a value appropriate to the supplied message.

15.2.3.13 VideoWidgetResolutionSet

Sets the video capture resolution.

Prototype:

```
void  
VideoWidgetResolutionSet (tWidget *pWidget,  
                          tBoolean bVGA)
```

Parameters:

pWidget is a pointer to the video widget.

bVGA is **true** if VGA resolution (640x480) video capture is to be set or **false** if QVGA (320x240) is to be used.

Description:

This function allows the video capture dimensions to be set or changed. An application using a statically allocated widget which calls [VideoWidgetCameraInit\(\)](#) need not call this function

unless it wishes to change the capture resolution since the initialization function already configures the capture resolution.

If the resolution requested is already in use, this function will return without doing anything.

As a side effect of this function, the video display scroll position is reset if a resolution change takes effect.

An application using this function must be careful to ensure that the video capture buffer configured via [VideoWidgetCameraInit\(\)](#) or [VideoWidgetInit\(\)](#) is large enough to hold the video image size requested here.

Returns:

None.

15.2.3.14 VideoWidgetSaturationSet

Sets the color saturation of the video image.

Prototype:

```
void  
VideoWidgetSaturationSet (tWidget *pWidget,  
                          unsigned char ucSaturation)
```

Parameters:

pWidget is a pointer to the video widget.

ucSaturation is a value representing the required color saturation for the video image. Normal saturation is represented by value 128 with values above this increasing saturation and values below decreasing it.

Description:

This function sets the color saturation of the captured video image. Higher values result in more vivid images and lower values produce a muted or even monochrome effect.

This is a camera setting and affects the image stored in the capture buffer.

Returns:

None.

15.3 Programming Example

The following example shows how to initialize a video widget. The sample application `boards/dk-lm3s9b96/videocap` provides a working example of the use of this widget.

```
#include "inc/hw_types.h"  
#include "driverlib/interrupt.h"  
#include "driverlib/sysctl.h"  
#include "grlib/grlib.h"  
#include "grlib/widget.h"  
#include "drivers/vidwidget.h"  
#include "drivers/kitronix320x240x16_fpga.h"  
#include "drivers/touch.h"  
#include "drivers/set_pinout.h"
```

```
#include "drivers/camera.h"

//*****
//
// Workspace for the video widget.
//
//*****
tVideoInst g_sVideoInst;

//*****
//
// The video widget used to hold the decompressed JPEG image and
// display it. This is a simple JPEG canvas which will decompress and display
// the image contained in buffer g_pucJPEGImage in a 320x215 pixel control with
// a single pixel white outline. Style flag JW_STYLE_SCROLL enables automatic
// redraw based on user touchscreen scrolling. This is rather CPU intensive
// so it may be better to remove this style flag and install an OnScroll
// callback that can be used to pace the redraws. This is demonstrated in the
// showjpeg example application.
//
//*****
#define VID_LEFT      0
#define VID_TOP       0
#define VID_WIDTH     320
#define VID_HEIGHT    240
VideoWidget(g_sBackground, WIDGET_ROOT, 0, 0,
            &g_sKitronix320x240x16_FPGA, VID_LEFT, VID_TOP, VID_WIDTH, VID_HEIGHT,
            VW_STYLE_VGA, ClrMagenta, 0, 0, 0, &g_sVideoInst);

...

int
main(void)
{
    //
    // Set the system clock to run at 50MHz from the PLL
    //
    SysCtlClockSet(SYSCTL_SYSDIV_4 | SYSCTL_USE_PLL | SYSCTL_OSC_MAIN |
                   SYSCTL_XTAL_16MHZ);

    //
    // Set the device pinout appropriately for this board.
    //
    PinoutSet();

    //
    // Make sure we detected the FPGA daughter board since this application
    // requires it.
    //
    if(g_eDaughterType != DAUGHTER_FPGA)
    {
        //
        // We can't run - the FPGA daughter board doesn't seem to be there.
        //
        while(1)
        {
            //
            // Hang here on error.
            //
        }
    }

    //
    // Enable Interrupts
    //
    IntMasterEnable();
}
```



```
//
// Initialize the video capture widget. This must be done before the
// display driver is initialized.
//
VideoWidgetCameraInit(&g_sBackground, VIDEO_BUFF_BASE);

//
// Initialize the display driver.
//
Kitronix320x240x16_FPGAInit(GRAPHICS_BUFF_BASE);

//
// Enable the display backlight.
//
Kitronix320x240x16_FGABacklight(true);

//
// Initialize the touch screen driver.
//
TouchScreenInit();

//
// Set the touch screen event handler.
//
TouchScreenCallbackSet(WidgetPointerMessage);

//
// Add the compile-time defined widgets to the widget tree.
//
WidgetAdd(WIDGET_ROOT, (tWidget *)&g_sBackground);

//
// Paint the widget tree to make sure they all appear on the display.
//
WidgetPaint(WIDGET_ROOT);

//
// Now that everything is set up, turn on the video display.
//
VideoWidgetBlankSet((tWidget *)&g_sBackground, false);

//
// Enter an infinite loop for reading and processing touchscreen input
// from the user.
//
while(1)
{
    WidgetMessageQueueProcess();
}
}
```


16 Command Line Processing Module

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16.1 Introduction

The command line processor allows a simple command line interface to be made available in an application, for example via a UART. It takes a buffer containing a string (which must be obtained by the application) and breaks it up into a command and arguments (in traditional C “argc, argv” format). The command is then found in a command table and the corresponding function in the table is called to process the command.

This module is contained in `utils/cmdline.c`, with `utils/cmdline.h` containing the API definitions for use by applications.

16.2 API Functions

Data Structures

- `tCmdLineEntry`

Defines

- `CMDLINE_BAD_CMD`
- `CMDLINE_TOO_MANY_ARGS`

Functions

- `int CmdLineProcess (char *pcCmdLine)`

Variables

- `tCmdLineEntry g_sCmdTable[]`

16.2.1 Data Structure Documentation

16.2.1.1 tCmdLineEntry

Definition:

```
typedef struct
{
    const char *pcCmd;
    pfnCmdLine pfnCmd;
    const char *pcHelp;
}
tCmdLineEntry
```

Members:

pcCmd A pointer to a string containing the name of the command.

pfnCmd A function pointer to the implementation of the command.

pcHelp A pointer to a string of brief help text for the command.

Description:

Structure for an entry in the command list table.

16.2.2 Define Documentation

16.2.2.1 CMDLINE_BAD_CMD

Definition:

```
#define CMDLINE_BAD_CMD
```

Description:

Defines the value that is returned if the command is not found.

16.2.2.2 CMDLINE_TOO_MANY_ARGS

Definition:

```
#define CMDLINE_TOO_MANY_ARGS
```

Description:

Defines the value that is returned if there are too many arguments.

16.2.3 Function Documentation

16.2.3.1 CmdLineProcess

Process a command line string into arguments and execute the command.

Prototype:

```
int
CmdLineProcess(char *pcCmdLine)
```

Parameters:

pcCmdLine points to a string that contains a command line that was obtained by an application by some means.

Description:

This function will take the supplied command line string and break it up into individual arguments. The first argument is treated as a command and is searched for in the command table. If the command is found, then the command function is called and all of the command line arguments are passed in the normal argc, argv form.

The command table is contained in an array named `g_sCmdTable` which must be provided by the application.

Returns:

Returns **CMDLINE_BAD_CMD** if the command is not found, **CMDLINE_TOO_MANY_ARGS** if there are more arguments than can be parsed. Otherwise it returns the code that was returned by the command function.

16.2.4 Variable Documentation

16.2.4.1 g_sCmdTable

Definition:

```
tCmdLineEntry g_sCmdTable[ ]
```

Description:

This is the command table that must be provided by the application.

16.3 Programming Example

The following example shows how to process a command line.

```
//
// Code for the "foo" command.
//
int
ProcessFoo(int argc, char *argv[])
{
    //
    // Do something, using argc and argv if the command takes arguments.
    //
}

//
// Code for the "bar" command.
//
int
ProcessBar(int argc, char *argv[])
{
    //
    // Do something, using argc and argv if the command takes arguments.
    //
}
```

```
//
// Code for the "help" command.
//
int
ProcessHelp(int argc, char *argv[])
{
    //
    // Provide help.
    //
}

//
// The table of commands supported by this application.
//
tCmdLineEntry g_sCmdTable[] =
{
    { "foo", ProcessFoo, "The first command." },
    { "bar", ProcessBar, "The second command." },
    { "help", ProcessHelp, "Application help." }
};

//
// Read a process a command.
//
int
Test(void)
{
    unsigned char pucCmd[256];

    //
    // Retrieve a command from the user into pucCmd.
    //
    ...

    //
    // Process the command line.
    //
    return(CmdLineProcess(pucCmd));
}
```

17 CPU Usage Module

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17.1 Introduction

The CPU utilization module uses one of the system timers and peripheral clock gating to determine the percentage of the time that the processor is being clocked. For the most part, the processor is executing code whenever it is being clocked (exceptions occur when the clocking is being configured, which only happens at startup, and when entering/exiting an interrupt handler, when the processor is performing stacking operations on behalf of the application).

The specified timer is configured to run when the processor is in run mode and to not run when the processor is in sleep mode. Therefore, the timer will only count when the processor is being clocked. Comparing the number of clocks the timer counted during a fixed period to the number of clocks in the fixed period provides the percentage utilization.

In order for this to be effective, the application must put the processor to sleep when it has no work to do (instead of busy waiting). If the processor never goes to sleep (either because of a continual stream of work to do or a busy loop), the processor utilization will be reported as 100%.

Since deep-sleep mode changes the clocking of the system, the computed processor usage may be incorrect if deep-sleep mode is utilized. The number of clocks the processor spends in run mode will be properly counted, but the timing period may not be accurate (unless extraordinary measures are taken to ensure timing period accuracy).

The accuracy of the computed CPU utilization depends upon the regularity with which [CPUUsageTick\(\)](#) is called by the application. If the CPU usage is constant, but [CPUUsageTick\(\)](#) is called sporadically, the reported CPU usage will fluctuate as well despite the fact that the CPU usage is actually constant.

This module is contained in `utils/cpu_usage.c`, with `utils/cpu_usage.h` containing the API definitions for use by applications.

17.2 API Functions

Functions

- void [CPUUsageInit](#) (unsigned long ulClockRate, unsigned long ulRate, unsigned long ulTimer)
- unsigned long [CPUUsageTick](#) (void)

17.2.1 Function Documentation

17.2.1.1 CPUUsageInit

Initializes the CPU usage measurement module.

Prototype:

```
void
CPUUsageInit(unsigned long ulClockRate,
              unsigned long ulRate,
              unsigned long ulTimer)
```

Parameters:

ulClockRate is the rate of the clock supplied to the timer module.

ulRate is the number of times per second that [CPUUsageTick\(\)](#) is called.

ulTimer is the index of the timer module to use.

Description:

This function prepares the CPU usage measurement module for measuring the CPU usage of the application.

Returns:

None.

17.2.1.2 CPUUsageTick

Updates the CPU usage for the new timing period.

Prototype:

```
unsigned long
CPUUsageTick(void)
```

Description:

This function, when called at the end of a timing period, will update the CPU usage.

Returns:

Returns the CPU usage percentage as a 16.16 fixed-point value.

17.3 Programming Example

The following example shows how to use the CPU usage module to measure the CPU usage where the foreground simply burns some cycles.

```
//
// The CPU usage for the most recent time period.
//
unsigned long g_ulCPUUsage;

//
// Handles the SysTick interrupt.
```



```
//
void
SysTickIntHandler(void)
{
    //
    // Compute the CPU usage for the last time period.
    //
    g_ulCPUUsage = CPUUsageTick();
}

//
// The main application.
//
int
main(void)
{
    //
    // Initialize the CPU usage module, using timer 0.
    //
    CPUUsageInit(8000000, 100, 0);

    //
    // Initialize SysTick to interrupt at 100 Hz.
    //
    SysTickPeriodSet(8000000 / 100);
    SysTickIntEnable();
    SysTickEnable();

    //
    // Loop forever.
    //
    while(1)
    {
        //
        // Delay for a little bit so that CPU usage is not zero.
        //
        SysCtlDelay(100);

        //
        // Put the processor to sleep.
        //
        SysCtlSleep();
    }
}
```


18 Flash Parameter Block Module

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18.1 Introduction

The flash parameter block module provides a simple, fault-tolerant, persistent storage mechanism for storing parameter information for an application.

The [FlashPBlockInit\(\)](#) function is used to initialize a parameter block. The primary conditions for the parameter block are that flash region used to store the parameter blocks must contain at least two erase blocks of flash to ensure fault tolerance, and the size of the parameter block must be an integral divisor of the the size of an erase block. [FlashPBlockGet\(\)](#) and [FlashPBlockSave\(\)](#) are used to read and write parameter block data into the parameter region. The only constraints on the content of the parameter block are that the first two bytes of the block are reserved for use by the read/write functions as a sequence number and checksum, respectively.

This module is contained in `utils/flash_pb.c`, with `utils/flash_pb.h` containing the API definitions for use by applications.

18.2 API Functions

Functions

- unsigned char * [FlashPBlockGet](#) (void)
- void [FlashPBlockInit](#) (unsigned long ulStart, unsigned long ulEnd, unsigned long ulSize)
- void [FlashPBlockSave](#) (unsigned char *pucBuffer)

18.2.1 Function Documentation

18.2.1.1 FlashPBlockGet

Gets the address of the most recent parameter block.

Prototype:

```
unsigned char *  
FlashPBlockGet(void)
```

Description:

This function returns the address of the most recent parameter block that is stored in flash.

Returns:

Returns the address of the most recent parameter block, or NULL if there are no valid parameter blocks in flash.

18.2.1.2 FlashPBInit

Initializes the flash parameter block.

Prototype:

```
void  
FlashPBInit(unsigned long ulStart,  
             unsigned long ulEnd,  
             unsigned long ulSize)
```

Parameters:

ulStart is the address of the flash memory to be used for storing flash parameter blocks; this must be the start of an erase block in the flash.

ulEnd is the address of the end of flash memory to be used for storing flash parameter blocks; this must be the start of an erase block in the flash (the first block that is NOT part of the flash memory to be used), or the address of the first word after the flash array if the last block of flash is to be used.

ulSize is the size of the parameter block when stored in flash; this must be a power of two less than or equal to the flash erase block size (typically 1024).

Description:

This function initializes a fault-tolerant, persistent storage mechanism for a parameter block for an application. The last several erase blocks of flash (as specified by *ulStart* and *ulEnd*) are used for the storage; more than one erase block is required in order to be fault-tolerant.

A parameter block is an array of bytes that contain the persistent parameters for the application. The only special requirement for the parameter block is that the first byte is a sequence number (explained in [FlashPBSave\(\)](#)) and the second byte is a checksum used to validate the correctness of the data (the checksum byte is the byte such that the sum of all bytes in the parameter block is zero).

The portion of flash for parameter block storage is split into N equal-sized regions, where each region is the size of a parameter block (*ulSize*). Each region is scanned to find the most recent valid parameter block. The region that has a valid checksum and has the highest sequence number (with special consideration given to wrapping back to zero) is considered to be the current parameter block.

In order to make this efficient and effective, three conditions must be met. The first is *ulStart* and *ulEnd* must be specified such that at least two erase blocks of flash are dedicated to parameter block storage. If not, fault tolerance can not be guaranteed since an erase of a single block will leave a window where there are no valid parameter blocks in flash. The second condition is that the size (*ulSize*) of the parameter block must be an integral divisor of the size of an erase block of flash. If not, a parameter block will end up spanning between two erase blocks of flash, making it more difficult to manage. The final condition is that the size of the flash dedicated to parameter blocks (*ulEnd* - *ulStart*) divided by the parameter block size (*ulSize*) must be less than or equal to 128. If not, it will not be possible in all cases to determine which parameter block is the most recent (specifically when dealing with the sequence number wrapping back to zero).

When the microcontroller is initially programmed, the flash blocks used for parameter block storage are left in an erased state.

This function must be called before any other flash parameter block functions are called.

Returns:

None.

18.2.1.3 FlashPBSave

Writes a new parameter block to flash.

Prototype:

```
void  
FlashPBSave(unsigned char *pucBuffer)
```

Parameters:

pucBuffer is the address of the parameter block to be written to flash.

Description:

This function will write a parameter block to flash. Saving the new parameter blocks involves three steps:

- Setting the sequence number such that it is one greater than the sequence number of the latest parameter block in flash.
- Computing the checksum of the parameter block.
- Writing the parameter block into the storage immediately following the latest parameter block in flash; if that storage is at the start of an erase block, that block is erased first.

By this process, there is always a valid parameter block in flash. If power is lost while writing a new parameter block, the checksum will not match and the partially written parameter block will be ignored. This is what makes this fault-tolerant.

Another benefit of this scheme is that it provides wear leveling on the flash. Since multiple parameter blocks fit into each erase block of flash, and multiple erase blocks are used for parameter block storage, it takes quite a few parameter block saves before flash is re-written.

Returns:

None.

18.3 Programming Example

The following example shows how to use the flash parameter block module to read the contents of a flash parameter block.

```
unsigned char pucBuffer[16], *pucPB;  
  
//  
// Initialize the flash parameter block module, using the last two pages of  
// a 64 KB device as the parameter block.  
//  
FlashPBInit(0xf800, 0x10000, 16);  
  
//  
// Read the current parameter block.  
//  
pucPB = FlashPBGet();  
if(pucPB)  
{  
    memcpy(pucBuffer, pucPB);  
}
```


19 File System Wrapper Module

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19.1 Introduction

The file system wrapper module allows several binary file system images and FatFs drives to be mounted simultaneously and referenced as if part of a single file system with each mount point identified by name. This is useful in applications which make use of SDCard and USB Mass Storage Class devices, allowing these to be referenced as, for example "/sdcard" and "/usb" respectively.

Mount points are defined using an array of structures, each entry of which describes a single mount and provides its name and details of the actual file system that is to be used to support that mount.

This module is contained in `utils/fswrapper.c`, with `utils/fswrapper.h` containing the API definitions for use by applications.

19.2 API Functions

Functions

- void [fs_close](#) (struct fs_file *phFile)
- tBoolean [fs_init](#) (fs_mount_data *psMountPoints, unsigned long ulNumMountPoints)
- tBoolean [fs_map_path](#) (const char *pcPath, char *pcMapped, int iLen)
- fs_file * [fs_open](#) (char *pcName)
- int [fs_read](#) (struct fs_file *phFile, char *pcBuffer, int iCount)
- void [fs_tick](#) (unsigned long ulTickMS)

19.2.1 Function Documentation

19.2.1.1 fs_close

Closes a file.

Prototype:

```
void
fs_close(struct fs_file *phFile)
```

Parameters:

phFile is the handle of the file that is to be closed. This will have been returned by an earlier call to [fs_open\(\)](#).

Description:

This function closes the file identified by *phFile* and frees all resources associated with the file handle.

Returns:

None.

19.2.1.2 fs_init

Initializes the file system wrapper.

Prototype:

```
tBoolean
fs_init(fs_mount_data *psMountPoints,
        unsigned long ulNumMountPoints)
```

Parameters:

psMountPoints points to an array of fs_mount_data structures. Each element in the array maps a top level directory name to a particular file system image or to the FAT file system and a logical drive number.

ulNumMountPoints provides the number of populated elements in the *psMountPoints* array.

Description:

This function should be called to initialize the file system wrapper and provide it with the information required to access the files in multiple file system images via a single filename space.

Each entry in *psMountPoints* describes a top level directory in the unified namespace and indicates to fswrapper where the files for that directory can be found. Each entry can describe either a file system image in system memory or a logical disk handled via the FatFs file system driver.

For example, consider the following 3 entry mount point table:

```
{{ "internal", &g_pcFSImage, 0, NULL, NULL },
{ "sdcard", NULL, 0, SDCardEnable, SDCardDisable },
{ NULL, &g_pcFSDefault, 0, NULL, NULL }}
```

Requests to open file “/internal/index.html” will be handled by attempting to open “/index.html” in the internal file system pointed to by *g_pcFSImage*. Similarly, opening “/sdcard/images/logo.gif” will result in a call to the FAT *f_open* function requesting “0:/images/logo.gif”. If a request to open “index.htm” is received, this is handled by attempting to open “index.htm” in the default internal file system image, *g_pcFSDefault*.

Returns:

Returns **true** on success or **false** on failure.

19.2.1.3 fs_map_path

Maps a path string containing mount point names to a path suitable for use in calls to the FatFs APIs.

Prototype:

```
tBoolean
fs_map_path(const char *pcPath,
            char *pcMapped,
            int iLen)
```


Parameters:

pcPath points to a string containing a path in the namespace defined by the mount information passed to `fs_init()`.

pcMapped points to a buffer into which the mapped path string will be written.

iLen is the size, in bytes, of the buffer pointed to by `pcMapped`.

Description:

This function may be used by applications which want to make use of FatFs functions which are not directly mapped by the fswrapper layer. A path in the namespace defined by the mount points passed to function `fs_init()` is translated to an equivalent path in the FatFs namespace and this may then be used in a direct call to functions such as `f_opendir()` or `f_getfree()`.

Returns:

Returns **true** on success or **false** if `fs_init()` has not been called, if the path provided maps to an internal file system image rather than a FatFs logical drive or if the buffer pointed to by `pcMapped` is too small to fit the output string.

19.2.1.4 fs_open

Opens a file.

Prototype:

```
struct fs_file *  
fs_open(char *pcName)
```

Parameters:

pcName points to a NULL terminated string containing the path and file name to open.

Description:

This function opens a file and returns a handle allowing it to be read.

Returns:

Returns a valid file handle on success or NULL on failure.

19.2.1.5 fs_read

Reads data from an open file.

Prototype:

```
int  
fs_read(struct fs_file *phFile,  
        char *pcBuffer,  
        int iCount)
```

Parameters:

phFile is the handle of the file which is to be read. This will have been returned by a previous call to `fs_open()`.

pcBuffer points to the first byte of the buffer into which the data read from the file will be copied. This buffer must be large enough to hold *iCount* bytes.

iCount is the maximum number of bytes of data that are to be read from the file.

Description:

This function reads the next block of data from the given file into a buffer and returns the number of bytes read or -1 if the end of the file has been reached.

Returns:

Returns the number of bytes read from the file or -1 if the end of the file has been reached and no more data is available.

19.2.1.6 fs_tick

Provides a periodic tick for the file system.

Prototype:

```
void  
fs_tick(unsigned long ulTickMS)
```

Parameters:

ulTickMS is the number of milliseconds which have elapsed since the last time this function was called.

Description:

Applications making use of the file system wrapper with underlying FatFs drives must call this function at least once every 10 milliseconds to provide a time reference for use by the file system. It is typically called in the context of the application's SysTick interrupt handler or from the handler of some other timer interrupt.

If only binary file system images are in use, this function need not be called.

Returns:

None

19.3 Programming Example

The following example shows how to set up the file system wrapper with two mount points, one for a flash-based file system image and the other for a FAT file system on an SDCard.

```
//*****  
//  
// This array describes the various file system mount points. These are passed  
// to the fswrapper module which allows us to use helpful paths to access the  
// various file systems installed via a single namespace.  
//  
// FS_ROOT is a pointer to a binary file system image (as can be generated by  
// the makefsfile utility) located in system flash.  
//  
//*****  
static fs_mount_data g_psMountData[] =  
{  
    {"sdcard",    0,        0, 0, 0}, // SDCard - FAT logical drive 0  
    {"usb",       0,        1, 0, 0}, // USB flash stick - FAT logical drive 1  
    {NULL,        (unsigned char *)FS_ROOT, 0, 0, 0} // Default root directory  
};  
  
void AccessFile(void)
```

```
{
    struct fs_file *fhSDCard;
    struct fs_file *fhFlash;

    //
    // Initialize the various file systems we will be using.
    //
    fs_init(g_psMountData, 3);

    //
    // Open a file on the SDCard
    //
    fhSDCard = fs_open("/sdcard/index.htm");

    //
    // Open a file in the flash file system image. The default mount point
    // identified by the "NULL" name pointer in g_psMountData is used if the
    // first directory in the path does not match any other mount point in the
    // table.
    //
    fhFlash = fs_open("/images/logo.gif");

    //
    // Do something useful with the files here.
    //

    //
    // Close our files.
    //
    fs_close(fhFlash);
    fs_close(fhSDCard);
}
```


20 Integer Square Root Module

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20.1 Introduction

The integer square root module provides an integer version of the square root operation that can be used instead of the floating point version provided in the C library. The algorithm used is a derivative of the manual pencil-and-paper method that used to be taught in school, and is closely related to the pencil-and-paper division method that is likely still taught in school.

For full details of the algorithm, see the article by Jack W. Crenshaw in the February 1998 issue of Embedded System Programming. It can be found online at <http://www.embedded.com/98/9802fe2.htm>.

This module is contained in `utils/isqrt.c`, with `utils/isqrt.h` containing the API definitions for use by applications.

20.2 API Functions

Functions

- unsigned long `isqrt` (unsigned long `ulValue`)

20.2.1 Function Documentation

20.2.1.1 `isqrt`

Compute the integer square root of an integer.

Prototype:

```
unsigned long  
isqrt(unsigned long ulValue)
```

Parameters:

ulValue is the value whose square root is desired.

Description:

This function will compute the integer square root of the given input value. Since the value returned is also an integer, it is actually better defined as the largest integer whose square is less than or equal to the input value.

Returns:

Returns the square root of the input value.

20.3 Programming Example

The following example shows how to compute the square root of a number.

```
unsigned long ulValue;  
  
//  
// Get the square root of 52378. The result returned will be 228, which is  
// the largest integer less than or equal to the square root of 52378.  
//  
ulValue = isqrt(52378);
```

21 Ethernet Board Locator Module

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21.1 Introduction

The locator module offers a simple way to add Ethernet board locator capability to an application which is using the lwIP TCP/IP stack. Applications running the locator service will be detected by the `finder` application which can be found in the `tools` directory of the StellarisWare installation.

APIs offered by the locator module allow an application to set various fields which are communicated to the `finder` application when it enumerates Stellaris boards on the network. These fields include an application-specified name, the MAC address of the board, the board ID and the client IP address.

This module is contained in `utils/locator.c`, with `utils/locator.h` containing the API definitions for use by applications.

21.2 API Functions

Functions

- void [LocatorAppTitleSet](#) (const char *pcAppTitle)
- void [LocatorBoardIDSet](#) (unsigned long ulID)
- void [LocatorBoardTypeSet](#) (unsigned long ulType)
- void [LocatorClientIPSet](#) (unsigned long ulIP)
- void [LocatorInit](#) (void)
- void [LocatorMACAddrSet](#) (unsigned char *pucMACArray)
- void [LocatorVersionSet](#) (unsigned long ulVersion)

21.2.1 Function Documentation

21.2.1.1 LocatorAppTitleSet

Sets the application title in the locator response packet.

Prototype:

```
void  
LocatorAppTitleSet(const char *pcAppTitle)
```

Parameters:

pcAppTitle is a pointer to the application title string.

Description:

This function sets the application title in the locator response packet. The string is truncated at 64 characters if it is longer (without a terminating 0), and is zero-filled to 64 characters if it is shorter.

Returns:

None.

21.2.1.2 LocatorBoardIDSet

Sets the board ID in the locator response packet.

Prototype:

```
void  
LocatorBoardIDSet(unsigned long ulID)
```

Parameters:

ulID is the ID of the board.

Description:

This function sets the board ID field in the locator response packet.

Returns:

None.

21.2.1.3 LocatorBoardTypeSet

Sets the board type in the locator response packet.

Prototype:

```
void  
LocatorBoardTypeSet(unsigned long ulType)
```

Parameters:

ulType is the type of the board.

Description:

This function sets the board type field in the locator response packet.

Returns:

None.

21.2.1.4 LocatorClientIPSet

Sets the client IP address in the locator response packet.

Prototype:

```
void  
LocatorClientIPSet(unsigned long ulIP)
```


Parameters:

uIP is the IP address of the currently connected client.

Description:

This function sets the IP address of the currently connected client in the locator response packet. The IP should be set to 0.0.0.0 if there is no client connected. It should never be set for devices that do not have a strict one-to-one mapping of client to server (for example, a web server).

Returns:

None.

21.2.1.5 LocatorInit

Initializes the locator service.

Prototype:

```
void  
LocatorInit(void)
```

Description:

This function prepares the locator service to handle device discovery requests. A UDP server is created and the locator response data is initialized to all empty.

Returns:

None.

21.2.1.6 LocatorMACAddrSet

Sets the MAC address in the locator response packet.

Prototype:

```
void  
LocatorMACAddrSet(unsigned char *pucMACArray)
```

Parameters:

pucMACArray is the MAC address of the network interface.

Description:

This function sets the MAC address of the network interface in the locator response packet.

Returns:

None.

21.2.1.7 LocatorVersionSet

Sets the firmware version in the locator response packet.

Prototype:

```
void  
LocatorVersionSet(unsigned long ulVersion)
```

Parameters:

ulVersion is the version number of the device firmware.

Description:

This function sets the version number of the device firmware in the locator response packet.

Returns:

None.

21.3 Programming Example

The following example shows how to set up the board locator service in an application which uses Ethernet and the lwIP TCP/IP stack.

```
//  
// Initialize the lwIP TCP/IP stack.  
//  
lwIPInit(pucMACAddr, 0, 0, 0, IPADDR_USE_DHCP);  
  
//  
// Setup the device locator service.  
//  
LocatorInit();  
LocatorMACAddrSet(pucMACAddr);  
LocatorAppTitleSet("Your application name");
```

22 lwIP Wrapper Module

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22.1 Introduction

The lwIP wrapper module provides a simple abstraction layer for the lwIP version 1.3.2 TCP/IP stack. The configuration of the TCP/IP stack is based on the options defined in the `lwipopts.h` file provided by the application.

The `lwIPInit()` function is used to initialize the lwIP TCP/IP stack. The `lwIPEthernetIntHandler()` is the interrupt handler function for use with the lwIP TCP/IP stack. This handler will process transmit and receive packets. If no RTOS is being used, the interrupt handler will also service the lwIP timers. The `lwIPTimer()` function is to be called periodically to support the TCP, ARP, DHCP and other timers used by the lwIP TCP/IP stack. If no RTOS is being used, this timer function will simply trigger an Ethernet interrupt to allow the interrupt handler to service the timers.

This module is contained in `utils/lwiplib.c`, with `utils/lwiplib.h` containing the API definitions for use by applications.

22.2 API Functions

Functions

- void `lwIPEthernetIntHandler` (void)
- void `lwIPInit` (const unsigned char *pucMAC, unsigned long ulIPAddr, unsigned long ulNetMask, unsigned long ulGWAddr, unsigned long ulIPMode)
- unsigned long `lwIPLocalGWAddrGet` (void)
- unsigned long `lwIPLocalIPAddrGet` (void)
- void `lwIPLocalMACGet` (unsigned char *pucMAC)
- unsigned long `lwIPLocalNetMaskGet` (void)
- void `lwIPNetworkConfigChange` (unsigned long ulIPAddr, unsigned long ulNetMask, unsigned long ulGWAddr, unsigned long ulIPMode)

22.2.1 Function Documentation

22.2.1.1 lwIPEthernetIntHandler

Handles Ethernet interrupts for the lwIP TCP/IP stack.

Prototype:

```
void
lwIPEthernetIntHandler(void)
```

Description:

This function handles Ethernet interrupts for the lwIP TCP/IP stack. At the lowest level, all receive packets are placed into a packet queue for processing at a higher level. Also, the transmit packet queue is checked and packets are drained and transmitted through the Ethernet MAC as needed. If the system is configured without an RTOS, additional processing is performed at the interrupt level. The packet queues are processed by the lwIP TCP/IP code, and lwIP periodic timers are serviced (as needed).

Returns:

None.

22.2.1.2 lwIPInit

Initializes the lwIP TCP/IP stack.

Prototype:

```
void  
lwIPInit(const unsigned char *pucMAC,  
          unsigned long ulIPAddr,  
          unsigned long ulNetMask,  
          unsigned long ulGWAddr,  
          unsigned long ulIPMode)
```

Parameters:

pucMAC is a pointer to a six byte array containing the MAC address to be used for the interface.

ulIPAddr is the IP address to be used (static).

ulNetMask is the network mask to be used (static).

ulGWAddr is the Gateway address to be used (static).

ulIPMode is the IP Address Mode. **IPADDR_USE_STATIC** will force static IP addressing to be used, **IPADDR_USE_DHCP** will force DHCP with fallback to Link Local (Auto IP), while **IPADDR_USE_AUTOIP** will force Link Local only.

Description:

This function performs initialization of the lwIP TCP/IP stack for the Stellaris Ethernet MAC, including DHCP and/or AutoIP, as configured.

Returns:

None.

22.2.1.3 lwIPLocalGWAddrGet

Returns the gateway address for this interface.

Prototype:

```
unsigned long  
lwIPLocalGWAddrGet(void)
```

Description:

This function will read and return the currently assigned gateway address for the Stellaris Ethernet interface.

Returns:

the assigned gateway address for this interface.

22.2.1.4 lwIPLocalIPAddrGet

Returns the IP address for this interface.

Prototype:

```
unsigned long  
lwIPLocalIPAddrGet(void)
```

Description:

This function will read and return the currently assigned IP address for the Stellaris Ethernet interface.

Returns:

Returns the assigned IP address for this interface.

22.2.1.5 lwIPLocalMACGet

Returns the local MAC/HW address for this interface.

Prototype:

```
void  
lwIPLocalMACGet(unsigned char *pucMAC)
```

Parameters:

pucMAC is a pointer to an array of bytes used to store the MAC address.

Description:

This function will read the currently assigned MAC address into the array passed in *pucMAC*.

Returns:

None.

22.2.1.6 lwIPLocalNetMaskGet

Returns the network mask for this interface.

Prototype:

```
unsigned long  
lwIPLocalNetMaskGet(void)
```

Description:

This function will read and return the currently assigned network mask for the Stellaris Ethernet interface.

Returns:

the assigned network mask for this interface.

22.2.1.7 lwIPNetworkConfigChange

Change the configuration of the lwIP network interface.

Prototype:

```
void  
lwIPNetworkConfigChange(unsigned long ulIPAddr,  
                        unsigned long ulNetMask,  
                        unsigned long ulGWAddr,  
                        unsigned long ulIPMode)
```

Parameters:

ulIPAddr is the new IP address to be used (static).

ulNetMask is the new network mask to be used (static).

ulGWAddr is the new Gateway address to be used (static).

ulIPMode is the IP Address Mode. **IPADDR_USE_STATIC** 0 will force static IP addressing to be used, **IPADDR_USE_DHCP** will force DHCP with fallback to Link Local (Auto IP), while **IPADDR_USE_AUTOIP** will force Link Local only.

Description:

This function will evaluate the new configuration data. If necessary, the interface will be brought down, reconfigured, and then brought back up with the new configuration.

Returns:

None.

22.3 Programming Example

The following example shows how to use the lwIP wrapper module to initialize the lwIP stack.

```
unsigned char pucMACArray[6];  
  
//  
// Fill in the MAC array and initialize the lwIP library using DHCP.  
//  
lwIPInit(pucMACArray, 0, 0, 0, IPADDR_USE_DHCP);  
  
//  
// Periodically call the lwIP timer tick. In a real application, this  
// would use a timer interrupt instead of an endless loop.  
//  
while(1)  
{  
    SysCtlDelay(1000);  
    lwIPTimer(1);  
}
```

23 PTPd Wrapper Module

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23.1 Introduction

The PTPd wrapper module provides a simple way to include the open-source PTPd library in an application. Because the PTPd library has compile-time options that may vary from one application to the next, it is not practical to provide this library in object format. By including the `ptpdlib.c` module in your application's project and/or make file, the library can be included at compile-time with a single reference.

The PTPd library provides IEEE Precision Time Protocol (1588) ported to the Stellaris family of Ethernet-enabled devices. This port uses lwIP as the underlying TCP/IP stack. Refer to the `enet_ptpd` sample application for the EK-6965 and EK-8962 Evaluation Kits for additional details.

This module is contained in `utils/ptpdlib.c`, with `utils/ptpdlib.h` containing the API definitions for use by applications.

23.2 API Functions

23.3 Programming Example

```
//
// Clear out all of the run time options and protocol stack options.
//
memset(&g_sRtOpts, 0, sizeof(g_sRtOpts));
memset(&g_sPTPClock, 0, sizeof(g_sPTPClock));

//
// Initialize all PTPd Run Time and Clock Options.
// Note: This code will be specific to your application
//
...

//
// Run the protocol engine for the first time to initialize the state
// machines.
//
protocol_first(&g_sRtOpts, &g_sPTPClock);

...

//
// Main Loop
//
while(1)
{
    ...
}
```

```
    //  
    // Run the protocol engine for each pass through the main process loop.  
    //  
    protocol_loop(&g_sRtOpts, &g_sPTPClock);  
  
    ...  
}
```


24 Ring Buffer Module

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24.1 Introduction

The ring buffer module provides a set of functions allowing management of a block of memory as a ring buffer. This is typically used in buffering transmit or receive data for a communication channel but has many other uses including implementing queues and FIFOs.

This module is contained in `utils/ringbuf.c`, with `utils/ringbuf.h` containing the API definitions for use by applications.

24.2 API Functions

Functions

- void [RingBufAdvanceRead](#) (tRingBufObject *ptRingBuf, unsigned long ulNumBytes)
- void [RingBufAdvanceWrite](#) (tRingBufObject *ptRingBuf, unsigned long ulNumBytes)
- unsigned long [RingBufContigFree](#) (tRingBufObject *ptRingBuf)
- unsigned long [RingBufContigUsed](#) (tRingBufObject *ptRingBuf)
- tBoolean [RingBufEmpty](#) (tRingBufObject *ptRingBuf)
- void [RingBufFlush](#) (tRingBufObject *ptRingBuf)
- unsigned long [RingBufFree](#) (tRingBufObject *ptRingBuf)
- tBoolean [RingBufFull](#) (tRingBufObject *ptRingBuf)
- void [RingBufInit](#) (tRingBufObject *ptRingBuf, unsigned char *pucBuf, unsigned long ulSize)
- void [RingBufRead](#) (tRingBufObject *ptRingBuf, unsigned char *pucData, unsigned long ulLength)
- unsigned char [RingBufReadOne](#) (tRingBufObject *ptRingBuf)
- unsigned long [RingBufSize](#) (tRingBufObject *ptRingBuf)
- unsigned long [RingBufUsed](#) (tRingBufObject *ptRingBuf)
- void [RingBufWrite](#) (tRingBufObject *ptRingBuf, unsigned char *pucData, unsigned long ulLength)
- void [RingBufWriteOne](#) (tRingBufObject *ptRingBuf, unsigned char ucData)

24.2.1 Function Documentation

24.2.1.1 RingBufAdvanceRead

Remove bytes from the ring buffer by advancing the read index.

Prototype:

```
void  
RingBufAdvanceRead(tRingBufObject *ptRingBuf,  
                   unsigned long ulNumBytes)
```

Parameters:

ptRingBuf points to the ring buffer from which bytes are to be removed.
ulNumBytes is the number of bytes to be removed from the buffer.

Description:

This function advances the ring buffer read index by a given number of bytes, removing that number of bytes of data from the buffer. If *ulNumBytes* is larger than the number of bytes currently in the buffer, the buffer is emptied.

Returns:

None.

24.2.1.2 RingBufAdvanceWrite

Add bytes to the ring buffer by advancing the write index.

Prototype:

```
void  
RingBufAdvanceWrite(tRingBufObject *ptRingBuf,  
                   unsigned long ulNumBytes)
```

Parameters:

ptRingBuf points to the ring buffer to which bytes have been added.
ulNumBytes is the number of bytes added to the buffer.

Description:

This function should be used by clients who wish to add data to the buffer directly rather than via calls to [RingBufWrite\(\)](#) or [RingBufWriteOne\(\)](#). It advances the write index by a given number of bytes. If the *ulNumBytes* parameter is larger than the amount of free space in the buffer, the read pointer will be advanced to cater for the addition. Note that this will result in some of the oldest data in the buffer being discarded.

Returns:

None.

24.2.1.3 RingBufContigFree

Returns number of contiguous free bytes available in a ring buffer.

Prototype:

```
unsigned long  
RingBufContigFree(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to check.

Description:

This function returns the number of contiguous free bytes ahead of the current write pointer in the ring buffer.

Returns:

Returns the number of contiguous bytes available in the ring buffer.

24.2.1.4 RingBufContigUsed

Returns number of contiguous bytes of data stored in ring buffer ahead of the current read pointer.

Prototype:

```
unsigned long  
RingBufContigUsed(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to check.

Description:

This function returns the number of contiguous bytes of data available in the ring buffer ahead of the current read pointer. This represents the largest block of data which does not straddle the buffer wrap.

Returns:

Returns the number of contiguous bytes available.

24.2.1.5 RingBufEmpty

Determines whether the ring buffer whose pointers and size are provided is empty or not.

Prototype:

```
tBoolean  
RingBufEmpty(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to empty.

Description:

This function is used to determine whether or not a given ring buffer is empty. The structure is specifically to ensure that we do not see warnings from the compiler related to the order of volatile accesses being undefined.

Returns:

Returns **true** if the buffer is empty or **false** otherwise.

24.2.1.6 RingBufFlush

Empties the ring buffer.

Prototype:

```
void  
RingBufFlush(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to empty.

Description:

Discards all data from the ring buffer.

Returns:

None.

24.2.1.7 RingBufFree

Returns number of bytes available in a ring buffer.

Prototype:

```
unsigned long  
RingBufFree(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to check.

Description:

This function returns the number of bytes available in the ring buffer.

Returns:

Returns the number of bytes available in the ring buffer.

24.2.1.8 RingBufFull

Determines whether the ring buffer whose pointers and size are provided is full or not.

Prototype:

```
tBoolean  
RingBufFull(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to empty.

Description:

This function is used to determine whether or not a given ring buffer is full. The structure is specifically to ensure that we do not see warnings from the compiler related to the order of volatile accesses being undefined.

Returns:

Returns **true** if the buffer is full or **false** otherwise.

24.2.1.9 RingBufInit

Initialize a ring buffer object.

Prototype:

```
void  
RingBufInit (tRingBufObject *ptRingBuf,  
             unsigned char *pucBuf,  
             unsigned long ulSize)
```

Parameters:

ptRingBuf points to the ring buffer to be initialized.
pucBuf points to the data buffer to be used for the ring buffer.
ulSize is the size of the buffer in bytes.

Description:

This function initializes a ring buffer object, preparing it to store data.

Returns:

None.

24.2.1.10 RingBufRead

Reads data from a ring buffer.

Prototype:

```
void  
RingBufRead (tRingBufObject *ptRingBuf,  
             unsigned char *pucData,  
             unsigned long ulLength)
```

Parameters:

ptRingBuf points to the ring buffer to be read from.
pucData points to where the data should be stored.
ulLength is the number of bytes to be read.

Description:

This function reads a sequence of bytes from a ring buffer.

Returns:

None.

24.2.1.11 RingBufReadOne

Reads a single byte of data from a ring buffer.

Prototype:

```
unsigned char  
RingBufReadOne (tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf points to the ring buffer to be written to.

Description:

This function reads a single byte of data from a ring buffer.

Returns:

The byte read from the ring buffer.

24.2.1.12 RingBufSize

Return size in bytes of a ring buffer.

Prototype:

```
unsigned long  
RingBufSize(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to check.

Description:

This function returns the size of the ring buffer.

Returns:

Returns the size in bytes of the ring buffer.

24.2.1.13 RingBufUsed

Returns number of bytes stored in ring buffer.

Prototype:

```
unsigned long  
RingBufUsed(tRingBufObject *ptRingBuf)
```

Parameters:

ptRingBuf is the ring buffer object to check.

Description:

This function returns the number of bytes stored in the ring buffer.

Returns:

Returns the number of bytes stored in the ring buffer.

24.2.1.14 RingBufWrite

Writes data to a ring buffer.

Prototype:

```
void  
RingBufWrite(tRingBufObject *ptRingBuf,  
             unsigned char *pucData,  
             unsigned long ulLength)
```

Parameters:

ptRingBuf points to the ring buffer to be written to.
pucData points to the data to be written.
ulLength is the number of bytes to be written.

Description:

This function write a sequence of bytes into a ring buffer.

Returns:

None.

24.2.1.15 RingBufWriteOne

Writes a single byte of data to a ring buffer.

Prototype:

```
void  
RingBufWriteOne(tRingBufObject *ptRingBuf,  
               unsigned char ucData)
```

Parameters:

ptRingBuf points to the ring buffer to be written to.
ucData is the byte to be written.

Description:

This function writes a single byte of data into a ring buffer.

Returns:

None.

24.3 Programming Example

The following example shows how to pass data through the ring buffer.

```
char pcBuffer[128], pcData[16];  
tRingBufObject sRingBuf;  
  
//  
// Initialize the ring buffer.  
//  
RingBufInit(&sRingBuf, pcBuffer, sizeof(pcBuffer));  
  
//  
// Write some data into the ring buffer.  
//  
RingBufWrite(&sRingBuf, "Hello World", 11);
```

```
//  
// Read the data out of the ring buffer.  
//  
RingBufRead(&sRingBuf, pData, 11);
```


25 Simple Task Scheduler Module

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25.1 Introduction

The simple task scheduler module offers an easy way to implement applications which rely upon a group of functions being called at regular time intervals. The module makes use of an application-defined task table listing functions to be called. Each task is defined by a function pointer, a parameter that will be passed to that function, the period between consecutive calls to the function and a flag indicating whether that particular task is enabled.

The scheduler makes use of the SysTick counter and interrupt to track time and calls enabled functions when the appropriate period has elapsed since the last call to that function.

In addition to providing the task table `g_psSchedulerTable[]` to the module, the application must also define a global variable `g_ulSchedulerNumTasks` containing the number of task entries in the table. The module also requires exclusive access to the SysTick hardware and the application must hook the scheduler's SysTick interrupt handler to the appropriate interrupt vector. Although the scheduler owns SysTick, functions are provided to allow the current system time to be queried and to calculate elapsed time between two system time values or between an earlier time value and the present time.

All times passed to the scheduler or returned from it are expressed in terms of system ticks. The basic system tick rate is set by the application when it initializes the scheduler module.

This module is contained in `utils/scheduler.c`, with `utils/scheduler.h` containing the API definitions for use by applications.

25.2 API Functions

Data Structures

- [tSchedulerTask](#)

Functions

- unsigned long [SchedulerElapsedTicksCalc](#) (unsigned long ulTickStart, unsigned long ulTickEnd)
- unsigned long [SchedulerElapsedTicksGet](#) (unsigned long ulTickCount)
- void [SchedulerInit](#) (unsigned long ulTicksPerSecond)
- void [SchedulerRun](#) (void)
- void [SchedulerSysTickIntHandler](#) (void)
- void [SchedulerTaskDisable](#) (unsigned long ulIndex)
- void [SchedulerTaskEnable](#) (unsigned long ulIndex, tBoolean bRunNow)

- unsigned long [SchedulerTickCountGet](#) (void)

Variables

- [tSchedulerTask](#) [g_psSchedulerTable](#)[]
- unsigned long [g_ulSchedulerNumTasks](#)

25.2.1 Data Structure Documentation

25.2.1.1 tSchedulerTask

Definition:

```
typedef struct
{
    void (*pfnFunction) (void *);
    void *pvParam;
    unsigned long ulFrequencyTicks;
    unsigned long ulLastCall;
    tBoolean bActive;
}
tSchedulerTask
```

Members:

pfnFunction A pointer to the function which is to be called periodically by the scheduler.

pvParam The parameter which is to be passed to this function when it is called.

ulFrequencyTicks The frequency the function is to be called expressed in terms of system ticks. If this value is 0, the function will be called on every call to SchedulerRun.

ulLastCall Tick count when this function was last called. This field is updated by the scheduler.

bActive A flag indicating whether or not this task is active. If true, the function will be called periodically. If false, the function is disabled and will not be called.

Description:

The structure defining a function which the scheduler will call periodically.

25.2.2 Function Documentation

25.2.2.1 SchedulerElapsedTicksCalc

Returns the number of ticks elapsed between two times.

Prototype:

```
unsigned long
SchedulerElapsedTicksCalc(unsigned long ulTickStart,
                          unsigned long ulTickEnd)
```

Parameters:

ulTickStart is the system tick count for the start of the period.

ulTickEnd is the system tick count for the end of the period.

Description:

This function may be called by a client to determine the number of ticks which have elapsed between provided starting and ending tick counts. The function takes into account wrapping cases where the end tick count is lower than the starting count assuming that the ending tick count always represents a later time than the starting count.

Returns:

The number of ticks elapsed between the provided start and end counts.

25.2.2.2 SchedulerElapsedTicksGet

Returns the number of ticks elapsed since the provided tick count.

Prototype:

```
unsigned long  
SchedulerElapsedTicksGet(unsigned long ulTickCount)
```

Parameters:

ulTickCount is the tick count from which to determine the elapsed time.

Description:

This function may be called by a client to determine how much time has passed since a particular tick count provided in the *ulTickCount* parameter. This function takes into account wrapping of the global tick counter and assumes that the provided tick count always represents a time in the past. The returned value will, of course, be wrong if the tick counter has wrapped more than once since the passed *ulTickCount*. As a result, please do not use this function if you are dealing with timeouts of 497 days or longer (assuming you use a 10mS tick period).

Returns:

The number of ticks elapsed since the provided tick count.

25.2.2.3 SchedulerInit

Initializes the task scheduler.

Prototype:

```
void  
SchedulerInit(unsigned long ulTicksPerSecond)
```

Parameters:

ulTicksPerSecond sets the basic frequency of the SysTick interrupt used by the scheduler to determine when to run the various task functions.

Description:

This function must be called during application startup to configure the SysTick timer. This is used by the scheduler module to determine when each of the functions provided in the `g_psSchedulerTable` array is called.

The caller is responsible for ensuring that [SchedulerSysTickIntHandler\(\)](#) has previously been installed in the SYSTICK vector in the vector table and must also ensure that interrupts are enabled at the CPU level.

Note that this call does not start the scheduler calling the configured functions. All function calls are made in the context of later calls to [SchedulerRun\(\)](#). This call merely configures the SysTick interrupt that is used by the scheduler to determine what the current system time is.

Returns:

None.

25.2.2.4 SchedulerRun

Instructs the scheduler to update its task table and make calls to functions needing called.

Prototype:

```
void  
SchedulerRun(void)
```

Description:

This function must be called periodically by the client to allow the scheduler to make calls to any configured task functions if it is their time to be called. The call must be made at least as frequently as the most frequent task configured in the `g_psSchedulerTable` array.

Although the scheduler makes use of the SysTick interrupt, all calls to functions configured in `g_psSchedulerTable` are made in the context of [SchedulerRun\(\)](#).

Returns:

None.

25.2.2.5 SchedulerSysTickIntHandler

Handles the SysTick interrupt on behalf of the scheduler module.

Prototype:

```
void  
SchedulerSysTickIntHandler(void)
```

Description:

Applications using the scheduler module must ensure that this function is hooked to the SysTick interrupt vector.

Returns:

None.

25.2.2.6 SchedulerTaskDisable

Disables a task and prevents the scheduler from calling it.

Prototype:

```
void  
SchedulerTaskDisable(unsigned long ulIndex)
```

Parameters:

ulIndex is the index of the task which is to be disabled in the global *g_psSchedulerTable* array.

Description:

This function marks one of the configured tasks as inactive and prevents [SchedulerRun\(\)](#) from calling it. The task may be reenabled by calling [SchedulerTaskEnable\(\)](#).

Returns:

None.

25.2.2.7 SchedulerTaskEnable

Enables a task and allows the scheduler to call it periodically.

Prototype:

```
void  
SchedulerTaskEnable(unsigned long ulIndex,  
                    tBoolean bRunNow)
```

Parameters:

ulIndex is the index of the task which is to be enabled in the global *g_psSchedulerTable* array.

bRunNow is **true** if the task is to be run on the next call to [SchedulerRun\(\)](#) or **false** if one whole period is to elapse before the task is run.

Description:

This function marks one of the configured tasks as enabled and causes [SchedulerRun\(\)](#) to call that task periodically. The caller may choose to have the enabled task run for the first time on the next call to [SchedulerRun\(\)](#) or to wait one full task period before making the first call.

Returns:

None.

25.2.2.8 SchedulerTickCountGet

Returns the current system time in ticks since power on.

Prototype:

```
unsigned long  
SchedulerTickCountGet(void)
```

Description:

This function may be called by a client to retrieve the current system time. The value returned is a count of ticks elapsed since the system last booted.

Returns:

Tick count since last boot.

25.2.3 Variable Documentation

25.2.3.1 g_psSchedulerTable

Definition:

```
tSchedulerTask g_psSchedulerTable[ ]
```

Description:

This global table must be populated by the client and contains information on each function that the scheduler is to call.

25.2.3.2 g_ulSchedulerNumTasks

Definition:

```
unsigned long g_ulSchedulerNumTasks
```

Description:

This global variable must be exported by the client. It must contain the number of entries in the g_psSchedulerTable array.

25.3 Programming Example

The following example shows how to use the task scheduler module. This code illustrates a simple application which toggles two LEDs at different rates and updates a scrolling text string on the display.

```
//*****  
//  
// Definition of the system tick rate. This results in a tick period of 10mS.  
//  
//*****  
#define TICKS_PER_SECOND 100  
  
//*****  
//  
// Prototypes of functions which will be called by the scheduler.  
//  
//*****  
static void ScrollTextBanner(void *pvParam);  
static void ToggleLED(void *pvParam);  
  
//*****  
//  
// This table defines all the tasks that the scheduler is to run, the periods  
// between calls to those tasks, and the parameter to pass to the task.  
//  
//*****  
tSchedulerTask g_psSchedulerTable[] =  
{  
    //  
    // Scroll the text banner 1 character to the left. This function is called  
    // every 20 ticks (5 times per second).  
    //  
    { ScrollTextBanner, (void *)0, 20, 0, true},  
}
```

```

//
// Toggle LED number 0 every 50 ticks (twice per second).
//
{ ToggleLED, (void *)0, 50, 0, true},

//
// Toggle LED number 1 every 100 ticks (once per second).
//
{ ToggleLED, (void *)1, 100, 0, true},
};

//*****
//
// The number of entries in the global scheduler task table.
//
//*****
unsigned long g_ulSchedulerNumTasks = (sizeof(g_psSchedulerTable) /
                                       sizeof(tSchedulerTask));

//*****
//
// This function is called by the scheduler to toggle one of two LEDs
//
//*****
static void
ToggleLED(void *pvParam)
{
    long lState;

    ulState = GPIOPinRead(LED_GPIO_BASE
                          (pvParam ? LED1_GPIO_PIN : LED0_GPIO_PIN));
    GPIOPinWrite(LED_GPIO_BASE, (pvParam ? LED1_GPIO_PIN : LED0_GPIO_PIN),
                 ~lState);
}

//*****
//
// This function is called by the scheduler to scroll a line of text on the
// display.
//
//*****
static void
ScrollTextBanner(void *pvParam)
{
    //
    // Left as an exercise for the reader.
    //
}

//*****
//
// Application main task.
//
//*****
int
main(void)
{
    //
    // Initialize system clock and any peripherals that are to be used.
    //
    SystemInit();

    //
    // Initialize the task scheduler and configure the SysTick to interrupt
    // 100 times per second.

```

```
//
SchedulerInit(TICKS_PER_SECOND);

//
// Turn on interrupts at the CPU level.
//
IntMasterEnable();

//
// Drop into the main loop.
//
while(1)
{
    //
    // Tell the scheduler to call any periodic tasks that are due to be
    // called.
    //
    SchedulerRun();
}
}
```


26 Sine Calculation Module

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26.1 Introduction

This module provides a fixed-point sine function. The input angle is a 0.32 fixed-point value that is the percentage of 360 degrees. This has two benefits; the sine function does not have to handle angles that are outside the range of 0 degrees through 360 degrees (in fact, 360 degrees can not be represented since it would wrap to 0 degrees), and the computation of the angle can be simplified since it does not have to deal with wrapping at values that are not natural for binary arithmetic (such as 360 degrees or 2π radians).

A sine table is used to find the approximate value for a given input angle. The table contains 128 entries that range from 0 degrees through 90 degrees and the symmetry of the sine function is used to determine the value between 90 degrees and 360 degrees. The maximum error caused by this table-based approach is 0.00618, which occurs near 0 and 180 degrees.

This module is contained in `utils/sine.c`, with `utils/sine.h` containing the API definitions for use by applications.

26.2 API Functions

Defines

- `cosine`(ulAngle)

Functions

- long `sine` (unsigned long ulAngle)

26.2.1 Define Documentation

26.2.1.1 cosine

Computes an approximation of the cosine of the input angle.

Definition:

```
#define cosine(ulAngle)
```

Parameters:

ulAngle is an angle expressed as a 0.32 fixed-point value that is the percentage of the way around a circle.

Description:

This function computes the cosine for the given input angle. The angle is specified in 0.32 fixed point format, and is therefore always between 0 and 360 degrees, inclusive of 0 and exclusive of 360.

Returns:

Returns the cosine of the angle, in 16.16 fixed point format.

26.2.2 Function Documentation

26.2.2.1 sine

Computes an approximation of the sine of the input angle.

Prototype:

```
long  
sine(unsigned long ulAngle)
```

Parameters:

ulAngle is an angle expressed as a 0.32 fixed-point value that is the percentage of the way around a circle.

Description:

This function computes the sine for the given input angle. The angle is specified in 0.32 fixed point format, and is therefore always between 0 and 360 degrees, inclusive of 0 and exclusive of 360.

Returns:

Returns the sine of the angle, in 16.16 fixed point format.

26.3 Programming Example

The following example shows how to produce a sine wave with 7 degrees between successive values.

```
unsigned long ulValue;  
  
//  
// Produce a sine wave with each step being 7 degrees advanced from the  
// previous.  
//  
for(ulValue = 0; ; ulValue += 0x04FA4FA4)  
{  
    //  
    // Compute the sine at this angle and do something with the result.  
    //  
    sine(ulValue);  
}
```

27 Software I2C Module

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27.1 Introduction

The software I2C module uses a timer and two GPIO pins to create a slow-speed software I2C peripheral. Multiple software I2C modules can be in use simultaneously, limited only by the availability of resources (RAM for the per-instance data structure, pins for the interface, timers if unique clock rates are required, and processor cycles to execute the code). The software I2C module supports master mode only; multi-master support is not provided. A callback mechanism is used to simulate the interrupts that would be provided by a hardware I2C module.

The API for the software I2C module has been constructed to be as close as possible to the API provided in the Stellaris Peripheral Driver Library for the hardware I2C module. The two notable differences are the function prefix being “SoftI2C” instead of “I2CMaster”, and the first argument of each API is a pointer to the [tSoftI2C](#) data structure instead of the base address of the hardware module.

Timing for the software I2C module is provided by the application. The [SoftI2CTimerTick\(\)](#) function must be called on a periodic basis to provide the timing for the software I2C module. The timer tick function must be called at four times the desired I2C clock rate; for example, to operate the software I2C interface at 10 KHz, the tick function must be called at a 40 KHz rate. By having the application providing the timing, the timer resource can be flexible and multiple software I2C modules can be driven from a single timer resource. Alternatively, if the software I2C module is only needed for brief periods of time and processor usage is not a concern, the timer tick function can simply be called in a loop until the entire I2C transaction has completed (maximizing both I2C clock speed and processor usage, but not requiring a timer).

The software I2C module requires two GPIO pins; one for SCL and one for SDA. The per-instance data structure is approximately 20 bytes in length (the actual length depends on how the structure is packed by the compiler).

As a point of reference, the following are some rough measurements of the processor usage of the software I2C module at various I2C clock speeds with the processor running at 50 MHz. Actual processor usage may vary, depending on how the application uses the software I2C module, processor clock speed, interrupt priority, and compiler.

I2C Clock	% Of Processor	Million Cycles Per Second
5 KHz	4.53	2.26
10 KHz	9.05	4.52
15 KHz	13.53	6.76
20 KHz	18.03	9.01
25 KHz	22.51	11.25
30 KHz	27.05	13.52
35 KHz	31.52	15.76
40 KHz	36.06	18.03
45 KHz	40.54	20.27
50 KHz	44.96	22.48

This module is contained in `utils/softi2c.c`, with `utils/softi2c.h` containing the API definitions for use by applications.

27.2 API Functions

Data Structures

- [tSoftI2C](#)

Functions

- `tBoolean SoftI2CBusy (tSoftI2C *pI2C)`
- `void SoftI2CCallbackSet (tSoftI2C *pI2C, void (*pfnCallback)(void))`
- `void SoftI2CControl (tSoftI2C *pI2C, unsigned long ulCmd)`
- `unsigned long SoftI2CDataGet (tSoftI2C *pI2C)`
- `void SoftI2CDataPut (tSoftI2C *pI2C, unsigned char ucData)`
- `unsigned long SoftI2CErr (tSoftI2C *pI2C)`
- `void SoftI2CInit (tSoftI2C *pI2C)`
- `void SoftI2CIntClear (tSoftI2C *pI2C)`
- `void SoftI2CIntDisable (tSoftI2C *pI2C)`
- `void SoftI2CIntEnable (tSoftI2C *pI2C)`
- `tBoolean SoftI2CIntStatus (tSoftI2C *pI2C, tBoolean bMasked)`
- `void SoftI2CSCLGPIOSet (tSoftI2C *pI2C, unsigned long ulBase, unsigned char ucPin)`
- `void SoftI2CSDAGPIOSet (tSoftI2C *pI2C, unsigned long ulBase, unsigned char ucPin)`
- `void SoftI2CSlaveAddrSet (tSoftI2C *pI2C, unsigned char ucSlaveAddr, tBoolean bReceive)`
- `void SoftI2CTimerTick (tSoftI2C *pI2C)`

27.2.1 Data Structure Documentation

27.2.1.1 tSoftI2C

Definition:

```
typedef struct
{
    void (*pfnIntCallback) (void);
    unsigned long ulSCLGPIO;
    unsigned long ulSDAGPIO;
    unsigned char ucFlags;
    unsigned char ucSlaveAddr;
    unsigned char ucData;
    unsigned char ucState;
    unsigned char ucCurrentBit;
    unsigned char ucIntMask;
    unsigned char ucIntStatus;
}
tSoftI2C
```

Members:

pfnIntCallback The address of the callback function that is called to simulate the interrupts that would be produced by a hardware I2C implementation. This address can be set via a direct structure access or using the `SoftI2CCallbackSet` function.

uiSCLGPIO The address of the GPIO pin to be used for the SCL signal. This member can be set via a direct structure access or using the `SoftI2CSCLGPIOSet` function.

uiSDAGPIO The address of the GPIO pin to be used for the SDA signal. This member can be set via a direct structure access or using the `SoftI2CSDAGPIOSet` function.

ucFlags The flags that control the operation of the SoftI2C module. This member should not be accessed or modified by the application.

ucSlaveAddr The slave address that is currently being accessed. This member should not be accessed or modified by the application.

ucData The data that is currently being transmitted or received. This member should not be accessed or modified by the application.

ucState The current state of the SoftI2C state machine. This member should not be accessed or modified by the application.

ucCurrentBit The number of bits that have been transmitted and received in the current frame. This member should not be accessed or modified by the application.

ucIntMask The set of virtual interrupts that should be sent to the callback function. This member should not be accessed or modified by the application.

ucIntStatus The set of virtual interrupts that are currently asserted. This member should not be accessed or modified by the application.

Description:

This structure contains the state of a single instance of a SoftI2C module.

27.2.2 Function Documentation

27.2.2.1 SoftI2CBusy

Indicates whether or not the SoftI2C module is busy.

Prototype:

```
tBoolean  
SoftI2CBusy(tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

This function returns an indication of whether or not the SoftI2C module is busy transmitting or receiving data.

Returns:

Returns **true** if the SoftI2C module is busy; otherwise, returns **false**.

27.2.2.2 SoftI2CCallbackSet

Sets the callback used by the SoftI2C module.

Prototype:

```
void  
SoftI2CCallbackSet(tSoftI2C *pI2C,  
                  void (*pfnCallback)(void))
```

Parameters:

pI2C specifies the SoftI2C data structure.

pfnCallback is a pointer to the callback function.

Description:

This function sets the address of the callback function that is called when there is an “interrupt” produced by the SoftI2C module.

Returns:

None.

27.2.2.3 SoftI2CControl

Controls the state of the SoftI2C module.

Prototype:

```
void  
SoftI2CControl(tSoftI2C *pI2C,  
              unsigned long ulCmd)
```

Parameters:

pI2C specifies the SoftI2C data structure.

ulCmd command to be issued to the SoftI2C module.

Description:

This function is used to control the state of the SoftI2C module send and receive operations. The *ucCmd* parameter can be one of the following values:

- SOFTI2C_CMD_SINGLE_SEND
- SOFTI2C_CMD_SINGLE_RECEIVE
- SOFTI2C_CMD_BURST_SEND_START
- SOFTI2C_CMD_BURST_SEND_CONT
- SOFTI2C_CMD_BURST_SEND_FINISH
- SOFTI2C_CMD_BURST_SEND_ERROR_STOP
- SOFTI2C_CMD_BURST_RECEIVE_START
- SOFTI2C_CMD_BURST_RECEIVE_CONT
- SOFTI2C_CMD_BURST_RECEIVE_FINISH
- SOFTI2C_CMD_BURST_RECEIVE_ERROR_STOP

Returns:

None.

27.2.2.4 SoftI2CDataGet

Receives a byte that has been sent to the SoftI2C module.

Prototype:

```
unsigned long  
SoftI2CDataGet (tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

This function reads a byte of data from the SoftI2C module that was received as a result of an appropriate call to [SoftI2CControl\(\)](#).

Returns:

Returns the byte received by the SoftI2C module, cast as an unsigned long.

27.2.2.5 SoftI2CDataPut

Transmits a byte from the SoftI2C module.

Prototype:

```
void  
SoftI2CDataPut (tSoftI2C *pI2C,  
                unsigned char ucData)
```

Parameters:

pI2C specifies the SoftI2C data structure.

ucData data to be transmitted from the SoftI2C module.

Description:

This function places the supplied data into SoftI2C module in preparation for being transmitted via an appropriate call to [SoftI2CControl\(\)](#).

Returns:

None.

27.2.2.6 SoftI2CErr

Gets the error status of the SoftI2C module.

Prototype:

```
unsigned long  
SoftI2CErr (tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

This function is used to obtain the error status of the SoftI2C module send and receive operations.

Returns:

Returns the error status, as one of **SOFTI2C_ERR_NONE**, **SOFTI2C_ERR_ADDR_ACK**, or **SOFTI2C_ERR_DATA_ACK**.

27.2.2.7 SoftI2CInit

Initializes the SoftI2C module.

Prototype:

```
void  
SoftI2CInit (tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

This function initializes operation of the SoftI2C module. After successful initialization of the SoftI2C module, the software I2C bus is in the idle state.

Returns:

None.

27.2.2.8 SoftI2CIntClear

Clears the SoftI2C “interrupt”.

Prototype:

```
void  
SoftI2CIntClear (tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

The SoftI2C “interrupt” source is cleared, so that it no longer asserts. This function must be called in the “interrupt” handler to keep it from being called again immediately on exit.

Returns:

None.

27.2.2.9 SoftI2CIntDisable

Disables the SoftI2C “interrupt”.

Prototype:

```
void  
SoftI2CIntDisable (tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

Disables the SoftI2C “interrupt” source.

Returns:

None.

27.2.2.10 SoftI2CIntEnable

Enables the SoftI2C “interrupt”.

Prototype:

```
void  
SoftI2CIntEnable (tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

Enables the SoftI2C “interrupt” source.

Returns:

None.

27.2.2.11 SoftI2CIntStatus

Gets the current SoftI2C “interrupt” status.

Prototype:

```
tBoolean  
SoftI2CIntStatus (tSoftI2C *pI2C,  
                  tBoolean bMasked)
```

Parameters:

pI2C specifies the SoftI2C data structure.

bMasked is **false** if the raw “interrupt” status is requested and **true** if the masked “interrupt” status is requested.

Description:

This returns the “interrupt” status for the SoftI2C module. Either the raw “interrupt” status or the status of “interrupts” that are allowed to reflect to the processor can be returned.

Returns:

The current interrupt status, returned as **true** if active or **false** if not active.

27.2.2.12 SoftI2CSCLGPIOSet

Sets the GPIO pin to be used as the SoftI2C SCL signal.

Prototype:

```
void  
SoftI2CSCLGPIOSet (tSoftI2C *pI2C,  
                   unsigned long ulBase,  
                   unsigned char ucPin)
```

Parameters:

pI2C specifies the SoftI2C data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used for the SoftI2C SCL signal.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

27.2.2.13 SoftI2CSDAGPIOSet

Sets the GPIO pin to be used as the SoftI2C SDA signal.

Prototype:

```
void  
SoftI2CSDAGPIOSet (tSoftI2C *pI2C,  
                   unsigned long ulBase,  
                   unsigned char ucPin)
```

Parameters:

pI2C specifies the SoftI2C data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used for the SoftI2C SDA signal.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

27.2.2.14 SoftI2CSlaveAddrSet

Sets the address that the SoftI2C module places on the bus.

Prototype:

```
void  
SoftI2CSlaveAddrSet (tSoftI2C *pI2C,
```

```
unsigned char ucSlaveAddr,  
tBoolean bReceive)
```

Parameters:

pI2C specifies the SoftI2C data structure.

ucSlaveAddr 7-bit slave address

bReceive flag indicating the type of communication with the slave.

Description:

This function sets the address that the SoftI2C module places on the bus when initiating a transaction. When the *bReceive* parameter is set to **true**, the address indicates that the SoftI2C module is initiating a read from the slave; otherwise the address indicates that the SoftI2C module is initiating a write to the slave.

Returns:

None.

27.2.2.15 SoftI2CTimerTick

Performs the periodic update of the SoftI2C module.

Prototype:

```
void  
SoftI2CTimerTick(tSoftI2C *pI2C)
```

Parameters:

pI2C specifies the SoftI2C data structure.

Description:

This function performs the periodic, time-based updates to the SoftI2C module. The transmission and reception of data over the SoftI2C link is performed by the state machine in this function.

This function must be called at four times the desired SoftI2C clock rate. For example, to run the SoftI2C clock at 10 KHz, this function must be called at a 40 KHz rate.

Returns:

None.

27.3 Programming Example

The following example shows how to configure the software I2C module and transmit some data to an external peripheral. This example uses Timer 0 as the timing source.

```
//  
// The instance data for the software I2C.  
//  
tSoftI2C g_sI2C;  
  
//  
// The timer tick function.
```

```
//
void
Timer0AIntHandler(void)
{
    //
    // Clear the timer interrupt.
    //
    TimerIntClear(TIMER0_BASE, TIMER_TIMA_TIMEOUT);

    //
    // Call the software I2C timer tick function.
    //
    SoftI2CTimerTick(&g_sI2C);
}

//
// The callback function for the software I2C. This function is equivalent
// to the interrupt handler for a hardware I2C.
//
void
I2CCallback(void)
{
    //
    // Clear the interrupt.
    //
    SoftI2CIntClear(&g_sI2C);

    //
    // Handle the interrupt.
    //
    ...
}

//
// Setup the software I2C and send some data.
//
void
TestSoftI2C(void)
{
    //
    // Clear the software I2C instance data.
    //
    memset(&g_sI2C, 0, sizeof(g_sI2C));

    //
    // Set the callback function used for this software I2C.
    //
    SoftI2CCallbackSet(&g_sI2C, I2CCallback);

    //
    // Configure the pins used for the software I2C. This example uses
    // pins PD0 and PE1.
    //
    SoftI2CSCLGPIOSet(&g_sI2C, GPIO_PORTD_BASE, GPIO_PIN_0);
    SoftI2CSDAGPIOSet(&g_sI2C, GPIO_PORTE_BASE, GPIO_PIN_1);

    //
    // Enable the GPIO modules that contains the GPIO pins to be used by
    // the software I2C.
    //
    SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
    SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOE);

    //
    // Initialize the software I2C module.
    //
}
```

```

SoftI2CInit(&g_sI2C);

//
// Configure the timer used to generate the timing for the software
// I2C. The interface will be run at 10 KHz, requiring a timer tick
// at 40 KHz.
//
SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER0);
TimerConfigure(TIMER0_BASE, TIMER_CFG_32_BIT_PER);
TimerLoadSet(TIMER0_BASE, TIMER_A, SysCtlClockGet() / 40000);
TimerIntEnable(TIMER0_BASE, TIMER_TIMA_TIMEOUT);
IntEnable(INT_TIMER0A);
TimerEnable(TIMER0_BASE, TIMER_A);

//
// Enable the software I2C interrupt.
//
SoftI2CIntEnable(&g_sI2C);

//
// Send a single byte to the slave device.
//
SoftI2CSlaveAddrSet(&g_sI2C, 0x55, 0);
SoftI2CDataPut(&g_sI2C, 0xaa);
SoftI2CControl(&g_sI2C, SOFTI2C_CMD_SINGLE_SEND);

//
// Wait until the software I2C is idle. The completion interrupt will
// be sent to the callback function prior to exiting this loop.
//
while(SoftI2CBusy(&g_sI2C))
{
}
}

```

As a comparison, the following is the equivalent code using the hardware I2C module and the Stellaris Peripheral Driver Library.

```

//
// The interrupt handler for the hardware I2C.
//
void
I2C0IntHandler(void)
{
    //
    // Clear the asserted interrupt sources.
    //
    I2CMasterIntClear(I2C0_MASTER_BASE);

    //
    // Handle the interrupt.
    //
    ...
}

//
// Setup the hardware I2C and send some data.
//
void
TestI2C(void)
{
    //
    // Enable the GPIO module that contains the GPIO pins to be used by
    // the I2C, as well as the I2C module.
    //

```

```
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOB);
SysCtlPeripheralEnable(SYSCTL_PERIPH_I2C0);

//
// Configure the GPIO pins for use by the I2C module.
//
GPIOPinTypeI2C(GPIO_PORTB_BASE, GPIO_PIN_2 | GPIO_PIN_3);

//
// Initialize the hardware I2C module.
//
I2CMasterInitExpClk(I2C0_MASTER_BASE, SysCtlClockGet(), false);

//
// Enable the hardware I2C.
//
I2CMasterEnable(I2C0_MASTER_BASE);

//
// Enable the interrupt in the hardware I2C.
//
I2CMasterIntEnable(I2C0_MASTER_BASE);
IntEnable(INT_I2C0);

//
// Write some data into the hardware I2C transmit FIFO.
//
I2CMasterSlaveAddrSet(I2C0_MASTER_BASE, 0x55, 0);
I2CMasterDataPut(I2C0_MASTER_BASE, 0xaa);
I2CMasterControl(I2C0_MASTER_BASE, I2C_MASTER_CMD_SINGLE_SEND);

//
// Wait until the hardware I2C is idle. The interrupt will be sent to
// the interrupt handler prior to exiting this loop.
//
while(I2CBusy(I2C0_MASTER_BASE))
{
}
```

28 Software SSI Module

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28.1 Introduction

The software SSI module uses a timer and a few GPIO pins to create a slow-speed software SSI peripheral. Multiple software SSI modules can be in use simultaneously, limited only by the availability of resources (RAM for the per-instance data structure, pins for the interface, timers if unique clock rates are required, and processor cycles to execute the code). The software SSI module supports the Motorola® SPI™ formats with 4 to 16 data bits. A callback mechanism is used to simulate the interrupts that would be provided by a hardware SSI module.

The API for the software SSI module has been constructed to be as close as possible to the API provided in the Stellaris Peripheral Driver Library for the hardware SSI module. The two notable difference are the function prefix being “SoftSSI” instead of “SSI”, and the first argument of each API is a pointer to the [tSoftSSI](#) data structure instead of the base address of the hardware module.

Timing for the software SSI module is provided by the application. The [SoftSSITimerTick\(\)](#) function must be called on a periodic basis to provide the timing for the software SSI module. The timer tick function must be called at twice the desired SSI clock rate; for example, to operate the software SSI interface at 10 KHz, the tick function must be called at a 20 KHz rate. By having the application providing the timing, the timer resource to be used is flexible and multiple software SSI modules can be driven from a single timer resource. Alternatively, if the software SSI module is only needed for brief periods of time and processor usage is not a concern, the timer tick function can simply be called in a loop until the entire SSI transaction has completed (maximizing both SSI clock speed and processor usage, but not requiring a timer).

The software SSI module requires a few as two and as many as four GPIO pins. The following table shows the possible pin usages for the software SSI module:

Fss	Clk	Tx	Rx	Pins	Description
	yes	yes		2	transmit only
yes	yes	yes		3	
	yes		yes	2	receive only
yes	yes		yes	3	
	yes	yes	yes	3	transmit and receive
yes	yes	yes	yes	4	

For the cases where Fss is not used, it is up to the application to control that signal (either via a separately-controlled GPIO, or by being tied to ground in the hardware).

The per-instance data structure is approximately 52 bytes in length (the actual length will depend upon how the structure is packed by the compiler in use).

As a point of reference, the following are some rough measurements of the processor usage of the software SSI module at various SSI clock speeds with the processor running at 50 MHz. Actual processor usage may vary, depending upon how the application uses the software SSI module, processor clock speed, interrupt priority, and compiler in use.

SSI Clock	% Of Processor	Million Cycles Per Second
10 KHz	5.26	2.63
20 KHz	10.48	5.24
30 KHz	15.68	7.84
40 KHz	20.90	10.45
50 KHz	26.10	13.05
60 KHz	31.38	15.69
70 KHz	36.54	18.27
80 KHz	41.79	20.89
90 KHz	47.06	23.53
100 KHz	52.17	26.08

This module is contained in `utils/softssi.c`, with `utils/softssi.h` containing the API definitions for use by applications.

28.2 API Functions

Data Structures

- [tSoftSSI](#)

Functions

- `tBoolean` [SoftSSIBusy](#) (`tSoftSSI *pSSI`)
- `void` [SoftSSICallbackSet](#) (`tSoftSSI *pSSI`, `void (*pfnCallback)(void)`)
- `void` [SoftSSIClkGPIOSet](#) (`tSoftSSI *pSSI`, `unsigned long ulBase`, `unsigned char ucPin`)
- `void` [SoftSSIConfigSet](#) (`tSoftSSI *pSSI`, `unsigned char ucProtocol`, `unsigned char ucBits`)
- `tBoolean` [SoftSSIDataAvail](#) (`tSoftSSI *pSSI`)
- `void` [SoftSSIDataGet](#) (`tSoftSSI *pSSI`, `unsigned long *pulData`)
- `long` [SoftSSIDataGetNonBlocking](#) (`tSoftSSI *pSSI`, `unsigned long *pulData`)
- `void` [SoftSSIDataPut](#) (`tSoftSSI *pSSI`, `unsigned long ulData`)
- `long` [SoftSSIDataPutNonBlocking](#) (`tSoftSSI *pSSI`, `unsigned long ulData`)
- `void` [SoftSSIDisable](#) (`tSoftSSI *pSSI`)
- `void` [SoftSSIEnable](#) (`tSoftSSI *pSSI`)
- `void` [SoftSSIFssGPIOSet](#) (`tSoftSSI *pSSI`, `unsigned long ulBase`, `unsigned char ucPin`)
- `void` [SoftSSIIntClear](#) (`tSoftSSI *pSSI`, `unsigned long ulIntFlags`)
- `void` [SoftSSIIntDisable](#) (`tSoftSSI *pSSI`, `unsigned long ulIntFlags`)
- `void` [SoftSSIIntEnable](#) (`tSoftSSI *pSSI`, `unsigned long ulIntFlags`)
- `unsigned long` [SoftSSIIntStatus](#) (`tSoftSSI *pSSI`, `tBoolean bMasked`)
- `void` [SoftSSIRxBufferSet](#) (`tSoftSSI *pSSI`, `unsigned short *pusRxBuffer`, `unsigned short usLen`)
- `void` [SoftSSIRxGPIOSet](#) (`tSoftSSI *pSSI`, `unsigned long ulBase`, `unsigned char ucPin`)
- `tBoolean` [SoftSSISpaceAvail](#) (`tSoftSSI *pSSI`)
- `void` [SoftSSITimerTick](#) (`tSoftSSI *pSSI`)
- `void` [SoftSSITxBufferSet](#) (`tSoftSSI *pSSI`, `unsigned short *pusTxBuffer`, `unsigned short usLen`)
- `void` [SoftSSITxGPIOSet](#) (`tSoftSSI *pSSI`, `unsigned long ulBase`, `unsigned char ucPin`)

28.2.1 Data Structure Documentation

28.2.1.1 tSoftSSI

Definition:

```
typedef struct
{
    void (*pfnIntCallback)(void);
    unsigned long ulFssGPIO;
    unsigned long ulClkGPIO;
    unsigned long ulTxGPIO;
    unsigned long ulRxGPIO;
    unsigned short *pusTxBuffer;
    unsigned short *pusRxBuffer;
    unsigned short usTxBufferLen;
    unsigned short usTxBufferRead;
    unsigned short usTxBufferWrite;
    unsigned short usRxBufferLen;
    unsigned short usRxBufferRead;
    unsigned short usRxBufferWrite;
    unsigned short usTxData;
    unsigned short usRxData;
    unsigned char ucFlags;
    unsigned char ucBits;
    unsigned char ucState;
    unsigned char ucCurrentBit;
    unsigned char ucIntMask;
    unsigned char ucIntStatus;
    unsigned char ucIdleCount;
}
tSoftSSI
```

Members:

pfnIntCallback The address of the callback function that is called to simulate the interrupts that would be produced by a hardware SSI implementation. This address can be set via a direct structure access or using the SoftSSICallbackSet function.

ulFssGPIO The address of the GPIO pin to be used for the Fss signal. If this member is zero, the Fss signal is not generated. This member can be set via a direct structure access or using the SoftSSIFssGPIOSet function.

ulClkGPIO The address of the GPIO pin to be used for the Clk signal. This member can be set via a direct structure access or using the SoftSSIClkGPIOSet function.

ulTxGPIO The address of the GPIO pin to be used for the Tx signal. This member can be set via a direct structure access or using the SoftSSITxGPIOSet function.

ulRxGPIO The address of the GPIO pin to be used for the Rx signal. If this member is zero, the Rx signal is not read. This member can be set via a direct structure access or using the SoftSSIRxGPIOSet function.

pusTxBuffer The address of the data buffer used for the transmit FIFO. This member can be set via a direct structure access or using the SoftSSITxBufferSet function.

pusRxBuffer The address of the data buffer used for the receive FIFO. This member can be set via a direct structure access or using the SoftSSIRxBufferSet function.

usTxBufferLen The length of the transmit FIFO. This member can be set via a direct structure access or using the SoftSSITxBufferSet function.

- usTxBufferRead** The index into the transmit FIFO of the next word to be transmitted. This member should be initialized to zero, but should not be accessed or modified by the application.
- usTxBufferWrite** The index into the transmit FIFO of the next location to store data into the FIFO. This member should be initialized to zero, but should not be accessed or modified by the application.
- usRxBufferLen** The length of the receive FIFO. This member can be set via a direct structure access or using the `SoftSSIRxBufferSet` function.
- usRxBufferRead** The index into the receive FIFO of the next word to be read from the FIFO. This member should be initialized to zero, but should not be accessed or modified by the application.
- usRxBufferWrite** The index into the receive FIFO of the location to store the next word received. This member should be initialized to zero, but should not be accessed or modified by the application.
- usTxData** The word that is currently being transmitted. This member should not be accessed or modified by the application.
- usRxData** The word that is currently being received. This member should not be accessed or modified by the application.
- ucFlags** The flags that control the operation of the SoftSSI module. This member should not be accessed or modified by the application.
- ucBits** The number of data bits in each SoftSSI frame, which also specifies the width of each data item in the transmit and receive FIFOs. This member can be set via a direct structure access or using the `SoftSSIConfigSet` function.
- ucState** The current state of the SoftSSI state machine. This member should not be accessed or modified by the application.
- ucCurrentBit** The number of bits that have been transmitted and received in the current frame. This member should not be accessed or modified by the application.
- ucIntMask** The set of virtual interrupts that should be sent to the callback function. This member should not be accessed or modified by the application.
- ucIntStatus** The set of virtual interrupts that are currently asserted. This member should not be accessed or modified by the application.
- ucIdleCount** The number of tick counts that the SoftSSI module has been idle with data stored in the receive FIFO, which is used to generate the receive timeout interrupt. This member should not be accessed or modified by the application.

Description:

This structure contains the state of a single instance of a SoftSSI module.

28.2.2 Function Documentation

28.2.2.1 SoftSSIBusy

Determines whether the SoftSSI transmitter is busy or not.

Prototype:

```
tBoolean  
SoftSSIBusy(tSoftSSI *pSSI)
```

Parameters:

pSSI specifies the SoftSSI data structure.

Description:

Allows the caller to determine whether all transmitted bytes have cleared the transmitter. If **false** is returned, then the transmit FIFO is empty and all bits of the last transmitted word have left the shift register.

Returns:

Returns **true** if the SoftSSI is transmitting or **false** if all transmissions are complete.

28.2.2.2 SoftSSICallbackSet

Sets the callback used by the SoftSSI module.

Prototype:

```
void  
SoftSSICallbackSet (tSoftSSI *pSSI,  
                   void (*pfnCallback) (void))
```

Parameters:

pSSI specifies the SoftSSI data structure.

pfnCallback is a pointer to the callback function.

Description:

This function sets the address of the callback function that is called when there is an “interrupt” produced by the SoftSSI module.

Returns:

None.

28.2.2.3 SoftSSIClkGPIOSet

Sets the GPIO pin to be used as the SoftSSI Clk signal.

Prototype:

```
void  
SoftSSIClkGPIOSet (tSoftSSI *pSSI,  
                  unsigned long ulBase,  
                  unsigned char ucPin)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used for the SoftSSI Clk signal.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

28.2.2.4 SoftSSIConfigSet

Sets the configuration of a SoftSSI module.

Prototype:

```
void  
SoftSSIConfigSet (tSoftSSI *pSSI,  
                  unsigned char ucProtocol,  
                  unsigned char ucBits)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ucProtocol specifies the data transfer protocol.

ucBits specifies the number of bits transferred per frame.

Description:

This function configures the data format of a SoftSSI module. The *ucProtocol* parameter can be one of the following values: **SOFTSSI_FRF_MOTO_MODE_0**, **SOFTSSI_FRF_MOTO_MODE_1**, **SOFTSSI_FRF_MOTO_MODE_2**, or **SOFTSSI_FRF_MOTO_MODE_3**. These frame formats imply the following polarity and phase configurations:

Polarity	Phase	Mode
0	0	SOFTSSI_FRF_MOTO_MODE_0
0	1	SOFTSSI_FRF_MOTO_MODE_1
1	0	SOFTSSI_FRF_MOTO_MODE_2
1	1	SOFTSSI_FRF_MOTO_MODE_3

The *ucBits* parameter defines the width of the data transfers, and can be a value between 4 and 16, inclusive.

Returns:

None.

28.2.2.5 SoftSSIDataAvail

Determines if there is any data in the receive FIFO.

Prototype:

```
tBoolean  
SoftSSIDataAvail (tSoftSSI *pSSI)
```

Parameters:

pSSI specifies the SoftSSI data structure.

Description:

This function determines if there is any data available to be read from the receive FIFO.

Returns:

Returns **true** if there is data in the receive FIFO or **false** if there is no data in the receive FIFO.

28.2.2.6 SoftSSIDataGet

Gets a data element from the SoftSSI receive FIFO.

Prototype:

```
void  
SoftSSIDataGet (tSoftSSI *pSSI,  
                unsigned long *pulData)
```

Parameters:

pSSI specifies the SoftSSI data structure.

pulData is a pointer to a storage location for data that was received over the SoftSSI interface.

Description:

This function gets received data from the receive FIFO of the specified SoftSSI module and places that data into the location specified by the *pulData* parameter.

Note:

Only the lower N bits of the value written to *pulData* contain valid data, where N is the data width as configured by [SoftSSIConfigSet\(\)](#). For example, if the interface is configured for 8-bit data width, only the lower 8 bits of the value written to *pulData* contain valid data.

Returns:

None.

28.2.2.7 SoftSSIDataGetNonBlocking

Gets a data element from the SoftSSI receive FIFO.

Prototype:

```
long  
SoftSSIDataGetNonBlocking (tSoftSSI *pSSI,  
                            unsigned long *pulData)
```

Parameters:

pSSI specifies the SoftSSI data structure.

pulData is a pointer to a storage location for data that was received over the SoftSSI interface.

Description:

This function gets received data from the receive FIFO of the specified SoftSSI module and places that data into the location specified by the *ulData* parameter. If there is no data in the FIFO, then this function returns a zero.

Note:

Only the lower N bits of the value written to *pulData* contain valid data, where N is the data width as configured by [SoftSSIConfigSet\(\)](#). For example, if the interface is configured for 8-bit data width, only the lower 8 bits of the value written to *pulData* contain valid data.

Returns:

Returns the number of elements read from the SoftSSI receive FIFO.

28.2.2.8 SoftSSIDataPut

Puts a data element into the SoftSSI transmit FIFO.

Prototype:

```
void  
SoftSSIDataPut (tSoftSSI *pSSI,  
               unsigned long ulData)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulData is the data to be transmitted over the SoftSSI interface.

Description:

This function places the supplied data into the transmit FIFO of the specified SoftSSI module.

Note:

The upper 32 - N bits of the *ulData* are discarded, where N is the data width as configured by [SoftSSIConfigSet\(\)](#). For example, if the interface is configured for 8-bit data width, the upper 24 bits of *ulData* are discarded.

Returns:

None.

28.2.2.9 SoftSSIDataPutNonBlocking

Puts a data element into the SoftSSI transmit FIFO.

Prototype:

```
long  
SoftSSIDataPutNonBlocking (tSoftSSI *pSSI,  
                           unsigned long ulData)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulData is the data to be transmitted over the SoftSSI interface.

Description:

This function places the supplied data into the transmit FIFO of the specified SoftSSI module. If there is no space in the FIFO, then this function returns a zero.

Note:

The upper 32 - N bits of the *ulData* are discarded, where N is the data width as configured by [SoftSSIConfigSet\(\)](#). For example, if the interface is configured for 8-bit data width, the upper 24 bits of *ulData* are discarded.

Returns:

Returns the number of elements written to the SSI transmit FIFO.

28.2.2.10 SoftSSIDisable

Disables the SoftSSI module.

Prototype:

```
void  
SoftSSIDisable (tSoftSSI *pSSI)
```

Parameters:

pSSI specifies the SoftSSI data structure.

Description:

This function disables operation of the SoftSSI module. If a data transfer is in progress, it is finished before the module is fully disabled.

Returns:

None.

28.2.2.11 SoftSSIEnable

Enables the SoftSSI module.

Prototype:

```
void  
SoftSSIEnable (tSoftSSI *pSSI)
```

Parameters:

pSSI specifies the SoftSSI data structure.

Description:

This function enables operation of the SoftSSI module. The SoftSSI module must be configured before it is enabled.

Returns:

None.

28.2.2.12 SoftSSIFssGPIOSet

Sets the GPIO pin to be used as the SoftSSI Fss signal.

Prototype:

```
void  
SoftSSIFssGPIOSet (tSoftSSI *pSSI,  
                   unsigned long ulBase,  
                   unsigned char ucPin)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used for the SoftSSI Fss signal. If there is not a GPIO pin allocated for Fss, the SoftSSI module does not assert/deassert the Fss signal, leaving it to the application either to do manually or to not do at all if the slave device has Fss tied to ground.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

28.2.2.13 SoftSSIIntClear

Clears SoftSSI “interrupt” sources.

Prototype:

```
void  
SoftSSIIntClear(tSoftSSI *pSSI,  
               unsigned long ulIntFlags)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulIntFlags is a bit mask of the “interrupt” sources to be cleared.

Description:

The specified SoftSSI “interrupt” sources are cleared so that they no longer assert. This function must be called in the “interrupt” handler to keep the “interrupt” from being recognized again immediately upon exit. The *ulIntFlags* parameter is the logical OR of any of the **SOFTSSI_TXEOT**, **SOFTSSI_RXTO**, and **SOFTSSI_RXOR** values.

Returns:

None.

28.2.2.14 SoftSSIIntDisable

Disables individual SoftSSI “interrupt” sources.

Prototype:

```
void  
SoftSSIIntDisable(tSoftSSI *pSSI,  
                 unsigned long ulIntFlags)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulIntFlags is a bit mask of the “interrupt” sources to be disabled.

Description:

Disables the indicated SoftSSI “interrupt” sources. The *ulIntFlags* parameter can be any of the **SOFTSSI_TXEOT**, **SOFTSSI_TXFF**, **SOFTSSI_RXFF**, **SOFTSSI_RXTO**, or **SOFTSSI_RXOR** values.

Returns:

None.

28.2.2.15 SoftSSIIntEnable

Enables individual SoftSSI “interrupt” sources.

Prototype:

```
void  
SoftSSIIntEnable(tSoftSSI *pSSI,  
                unsigned long ulIntFlags)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulIntFlags is a bit mask of the “interrupt” sources to be enabled.

Description:

Enables the indicated SoftSSI “interrupt” sources. Only the sources that are enabled can be reflected to the callback function; disabled sources do not result in a callback. The **ulIntFlags** parameter can be any of the **SOFTSSI_TXEOT**, **SOFTSSI_TXFF**, **SOFTSSI_RXFF**, **SOFTSSI_RXTO**, or **SOFTSSI_RXOR** values.

Returns:

None.

28.2.2.16 SoftSSIIntStatus

Gets the current “interrupt” status.

Prototype:

```
unsigned long  
SoftSSIIntStatus(tSoftSSI *pSSI,  
                tBoolean bMasked)
```

Parameters:

pSSI specifies the SoftSSI data structure.

bMasked is **false** if the raw “interrupt” status is required or **true** if the masked “interrupt” status is required.

Description:

This function returns the “interrupt” status for the SoftSSI module. Either the raw “interrupt” status or the status of “interrupts” that are allowed to reflect to the callback can be returned.

Returns:

The current “interrupt” status, enumerated as a bit field of **SOFTSSI_TXEOT**, **SOFTSSI_TXFF**, **SOFTSSI_RXFF**, **SOFTSSI_RXTO**, and **SOFTSSI_RXOR**.

28.2.2.17 SoftSSIRxBufferSet

Sets the receive FIFO buffer for a SoftSSI module.

Prototype:

```
void  
SoftSSIRxBufferSet (tSoftSSI *pSSI,  
                    unsigned short *pusRxBuffer,  
                    unsigned short usLen)
```

Parameters:

pSSI specifies the SoftSSI data structure.

pusRxBuffer is the address of the receive FIFO buffer.

usLen is the size, in 16-bit half-words, of the receive FIFO buffer.

Description:

This function sets the address and size of the receive FIFO buffer and also resets the read and write pointers, marking the receive FIFO as empty. When the buffer pointer and length are configured as zero, all data received from the slave device is discarded. This capability is useful when there is no GPIO pin allocated for the Rx signal.

Returns:

None.

28.2.2.18 SoftSSIRxGPIOSet

Sets the GPIO pin to be used as the SoftSSI Rx signal.

Prototype:

```
void  
SoftSSIRxGPIOSet (tSoftSSI *pSSI,  
                  unsigned long ulBase,  
                  unsigned char ucPin)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used for the SoftSSI Rx signal. If there is not a GPIO pin allocated for Rx, the SoftSSI module does not read data from the slave device.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

28.2.2.19 SoftSSISpaceAvail

Determines if there is any space in the transmit FIFO.

Prototype:

```
tBoolean  
SoftSSISpaceAvail (tSoftSSI *pSSI)
```

Parameters:

pSSI specifies the SoftSSI data structure.

Description:

This function determines if there is space available in the transmit FIFO.

Returns:

Returns **true** if there is space available in the transmit FIFO or **false** if there is no space available in the transmit FIFO.

28.2.2.20 SoftSSITimerTick

Performs the periodic update of the SoftSSI module.

Prototype:

```
void  
SoftSSITimerTick (tSoftSSI *pSSI)
```

Parameters:

pSSI specifies the SoftSSI data structure.

Description:

This function performs the periodic, time-based updates to the SoftSSI module. The transmission and reception of data over the SoftSSI link is performed by the state machine in this function.

This function must be called at twice the desired SoftSSI clock rate. For example, to run the SoftSSI clock at 10 KHz, this function must be called at a 20 KHz rate.

Returns:

None.

28.2.2.21 SoftSSITxBufferSet

Sets the transmit FIFO buffer for a SoftSSI module.

Prototype:

```
void  
SoftSSITxBufferSet (tSoftSSI *pSSI,  
                    unsigned short *pusTxBuffer,  
                    unsigned short usLen)
```

Parameters:

pSSI specifies the SoftSSI data structure.

pusTxBuffer is the address of the transmit FIFO buffer.

usLen is the size, in 16-bit half-words, of the transmit FIFO buffer.

Description:

This function sets the address and size of the transmit FIFO buffer and also resets the read and write pointers, marking the transmit FIFO as empty.

Returns:

None.

28.2.2.22 SoftSSITxGPIOSet

Sets the GPIO pin to be used as the SoftSSI Tx signal.

Prototype:

```
void  
SoftSSITxGPIOSet (tSoftSSI *pSSI,  
                  unsigned long ulBase,  
                  unsigned char ucPin)
```

Parameters:

pSSI specifies the SoftSSI data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used for the SoftSSI Tx signal.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

28.3 Programming Example

The following example shows how to configure the software SSI module and transmit some data to an external peripheral. This example uses Timer 0 as the timing source.

```
//  
// The instance data for the software SSI.  
//  
tSoftSSI g_sSSI;  
  
//  
// The buffer used to hold the transmit data.  
//  
unsigned short g_pusTxBuffer[8];  
  
//  
// The timer tick function.  
//
```

```

void
Timer0AIntHandler(void)
{
    //
    // Clear the timer interrupt.
    //
    TimerIntClear(TIMER0_BASE, TIMER_TIMA_TIMEOUT);

    //
    // Call the software SSI timer tick function.
    //
    SoftSSITimerTick(&g_sSSI);
}

//
// The callback function for the software SSI. This function is equivalent
// to the interrupt handler for a hardware SSI.
//
void
SSICallback(void)
{
    unsigned long ulInts;

    //
    // Read the asserted interrupt sources.
    //
    ulInts = SoftSSIIntStatus(&g_sSSI, true);

    //
    // Clear the asserted interrupt sources.
    //
    SoftSSIIntClear(&g_sSSI, ulInts);

    //
    // Handle the asserted interrupts.
    //
    ...
}

//
// Setup the software SSI and send some data.
//
void
TestSoftSSI(void)
{
    //
    // Clear the software SSI instance data.
    //
    memset(&g_sSSI, 0, sizeof(g_sSSI));

    //
    // Set the callback function used for this software SSI.
    //
    SoftSSICallbackSet(&g_sSSI, SSICallback);

    //
    // Configure the pins used for the software SSI. This example uses
    // pins PD0, PE1, and PF2.
    //
    SoftSSIFssGPIOSet(&g_sSSI, GPIO_PORTD_BASE, GPIO_PIN_0);
    SoftSSIClkGPIOSet(&g_sSSI, GPIO_PORTE_BASE, GPIO_PIN_1);
    SoftSSITxGPIOSet(&g_sSSI, GPIO_PORTF_BASE, GPIO_PIN_2);

    //
    // Configure the data buffer used as the transmit FIFO.
    //

```

```
SoftSSITxBufferSet(&g_sSSI, g_pusTxBuffer, 8);

//
// Enable the GPIO modules that contains the GPIO pins to be used by
// the software SSI.
//
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOE);
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOF);

//
// Initialize the software SSI module, using mode 3 and 8 data bits.
//
SoftSSIConfigSet(&g_sSSI, SOFTSSI_FRF_MOTO_MODE_3, 8);

//
// Enable the software SSI.
//
SoftSSIEnable(&g_sSSI);

//
// Configure the timer used to generate the timing for the software
// SSI. The interface will be run at 10 KHz, requiring a timer tick
// at 20 KHz.
//
SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER0);
TimerConfigure(TIMER0_BASE, TIMER_CFG_32_BIT_PER);
TimerLoadSet(TIMER0_BASE, TIMER_A, SysCtlClockGet() / 20000);
TimerIntEnable(TIMER0_BASE, TIMER_TIMA_TIMEOUT);
IntEnable(INT_TIMER0A);
TimerEnable(TIMER0_BASE, TIMER_A);

//
// Enable the transmit FIFO half full interrupt in the software SSI.
//
SoftSSIIntEnable(&g_sSSI, SOFTSSI_TXFF);

//
// Write some data into the software SSI transmit FIFO.
//
SoftSSIDataPut(&g_sSSI, 0x55);
SoftSSIDataPut(&g_sSSI, 0xaa);
SoftSSIDataPut(&g_sSSI, 0x55);
SoftSSIDataPut(&g_sSSI, 0xaa);
SoftSSIDataPut(&g_sSSI, 0x55);
SoftSSIDataPut(&g_sSSI, 0xaa);

//
// Wait until the software SSI is idle. The transmit FIFO half full
// interrupt will be sent to the callback function prior to exiting
// this loop.
//
while(SoftSSIBusy(&g_sSSI))
{
}
}
```

As a comparison, the following is the equivalent code using the hardware SSI module and the Stellaris Peripheral Driver Library.

```
//
// The interrupt handler for the hardware SSI.
//
void
SSI0IntHandler(void)
```

```
{
    unsigned long ulInts;

    //
    // Read the asserted interrupt sources.
    //
    ulInts = SSIIntStatus(SSIO_BASE, true);

    //
    // Clear the asserted interrupt sources.
    //
    SSIIntClear(SSIO_BASE, ulInts);

    //
    // Handle the asserted interrupts.
    //
    ...
}

//
// Setup the hardware SSI and send some data.
//
void
TestSSI(void)
{
    //
    // Enable the GPIO module that contains the GPIO pins to be used by
    // the SSI, as well as the SSI module.
    //
    SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA);
    SysCtlPeripheralEnable(SYSCTL_PERIPH_SSI0);

    //
    // Configure the GPIO pins for use by the SSI module.
    //
    GPIOPinTypeSSI(GPIO_PORTA_BASE, (GPIO_PIN_2 | GPIO_PIN_3 |
                                     GPIO_PIN_4 | GPIO_PIN_5));

    //
    // Initialize the hardware SSI module, using mode 3 and 8 data bits.
    //
    SSIConfigSetExpClk(SSIO_BASE, SysCtlClockGet(), SSI_FRF_MOTO_MODE_3,
                      SSI_MODE_MASTER, 10000, 8);

    //
    // Enable the hardware SSI.
    //
    SSISetup(SSIO_BASE);

    //
    // Enable the transmit FIFO half full interrupt in the hardware SSI.
    //
    SSIIntEnable(SSIO_BASE, SSI_TXFF);
    IntEnable(INT_SSI0);

    //
    // Write some data into the hardware SSI transmit FIFO.
    //
    SSIDataPut(SSIO_BASE, 0x55);
    SSIDataPut(SSIO_BASE, 0xaa);
    SSIDataPut(SSIO_BASE, 0x55);
    SSIDataPut(SSIO_BASE, 0xaa);
    SSIDataPut(SSIO_BASE, 0x55);
    SSIDataPut(SSIO_BASE, 0xaa);

    //

```

```
// Wait until the hardware SSI is idle. The transmit FIFO half full
// interrupt will be sent to the interrupt handler prior to exiting
// this loop.
//
while(SSIBusy(SSIO_BASE))
{
}
```


29 Software UART Module

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29.1 Introduction

The software UART module uses two timers and a two GPIO pins to create a software UART peripheral. Multiple software UART modules can be in use simultaneously, limited only by the availability of resources (RAM for the per-instance data structure, pins for the interface, timers, and processor cycles to execute the code). The software UART module supports five through eight data bits, a variety of parity modes (odd, even, one, zero, and none), and one or two stop bits. A callback mechanism is used to simulate the interrupts that would be provided by a hardware UART module.

The API for the software UART module has been constructed to be as close as possible to the API provided in the Stellaris Peripheral Driver Library for the hardware UART module. The two notable difference are the function prefix being “SoftUART” instead of “UART”, and the first argument of each API is a pointer to the [tSoftUART](#) data structure instead of the base address of the hardware module.

The software UART transmitter and receiver are handled independently (because of the asynchronous nature of the two). As a result, there are separate timers for each, and if only one is required then the other does not need to be utilized.

Timing for the software UART transmitter is provided by the application. The [SoftUARTTx-TimerTick\(\)](#) function must be called on a periodic basis to provide the timing for the software UART transmitter. The timer tick function must be called at the desired UART baud rate; for example, to operate the software UART transmitter at 38,400 baud, the tick function must be called at a 38,400 Hz rate. Because the application provides the timing, the timer resource can be flexible and multiple software UART transmitters can be driven from a single timer resource.

Timing for the software UART receiver is also provided by the application. Initially, the Rx pin is configured by the software UART module for a GPIO edge interrupt. The GPIO edge interrupt handler must be provided by the application (so that it can be shared with other possible GPIO interrupts on that port). When the interrupt occurs, a timer must be started at the desired baud rate (i.e. for 38,400 baud, it must run at 38,400 Hz) and the [SoftUARTRxTick\(\)](#) function must be called. Then, whenever the timer interrupt occurs, the [SoftUARTRxTick\(\)](#) function must be called. The timer is disabled whenever [SoftUARTRxTick\(\)](#) indicates that it is no longer needed. Because the application provides the timing, the timer resource can be flexible. However, each software UART receiver must have its own timer resource.

The software UART module requires one or two GPIO pins. The following table shows the possible pin usages for the software UART module:

Tx	Rx	Pins	Description
yes		1	transmit only
	yes	1	receive only
yes	yes	2	transmit and receive

The per-instance data structure is approximately 52 bytes in length (the actual length depends on how the structure is packed by the compiler in use).

The following table shows some approximate measurements of the processor usage of the software UART module at various baud rates with the processor running at 50 MHz. Actual processor usage may vary, depending on how the application uses the software UART module, processor clock speed, interrupt priority, and compiler in use.

UART Baud Rate	% Of Processor	Million Cycles Per Second
9600	5.32	2.66
14400	7.99	3.99
19200	10.65	5.32
28800	15.96	7.98
38400	21.28	10.64
57600	32.00	16.00
115200	64.04	32.02

This module is contained in `utils/softuart.c`, with `utils/softuart.h` containing the API definitions for use by applications.

29.2 API Functions

Data Structures

- [tSoftUART](#)

Functions

- void [SoftUARTBreakCtl](#) ([tSoftUART](#) *pUART, tBoolean bBreakState)
- tBoolean [SoftUARTBusy](#) ([tSoftUART](#) *pUART)
- void [SoftUARTCallbackSet](#) ([tSoftUART](#) *pUART, void (*pfnCallback)(void))
- long [SoftUARTCharGet](#) ([tSoftUART](#) *pUART)
- long [SoftUARTCharGetNonBlocking](#) ([tSoftUART](#) *pUART)
- void [SoftUARTCharPut](#) ([tSoftUART](#) *pUART, unsigned char ucData)
- tBoolean [SoftUARTCharPutNonBlocking](#) ([tSoftUART](#) *pUART, unsigned char ucData)
- tBoolean [SoftUARTCharsAvail](#) ([tSoftUART](#) *pUART)
- void [SoftUARTConfigGet](#) ([tSoftUART](#) *pUART, unsigned long *pulConfig)
- void [SoftUARTConfigSet](#) ([tSoftUART](#) *pUART, unsigned long ulConfig)
- void [SoftUARTDisable](#) ([tSoftUART](#) *pUART)
- void [SoftUARTEnable](#) ([tSoftUART](#) *pUART)
- void [SoftUARTFIFOLevelGet](#) ([tSoftUART](#) *pUART, unsigned long *pulTxLevel, unsigned long *pulRxLevel)
- void [SoftUARTFIFOLevelSet](#) ([tSoftUART](#) *pUART, unsigned long ulTxLevel, unsigned long ulRxLevel)
- void [SoftUARTInit](#) ([tSoftUART](#) *pUART)
- void [SoftUARTIntClear](#) ([tSoftUART](#) *pUART, unsigned long ulIntFlags)
- void [SoftUARTIntDisable](#) ([tSoftUART](#) *pUART, unsigned long ulIntFlags)
- void [SoftUARTIntEnable](#) ([tSoftUART](#) *pUART, unsigned long ulIntFlags)

- unsigned long [SoftUARTIntStatus](#) (tSoftUART *pUART, tBoolean bMasked)
- unsigned long [SoftUARTParityModeGet](#) (tSoftUART *pUART)
- void [SoftUARTParityModeSet](#) (tSoftUART *pUART, unsigned long ulParity)
- void [SoftUARTRxBufferSet](#) (tSoftUART *pUART, unsigned short *pusRxBuffer, unsigned short usLen)
- void [SoftUARTRxErrorClear](#) (tSoftUART *pUART)
- unsigned long [SoftUARTRxErrorGet](#) (tSoftUART *pUART)
- void [SoftUARTRxGPIOSet](#) (tSoftUART *pUART, unsigned long ulBase, unsigned char ucPin)
- unsigned long [SoftUARTRxTick](#) (tSoftUART *pUART, tBoolean bEdgeInt)
- tBoolean [SoftUARTSpaceAvail](#) (tSoftUART *pUART)
- void [SoftUARTTxBufferSet](#) (tSoftUART *pUART, unsigned char *pucTxBuffer, unsigned short usLen)
- void [SoftUARTTxGPIOSet](#) (tSoftUART *pUART, unsigned long ulBase, unsigned char ucPin)
- void [SoftUARTTxTimerTick](#) (tSoftUART *pUART)

29.2.1 Data Structure Documentation

29.2.1.1 tSoftUART

Definition:

```
typedef struct
{
    void (*pfnIntCallback) (void);
    unsigned long ulTxGPIO;
    unsigned long ulRxGPIOPort;
    unsigned char *pucTxBuffer;
    unsigned short *pusRxBuffer;
    unsigned short usTxBufferLen;
    unsigned short usTxBufferRead;
    unsigned short usTxBufferWrite;
    unsigned short usTxBufferLevel;
    unsigned short usRxBufferLen;
    unsigned short usRxBufferRead;
    unsigned short usRxBufferWrite;
    unsigned short usRxBufferLevel;
    unsigned short usIntStatus;
    unsigned short usIntMask;
    unsigned short usConfig;
    unsigned char ucFlags;
    unsigned char ucTxState;
    unsigned char ucTxNext;
    unsigned char ucTxData;
    unsigned char ucRxPin;
    unsigned char ucRxState;
    unsigned char ucRxData;
    unsigned char ucRxFlags;
    unsigned char ucRxStatus;
}
tSoftUART
```

Members:

- pfIntCallback*** The address of the callback function that is called to simulate the interrupts that would be produced by a hardware UART implementation. This address can be set via a direct structure access or using the `SoftUARTCallbackSet` function.
- ulTxGPIO*** The address of the GPIO pin to be used for the Tx signal. This member can be set via a direct structure access or using the `SoftUARTTxGPIOSet` function.
- ulRxGPIOPort*** The address of the GPIO port to be used for the Rx signal. This member can be set via a direct structure access or using the `SoftUARTRxGPIOSet` function.
- pucTxBuffer*** The address of the data buffer used for the transmit buffer. This member can be set via a direct structure access or using the `SoftUARTTxBufferSet` function.
- pusRxBuffer*** The address of the data buffer used for the receive buffer. This member can be set via a direct structure access or using the `SoftUARTRxBufferSet` function.
- usTxBufferLen*** The length of the transmit buffer. This member can be set via a direct structure access or using the `SoftUARTTxBufferSet` function.
- usTxBufferRead*** The index into the transmit buffer of the next character to be transmitted. This member should not be accessed or modified by the application.
- usTxBufferWrite*** The index into the transmit buffer of the next location to store a character into the buffer. This member should not be accessed or modified by the application.
- usTxBufferLevel*** The transmit buffer level at which the transmit interrupt is asserted. This member should not be accessed or modified by the application.
- usRxBufferLen*** The length of the receive buffer. This member can be set via a direct structure access or using the `SoftUARTRxBufferSet` function.
- usRxBufferRead*** The index into the receive buffer of the next character to be read from the buffer. This member should not be accessed or modified by the application.
- usRxBufferWrite*** The index into the receive buffer of the location to store the next character received. This member should not be accessed or modified by the application.
- usRxBufferLevel*** The receive buffer level at which the receive interrupt is asserted. This member should not be accessed or modified by the application.
- usIntStatus*** The set of virtual interrupts that are currently asserted. This member should not be accessed or modified by the application.
- usIntMask*** The set of virtual interrupts that should be sent to the callback function. This member should not be accessed or modified by the application.
- usConfig*** The configuration of the SoftUART module. This member can be set via the `SoftUARTConfigSet` and `SoftUARTFIFOLevelSet` functions.
- ucFlags*** The flags that control the operation of the SoftUART module. This member should not be accessed or modified by the application.
- ucTxState*** The current state of the SoftUART transmit state machine. This member should not be accessed or modified by the application.
- ucTxNext*** The value that is written to the Tx pin at the start of the next transmit timer tick. This member should not be accessed or modified by the application.
- ucTxData*** The character that is currently being sent via the Tx pin. This member should not be accessed or modified by the application.
- ucRxPin*** The GPIO pin to be used for the Rx signal. This member can be set via a direct structure access or using the `SoftUARTRxGPIOSet` function.
- ucRxState*** The current state of the SoftUART receive state machine. This member should not be accessed or modified by the application.
- ucRxData*** The character that is currently being received via the Rx pin. This member should not be accessed or modified by the application.

ucRxFlags The flags that indicate any errors that have occurred during the reception of the current character via the Rx pin. This member should not be accessed or modified by the application.

ucRxStatus The receive error status. This member should only be accessed via the SoftUARTRxErrorGet and SoftURATRxErrorClear functions.

Description:

This structure contains the state of a single instance of a SoftUART module.

29.2.2 Function Documentation

29.2.2.1 SoftUARTBreakCtl

Causes a BREAK to be sent.

Prototype:

```
void  
SoftUARTBreakCtl (tSoftUART *pUART,  
                  tBoolean bBreakState)
```

Parameters:

pUART specifies the SoftUART data structure.

bBreakState controls the output level.

Description:

Calling this function with *bBreakState* set to **true** asserts a break condition on the SoftUART. Calling this function with *bBreakState* set to **false** removes the break condition. For proper transmission of a break command, the break must be asserted for at least two complete frames.

Returns:

None.

29.2.2.2 SoftUARTBusy

Determines whether the UART transmitter is busy or not.

Prototype:

```
tBoolean  
SoftUARTBusy (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

Allows the caller to determine whether all transmitted bytes have cleared the transmitter hardware. If **false** is returned, the transmit buffer is empty and all bits of the last transmitted character, including all stop bits, have left the hardware shift register.

Returns:

Returns **true** if the UART is transmitting or **false** if all transmissions are complete.

29.2.2.3 SoftUARTCallbackSet

Sets the callback used by the SoftUART module.

Prototype:

```
void  
SoftUARTCallbackSet (tSoftUART *pUART,  
                     void (*pfnCallback) (void))
```

Parameters:

pUART specifies the SoftUART data structure.
pfnCallback is a pointer to the callback function.

Description:

This function sets the address of the callback function that is called when there is an “interrupt” produced by the SoftUART module.

Returns:

None.

29.2.2.4 SoftUARTCharGet

Waits for a character from the specified port.

Prototype:

```
long  
SoftUARTCharGet (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

Gets a character from the receive buffer for the specified port. If there are no characters available, this function waits until a character is received before returning.

Returns:

Returns the character read from the specified port, cast as a *long*.

29.2.2.5 SoftUARTCharGetNonBlocking

Receives a character from the specified port.

Prototype:

```
long  
SoftUARTCharGetNonBlocking (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

Gets a character from the receive buffer for the specified port.

Returns:

Returns the character read from the specified port, cast as a *long*. A **-1** is returned if there are no characters present in the receive buffer. The [SoftUARTCharsAvail\(\)](#) function should be called before attempting to call this function.

29.2.2.6 SoftUARTCharPut

Waits to send a character from the specified port.

Prototype:

```
void  
SoftUARTCharPut (tSoftUART *pUART,  
                 unsigned char ucData)
```

Parameters:

pUART specifies the SoftUART data structure.

ucData is the character to be transmitted.

Description:

Sends the character *ucData* to the transmit buffer for the specified port. If there is no space available in the transmit buffer, this function waits until there is space available before returning.

Returns:

None.

29.2.2.7 SoftUARTCharPutNonBlocking

Sends a character to the specified port.

Prototype:

```
tBoolean  
SoftUARTCharPutNonBlocking (tSoftUART *pUART,  
                            unsigned char ucData)
```

Parameters:

pUART specifies the SoftUART data structure.

ucData is the character to be transmitted.

Description:

Writes the character *ucData* to the transmit buffer for the specified port. This function does not block, so if there is no space available, then a **false** is returned, and the application must retry the function later.

Returns:

Returns **true** if the character was successfully placed in the transmit buffer or **false** if there was no space available in the transmit buffer.

29.2.2.8 SoftUARTCharsAvail

Determines if there are any characters in the receive buffer.

Prototype:

```
tBoolean  
SoftUARTCharsAvail (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function returns a flag indicating whether or not there is data available in the receive buffer.

Returns:

Returns **true** if there is data in the receive buffer or **false** if there is no data in the receive buffer.

29.2.2.9 SoftUARTConfigGet

Gets the current configuration of a UART.

Prototype:

```
void  
SoftUARTConfigGet (tSoftUART *pUART,  
                  unsigned long *pulConfig)
```

Parameters:

pUART specifies the SoftUART data structure.

pulConfig is a pointer to storage for the data format.

Description:

Returns the data format of the SoftUART. The data format returned in *pulConfig* is enumerated the same as the *ulConfig* parameter of [SoftUARTConfigSet\(\)](#).

Returns:

None.

29.2.2.10 SoftUARTConfigSet

Sets the configuration of a SoftUART module.

Prototype:

```
void  
SoftUARTConfigSet (tSoftUART *pUART,  
                  unsigned long ulConfig)
```

Parameters:

pUART specifies the SoftUART data structure.

ulConfig is the data format for the port (number of data bits, number of stop bits, and parity).

Description:

This function configures the SoftUART for operation in the specified data format, as specified in the *ulConfig* parameter.

The *ulConfig* parameter is the logical OR of three values: the number of data bits, the number of stop bits, and the parity. **SOFTUART_CONFIG_WLEN_8**, **SOFTUART_CONFIG_WLEN_7**, **SOFTUART_CONFIG_WLEN_6**, and **SOFTUART_CONFIG_WLEN_5** select from eight to five data bits per byte (respectively). **SOFTUART_CONFIG_STOP_ONE** and **SOFTUART_CONFIG_STOP_TWO** select one or two stop bits (respectively). **SOFTUART_CONFIG_PAR_NONE**, **SOFTUART_CONFIG_PAR_EVEN**, **SOFTUART_CONFIG_PAR_ODD**, **SOFTUART_CONFIG_PAR_ONE**, and **SOFTUART_CONFIG_PAR_ZERO** select the parity mode (no parity bit, even parity bit, odd parity bit, parity bit always one, and parity bit always zero, respectively).

Returns:

None.

29.2.2.11 SoftUARTDisable

Disables the SoftUART.

Prototype:

```
void  
SoftUARTDisable(tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function disables the SoftUART after waiting for it to become idle.

Returns:

None.

29.2.2.12 SoftUARTEnable

Enables the SoftUART.

Prototype:

```
void  
SoftUARTEnable(tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function enables the SoftUART, allowing data to be transmitted and received.

Returns:

None.

29.2.2.13 SoftUARTFIFOLevelGet

Gets the buffer level at which “interrupts” are generated.

Prototype:

```
void  
SoftUARTFIFOLevelGet (tSoftUART *pUART,  
                      unsigned long *pulTxLevel,  
                      unsigned long *pulRxLevel)
```

Parameters:

pUART specifies the SoftUART data structure.

pulTxLevel is a pointer to storage for the transmit buffer level, returned as one of **UART_FIFO_TX1_8**, **UART_FIFO_TX2_8**, **UART_FIFO_TX4_8**, **UART_FIFO_TX6_8**, or **UART_FIFO_TX7_8**.

pulRxLevel is a pointer to storage for the receive buffer level, returned as one of **UART_FIFO_RX1_8**, **UART_FIFO_RX2_8**, **UART_FIFO_RX4_8**, **UART_FIFO_RX6_8**, or **UART_FIFO_RX7_8**.

Description:

This function gets the buffer level at which transmit and receive “interrupts” are generated.

Returns:

None.

29.2.2.14 SoftUARTFIFOLevelSet

Sets the buffer level at which “interrupts” are generated.

Prototype:

```
void  
SoftUARTFIFOLevelSet (tSoftUART *pUART,  
                      unsigned long ulTxLevel,  
                      unsigned long ulRxLevel)
```

Parameters:

pUART specifies the SoftUART data structure.

ulTxLevel is the transmit buffer “interrupt” level, specified as one of **UART_FIFO_TX1_8**, **UART_FIFO_TX2_8**, **UART_FIFO_TX4_8**, **UART_FIFO_TX6_8**, or **UART_FIFO_TX7_8**.

ulRxLevel is the receive buffer “interrupt” level, specified as one of **UART_FIFO_RX1_8**, **UART_FIFO_RX2_8**, **UART_FIFO_RX4_8**, **UART_FIFO_RX6_8**, or **UART_FIFO_RX7_8**.

Description:

This function sets the buffer level at which transmit and receive “interrupts” are generated.

Returns:

None.

29.2.2.15 SoftUARTInit

Initializes the SoftUART module.

Prototype:

```
void  
SoftUARTInit (tSoftUART *pUART)
```

Parameters:

pUART specifies the soft UART data structure.

Description:

This function initializes the data structure for the SoftUART module, putting it into the default configuration.

Returns:

None.

29.2.2.16 SoftUARTIntClear

Clears SoftUART “interrupt” sources.

Prototype:

```
void  
SoftUARTIntClear (tSoftUART *pUART,  
                  unsigned long ulIntFlags)
```

Parameters:

pUART specifies the SoftUART data structure.

ulIntFlags is a bit mask of the “interrupt” sources to be cleared.

Description:

The specified SoftUART “interrupt” sources are cleared, so that they no longer assert. This function must be called in the callback function to keep the “interrupt” from being recognized again immediately upon exit.

The *ulIntFlags* parameter has the same definition as the *ulIntFlags* parameter to [SoftUARTIntEnable\(\)](#).

Returns:

None.

29.2.2.17 SoftUARTIntDisable

Disables individual SoftUART “interrupt” sources.

Prototype:

```
void  
SoftUARTIntDisable (tSoftUART *pUART,  
                    unsigned long ulIntFlags)
```

Parameters:

pUART specifies the SoftUART data structure.

ulIntFlags is the bit mask of the “interrupt” sources to be disabled.

Description:

Disables the indicated SoftUART “interrupt” sources. Only the sources that are enabled can be reflected to the SoftUART callback.

The *ulIntFlags* parameter has the same definition as the *ulIntFlags* parameter to [SoftUARTIntEnable\(\)](#).

Returns:

None.

29.2.2.18 SoftUARTIntEnable

Enables individual SoftUART “interrupt” sources.

Prototype:

```
void  
SoftUARTIntEnable(tSoftUART *pUART,  
                  unsigned long ulIntFlags)
```

Parameters:

pUART specifies the SoftUART data structure.

ulIntFlags is the bit mask of the “interrupt” sources to be enabled.

Description:

Enables the indicated SoftUART “interrupt” sources. Only the sources that are enabled can be reflected to the SoftUART callback.

The *ulIntFlags* parameter is the logical OR of any of the following:

- **SOFTUART_INT_OE** - Overrun Error “interrupt”
- **SOFTUART_INT_BE** - Break Error “interrupt”
- **SOFTUART_INT_PE** - Parity Error “interrupt”
- **SOFTUART_INT_FE** - Framing Error “interrupt”
- **SOFTUART_INT_RT** - Receive Timeout “interrupt”
- **SOFTUART_INT_TX** - Transmit “interrupt”
- **SOFTUART_INT_RX** - Receive “interrupt”

Returns:

None.

29.2.2.19 SoftUARTIntStatus

Gets the current SoftUART “interrupt” status.

Prototype:

```
unsigned long  
SoftUARTIntStatus(tSoftUART *pUART,  
                  tBoolean bMasked)
```

Parameters:

pUART specifies the SoftUART data structure.

bMasked is **false** if the raw “interrupt” status is required and **true** if the masked “interrupt” status is required.

Description:

This returns the “interrupt” status for the SoftUART. Either the raw “interrupt” status or the status of “interrupts” that are allowed to reflect to the SoftUART callback can be returned.

Returns:

Returns the current “interrupt” status, enumerated as a bit field of values described in [SoftUARTIntEnable\(\)](#).

29.2.2.20 SoftUARTParityModeGet

Gets the type of parity currently being used.

Prototype:

```
unsigned long  
SoftUARTParityModeGet (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function gets the type of parity used for transmitting data and expected when receiving data.

Returns:

Returns the current parity settings, specified as one of **SOFTUART_CONFIG_PAR_NONE**, **SOFTUART_CONFIG_PAR_EVEN**, **SOFTUART_CONFIG_PAR_ODD**, **SOFTUART_CONFIG_PAR_ONE**, or **SOFTUART_CONFIG_PAR_ZERO**.

29.2.2.21 SoftUARTParityModeSet

Sets the type of parity.

Prototype:

```
void  
SoftUARTParityModeSet (tSoftUART *pUART,  
                        unsigned long ulParity)
```

Parameters:

pUART specifies the SoftUART data structure.

ulParity specifies the type of parity to use.

Description:

Sets the type of parity to use for transmitting and expect when receiving. The *ulParity* parameter must be one of **SOFTUART_CONFIG_PAR_NONE**, **SOFTUART_CONFIG_PAR_EVEN**, **SOFTUART_CONFIG_PAR_ODD**, **SOFTUART_CONFIG_PAR_ONE**, or **SOFTUART_CONFIG_PAR_ZERO**. The last two allow direct control of the parity bit; it is always either one or zero based on the mode.

Returns:

None.

29.2.2.22 SoftUARTRxBufferSet

Sets the receive buffer for a SoftUART module.

Prototype:

```
void  
SoftUARTRxBufferSet (tSoftUART *pUART,  
                     unsigned short *pusRxBuffer,  
                     unsigned short usLen)
```

Parameters:

pUART specifies the SoftUART data structure.

pusRxBuffer is the address of the receive buffer.

usLen is the size, in 16-bit half-words, of the receive buffer.

Description:

This function sets the address and size of the receive buffer. It also resets the read and write pointers, marking the receive buffer as empty.

Returns:

None.

29.2.2.23 SoftUARTRxErrorClear

Clears all reported receiver errors.

Prototype:

```
void  
SoftUARTRxErrorClear (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function is used to clear all receiver error conditions reported via [SoftUARTRxErrorGet\(\)](#). If using the overrun, framing error, parity error or break interrupts, this function must be called after clearing the interrupt to ensure that later errors of the same type trigger another interrupt.

Returns:

None.

29.2.2.24 SoftUARTRxErrorGet

Gets current receiver errors.

Prototype:

```
unsigned long
SoftUARTRxErrorGet (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function returns the current state of each of the 4 receiver error sources. The returned errors are equivalent to the four error bits returned via the previous call to [SoftUARTCharGet\(\)](#) or [SoftUARTCharGetNonBlocking\(\)](#) with the exception that the overrun error is set immediately when the overrun occurs rather than when a character is next read.

Returns:

Returns a logical OR combination of the receiver error flags, **SOFTUART_RXERROR_FRAMING**, **SOFTUART_RXERROR_PARITY**, **SOFTUART_RXERROR_BREAK** and **SOFTUART_RXERROR_OVERRUN**.

29.2.2.25 SoftUARTRxGPIOSet

Sets the GPIO pin to be used as the SoftUART Rx signal.

Prototype:

```
void
SoftUARTRxGPIOSet (tSoftUART *pUART,
                   unsigned long ulBase,
                   unsigned char ucPin)
```

Parameters:

pUART specifies the SoftUART data structure.
ulBase is the base address of the GPIO module.
ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used when the SoftUART must sample the Rx signal. If there is not a GPIO pin allocated for Rx, the SoftUART module will not read data from the slave device.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

29.2.2.26 SoftUARTRxTick

Performs the periodic update of the SoftUART receiver.

Prototype:

```
unsigned long
SoftUARTRxTick (tSoftUART *pUART,
                tBoolean bEdgeInt)
```

Parameters:

pUART specifies the SoftUART data structure.

bEdgeInt should be **true** if this function is being called because of a GPIO edge interrupt and **false** if it is being called because of a timer interrupt.

Description:

This function performs the periodic, time-based updates to the SoftUART receiver. The reception of data to the SoftUART is performed by the state machine in this function.

This function must be called by the GPIO interrupt handler, and then periodically at the desired SoftUART baud rate. For example, to run the SoftUART at 115,200 baud, this function must be called at a 115,200 Hz rate.

Returns:

Returns **SOFTUART_RXTIMER_NOP** if the receive timer should continue to operate or **SOFTUART_RXTIMER_END** if it should be stopped.

29.2.2.27 SoftUARTSpaceAvail

Determines if there is any space in the transmit buffer.

Prototype:

```
tBoolean  
SoftUARTSpaceAvail (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function returns a flag indicating whether or not there is space available in the transmit buffer.

Returns:

Returns **true** if there is space available in the transmit buffer or **false** if there is no space available in the transmit buffer.

29.2.2.28 SoftUARTTxBufferSet

Sets the transmit buffer for a SoftUART module.

Prototype:

```
void  
SoftUARTTxBufferSet (tSoftUART *pUART,  
                     unsigned char *pucTxBuffer,  
                     unsigned short usLen)
```

Parameters:

pUART specifies the SoftUART data structure.

pucTxBuffer is the address of the transmit buffer.

usLen is the size, in 8-bit bytes, of the transmit buffer.

Description:

This function sets the address and size of the transmit buffer. It also resets the read and write pointers, marking the transmit buffer as empty.

Returns:

None.

29.2.2.29 SoftUARTTxGPIOSet

Sets the GPIO pin to be used as the SoftUART Tx signal.

Prototype:

```
void  
SoftUARTTxGPIOSet (tSoftUART *pUART,  
                   unsigned long ulBase,  
                   unsigned char ucPin)
```

Parameters:

pUART specifies the SoftUART data structure.

ulBase is the base address of the GPIO module.

ucPin is the bit-packed representation of the pin to use.

Description:

This function sets the GPIO pin that is used when the SoftUART must assert the Tx signal.

The pin is specified using a bit-packed byte, where bit 0 of the byte represents GPIO port pin 0, bit 1 represents GPIO port pin 1, and so on.

Returns:

None.

29.2.2.30 SoftUARTTxTimerTick

Performs the periodic update of the SoftUART transmitter.

Prototype:

```
void  
SoftUARTTxTimerTick (tSoftUART *pUART)
```

Parameters:

pUART specifies the SoftUART data structure.

Description:

This function performs the periodic, time-based updates to the SoftUART transmitter. The transmission of data from the SoftUART is performed by the state machine in this function.

This function must be called at the desired SoftUART baud rate. For example, to run the SoftUART at 115,200 baud, this function must be called at a 115,200 Hz rate.

Returns:

None.

29.3 Programming Example

The following example shows how to configure the software UART module and transmit some data to an external peripheral. This example uses Timer 0 as the timing source.

```
//
// The instance data for the software UART.
//
tSoftUART g_sUART;

//
// The buffer used to hold the transmit data.
//
unsigned char g_pucTxBuffer[16];

//
// The buffer used to hold the receive data.
//
unsigned short g_pusRxBuffer[16];

//
// The number of processor clocks in the time period of a single bit on the
// software UART interface.
//
unsigned long g_ulBitTime;

//
// The transmit timer tick function.
//
void
Timer0AIntHandler(void)
{
    //
    // Clear the timer interrupt.
    //
    TimerIntClear(TIMER0_BASE, TIMER_TIMA_TIMEOUT);

    //
    // Call the software UART transmit timer tick function.
    //
    SoftUARTTxTimerTick(&g_sUART);
}

//
// The receive timer tick function.
//
void
Timer0BIntHandler(void)
{
    //
    // Clear the timer interrupt.
    //
    TimerIntClear(TIMER0_BASE, TIMER_TIMA_TIMEOUT);

    //
    // Call the software UART receive timer tick function, and see if the
    // timer should be disabled.
    //
    if(SoftUARTRxTick(&g_sUART, false) == SOFTUART_RXTIMER_END)
    {
        //
        // Disable the timer interrupt since the software UART doesn't need
        // it any longer.
        //
    }
}
```

```

        TimerDisable(TIMER0_BASE, TIMER_B);
    }
}

//
// The interrupt handler for the software UART GPIO edge interrupt.
//
void
GPIOIntHandler(void)
{
    //
    // Configure the software UART receive timer so that it samples at the
    // mid-bit time of this character.
    //
    TimerDisable(TIMER0_BASE, TIMER_B);
    TimerLoadSet(TIMER0_BASE, TIMER_B, g_ulBitTime);
    TimerIntClear(TIMER0_BASE, TIMER_TIMB_TIMEOUT);
    TimerEnable(TIMER0_BASE, TIMER_B);

    //
    // Call the software UART receive timer tick function.
    //
    SoftUARTRxTick(&g_sUART, true);
}

//
// The callback function for the software UART. This function is
// equivalent to the interrupt handler for a hardware UART.
//
void
UARTCallback(void)
{
    unsigned long ulInts;

    //
    // Read the asserted interrupt sources.
    //
    ulInts = SoftUARTIntStatus(&g_sUART, true);

    //
    // Clear the asserted interrupt sources.
    //
    SoftUARTIntClear(&g_sUART, ulInts);

    //
    // Handle the asserted interrupts.
    //
    ...
}

//
// Setup the software UART and send some data.
//
void
TestSoftUART(void)
{
    //
    // Initialize the software UART instance data.
    //
    SoftUARTInit(&g_sUART);

    //
    // Set the callback function used for this software UART.
    //
    SoftUARTCallbackSet(&g_sUART, UARTCallback);
}

```

```
//
// Configure the pins used for the software UART. This example uses
// pins PD0 and PE1.
//
SoftUARTTxGPIOSet(&g_sUART, GPIO_PORTD_BASE, GPIO_PIN_0);
SoftUARTRxGPIOSet(&g_sUART, GPIO_PORTE_BASE, GPIO_PIN_1);

//
// Configure the data buffers used as the transmit and receive buffers.
//
SoftUARTTxBufferSet(&g_sUART, g_pucTxBuffer, 16);
SoftUARTRxBufferSet(&g_sUART, g_pusRxBuffer, 16);

//
// Enable the GPIO modules that contains the GPIO pins to be used by
// the software UART.
//
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOE);

//
// Configure the software UART module: 8 data bits, no parity, and one
// stop bit.
//
SoftUARTConfigSet(&g_sUART,
                  (SOFTUART_CONFIG_WLEN_8 | SOFTUART_CONFIG_PAR_NONE |
                   SOFTUART_CONFIG_STOP_ONE));

//
// Compute the bit time for 38,400 baud.
//
g_ulBitTime = (SysCtlClockGet() / 38400) - 1;

//
// Configure the timers used to generate the timing for the software
// UART. The interface in this example is run at 38,400 baud,
// requiring a timer tick at 38,400 Hz.
//
SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER0);
TimerConfigure(TIMER0_BASE,
               (TIMER_CFG_16_BIT_PAIR | TIMER_CFG_A_PERIODIC |
                TIMER_CFG_B_PERIODIC));
TimerLoadSet(TIMER0_BASE, TIMER_A, g_ulBitTime);
TimerIntEnable(TIMER0_BASE, TIMER_TIMA_TIMEOUT | TIMER_TIMB_TIMEOUT);
TimerEnable(TIMER0_BASE, TIMER_A);

//
// Set the priorities of the interrupts associated with the software
// UART. The receiver is higher priority than the transmitter, and the
// receiver edge interrupt is higher priority than the receiver timer
// interrupt.
//
IntPrioritySet(INT_GPIOE, 0x00);
IntPrioritySet(INT_TIMER0B, 0x40);
IntPrioritySet(INT_TIMER0A, 0x80);

//
// Enable the interrupts associated with the software UART.
//
IntEnable(INT_GPIOE);
IntEnable(INT_TIMER0A);
IntEnable(INT_TIMER0B);

//
// Enable the transmit FIFO half full interrupt in the software UART.
//
```

```
SoftUARTIntEnable(&g_sUART, SOFTUART_INT_TX);

//
// Write some data into the software UART transmit FIFO.
//
SoftUARTCharPut(&g_sUART, 0x55);
SoftUARTCharPut(&g_sUART, 0xaa);
SoftUARTCharPut(&g_sUART, 0x55);
SoftUARTCharPut(&g_sUART, 0xaa);
SoftUARTCharPut(&g_sUART, 0x55);
SoftUARTCharPut(&g_sUART, 0xaa);
SoftUARTCharPut(&g_sUART, 0x55);
SoftUARTCharPut(&g_sUART, 0xaa);
SoftUARTCharPut(&g_sUART, 0x55);
SoftUARTCharPut(&g_sUART, 0xaa);
SoftUARTCharPut(&g_sUART, 0x55);
SoftUARTCharPut(&g_sUART, 0xaa);

//
// Wait until the software UART is idle. The transmit FIFO half full
// interrupt is sent to the callback function prior to exiting this
// loop.
//
while(SoftUARTBusy(&g_sUART))
{
}
}
```

As a comparison, the following is the equivalent code using the hardware UART module and the Stellaris Peripheral Driver Library.

```
//
// The interrupt handler for the hardware UART.
//
void
UART0IntHandler(void)
{
    unsigned long ulInts;

    //
    // Read the asserted interrupt sources.
    //
    ulInts = UARTIntStatus(UART0_BASE, true);

    //
    // Clear the asserted interrupt sources.
    //
    UARTIntClear(UART0_BASE, ulInts);

    //
    // Handle the asserted interrupts.
    //
    ...
}

//
// Setup the hardware UART and send some data.
//
void
TestUART(void)
{
    //
    // Enable the GPIO module that contains the GPIO pins to be used by
    // the UART, as well as the UART module.
    //
}
```

```
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA);
SysCtlPeripheralEnable(SYSCTL_PERIPH_UART0);

//
// Configure the GPIO pins for use by the UART module.
//
GPIOPinTypeUART(GPIO_PORTA_BASE, GPIO_PIN_0 | GPIO_PIN_1);

//
// Initialize the hardware UART module: 8 data bits, no parity, one stop
// bit, and 38,400 baud rate.
//
UARTConfigSetExpClk(UART0_BASE, SysCtlClockGet(), 38400,
                    (UART_CONFIG_WLEN_8 | UART_CONFIG_PAR_NONE |
                     UART_CONFIG_STOP_ONE));

//
// Enable the transmit FIFO half full interrupt in the hardware UART.
//
UARTIntEnable(UART0_BASE, UART_INT_TX);
IntEnable(INT_UART0);

//
// Write some data into the hardware UART transmit FIFO.
//
UARTCharPut(UART0_BASE, 0x55);
UARTCharPut(UART0_BASE, 0xaa);
UARTCharPut(UART0_BASE, 0x55);
UARTCharPut(UART0_BASE, 0xaa);
UARTCharPut(UART0_BASE, 0x55);
UARTCharPut(UART0_BASE, 0xaa);
UARTCharPut(UART0_BASE, 0x55);
UARTCharPut(UART0_BASE, 0xaa);
UARTCharPut(UART0_BASE, 0x55);
UARTCharPut(UART0_BASE, 0xaa);
UARTCharPut(UART0_BASE, 0x55);
UARTCharPut(UART0_BASE, 0xaa);

//
// Wait until the hardware UART is idle. The transmit FIFO half full
// interrupt is sent to the interrupt handler prior to exiting this
// loop.
//
while(UARTBusy(UART0_BASE))
{
}
}
```

30 Ethernet Software Update Module

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30.1 Introduction

The Ethernet software update module provides a convenient method of registering a callback which will be notified when a user attempts to initiate a firmware update over Ethernet using the LM Flash Programmer application. In addition to providing notification of an update request, the module also provides a function that can be called to initiate an update using the Ethernet boot loader.

To make use of this module, an application must include the lwIP TCP/IP stack and must be run on a system configured to use the Ethernet boot loader.

This module is contained in `utils/swupdate.c`, with `utils/swupdate.h` containing the API definitions for use by applications.

30.2 API Functions

Functions

- void [SoftwareUpdateBegin](#) (void)
- void [SoftwareUpdateInit](#) (tSoftwareUpdateRequested pfnCallback)

30.2.1 Function Documentation

30.2.1.1 SoftwareUpdateBegin

Passes control to the bootloader and initiates a remote software update over Ethernet.

Prototype:

```
void  
SoftwareUpdateBegin(void)
```

Description:

This function passes control to the bootloader and initiates an update of the main application firmware image via BOOTP across Ethernet. This function may only be used on parts supporting Ethernet and in cases where the Ethernet boot loader is in use alongside the main application image. It must not be called in interrupt context.

Applications wishing to make use of this function must be built to operate with the bootloader. If this function is called on a system which does not include the bootloader, the results are unpredictable.

Note:

It is not safe to call this function from within the callback provided on the initial call to [SoftwareUpdateInit\(\)](#). The application must use the callback to signal a pending update (assuming the update is to be permitted) to some other code running in a non-interrupt context.

Returns:

Never returns.

30.2.1.2 SoftwareUpdateInit

Initializes the remote Ethernet software update notification feature.

Prototype:

```
void  
SoftwareUpdateInit(tSoftwareUpdateRequested pfnCallback)
```

Parameters:

pfnCallback is a pointer to a function which will be called whenever a remote firmware update request is received. If the application wishes to allow the update to go ahead, it must call [SoftwareUpdateBegin\(\)](#) from non-interrupt context after the callback is received. Note that the callback will most likely be made in interrupt context so it is not safe to call [SoftwareUpdateBegin\(\)](#) from within the callback itself.

Description:

This function may be used on Ethernet-enabled parts to support remotely-signaled firmware updates over Ethernet. The LM Flash Programmer (LMFlash.exe) application sends a magic packet to UDP port 9 whenever the user requests an Ethernet-based firmware update. This packet consists of 6 bytes of 0xAA followed by the target MAC address repeated 4 times. This function starts listening on UDP port 9 and, if a magic packet matching the MAC address of this board is received, makes a call to the provided callback function to indicate that an update has been requested.

The callback function provided here will typically be called in the context of the lwIP Ethernet interrupt handler. It is not safe to call [SoftwareUpdateBegin\(\)](#) in this context so the application should use the callback to signal code running in a non-interrupt context to perform the update if it is to be allowed.

UDP port 9 is chosen for this function since this is the well-known port associated with “discard” operation. In other words, any other system receiving the magic packet will simply ignore it. The actual magic packet used is modeled on Wake-On-LAN which uses a similar structure (6 bytes of 0xFF followed by 16 repetitions of the target MAC address). Some Wake-On-LAN implementations also use UDP port 9 for their signaling.

Note:

Applications using this function must initialize the lwIP stack prior to making this call and must ensure that the `lwIPTimer()` function is called periodically. lwIP UDP must be enabled in `lwipopts.h` to ensure that the magic packets can be received.

Returns:

None.

30.3 Programming Example

The following example shows how to use the software update module.

```
//*****
//
// A flag used to indicate that an Ethernet remote firmware update request
// has been received.
//
//*****
volatile tBoolean g_bFirmwareUpdate = false;

//*****
//
// This function is called by the software update module whenever a remote
// host requests to update the firmware on this board. We set a flag that
// will cause the bootloader to be entered the next time the user enters a
// command on the console.
//
//*****
void
SoftwareUpdateRequestCallback(void)
{
    g_bFirmwareUpdate = true;
}

//*****
//
// The main entry point for the application. This function contains all
// hardware initialization code and also the main loop for the application.
//
//*****
int
main(void)
{
    unsigned char pucMACAddr[6];

    //
    // System clock initialization and reading of the MAC address into array
    // pucMACAddr occurs here. This code is omitted for clarity.
    //

    //
    // Initialize the lwIP TCP/IP stack.
    //
    lwIPInit(pucMACAddr, 0, 0, 0, IPADDR_USE_DHCP);

    //
    // Start the remote software update module.
    //
    SoftwareUpdateInit(SoftwareUpdateRequestCallback);

    //
    // Do whatever other setup things the application needs.
    //

    //
    // Loop until someone requests a remote firmware update.
    //
    while(!g_bFirmwareUpdate)
    {
        //
        // Perform your main loop functions here.
        //
    }
}
```

```
    }  
  
    //  
    // If we drop out, a remote firmware update request has been received.  
    // Transfer control to the bootloader which will perform the update.  
    //  
    SoftwareUpdateBegin();  
}
```

31 TFTP Server Module

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31.1 Introduction

The TFTP (tiny file transfer protocol) server module provides a simple way of transferring files to and from a system over an Ethernet connection. The general-purpose server module implements all the basic TFTP protocol and interacts with applications via a number of application-provided callback functions which are called when:

- A new file transfer request is received from a client.
- Another block of file data is required to satisfy an ongoing GET (read) request.
- A new block of data is received during an ongoing PUT (write) request.
- A file transfer has completed.

To make use of this module, an application must include the lwIP TCP/IP stack with UDP enabled in the `lwipopts.h` header file.

This module is contained in `utils/tftp.c`, with `utils/tftp.h` containing the API definitions for use by applications.

31.2 Usage

The TFTP server module handles the TFTP protocol on behalf of an application but the application using it is responsible for all file system interaction - reading and writing files in response to callbacks from the TFTP server. To make use of the module, an application must provide the following callback functions to the server.

pfnRequest (type `tTFTPRequest`) This function pointer is provided to the server as a parameter to the `TFTPInit()` function. It will be called whenever a new incoming TFTP request is received by the server and allows the application to determine whether the connection should be accepted or rejected.

pfnGetData (type `{tTFTPTransfer}`) This function is called to read each block of file data during an ongoing GET request. It must copy the requested number of bytes from a given position in the file into a supplied buffer. The application writes a pointer to this function into the `tTFTPConnection` instance data structure during processing of the `pfnRequest` callback if a GET request is to be accepted.

pfnPutData (type `tTFTPTransfer`) This function is called to write each block of file data during an ongoing PUT request. It must write the provided block of data into the target file. The application writes a pointer to this function into the `tTFTPConnection` instance data structure during processing of the `pfnRequest` callback if a PUT request is to be accepted.

pfnClose (type `tTFTPClose`) This function is called when a TFTP connection ends and allows the application to perform any cleanup required - freeing workspace memory and closing files, for example. The application writes a pointer to this function into the `tTFTPConnection` instance data structure during processing of the `pfnRequest` callback if the request is to be accepted.

31.2.0.3 pfnRequest

Application callback function called whenever a new TFTP request is received by the server.

Prototype:

```
tTFTPError  
pfnRequest(struct _tTFTPConnection *psTFTP, tBoolean bGet, char  
*pucFileName, tTFTPMode eMode)
```

Parameters:

psTFTP points to the TFTP connection instance data for the new request.

bGet is `true` if the incoming request is a GET (read) request or `false` if it is a PUT (write) request.

pucFileName points to the first character of the name of the local file which is to be read (on a GET request) or written (on a PUT request).

eMode indicates the requested transfer mode, `TFTP_MODE_NETASCII` (text) or `TFTP_MODE_OCTET` (binary).

Description:

This function, whose pointer is passed to the server as a parameter to function `TFTPInit()`, is called whenever a new TFTP request is received. It passes information about the request to the application allowing it to accept or reject it. The request type, GET or PUT, is determined from the `bGet` parameter and the target file name is provided in `pucFileName`.

If the application wishes to reject the request, it should set the `pcErrorString` field in the `psTFTP` structure and return an error code other than **TFTP_OK**.

To accept an incoming connection and start the file transfer, the application should return **TFTP_OK** after completing various fields in the `psTFTP` structure. For a GET request, fill in the `pfnGetData` and `pfnClose` function pointers and set `ulDataRemaining` to the size of the file which is being requested. For a PUT request, fill in the `pfnPutData` and `pfnClose` function pointers.

During processing of `pfnRequest`, the application may use the `pucUser` field as an anchor for any additional instance data required to process the request - a file handle, for example. This field will be accessible on all future callbacks related to this connection since the `psTFTP` structure is passed as a parameter in each case. Any resources allocated during `pfnRequest` can be freed during the later call to `pfnClose`.

Returns:

Returns **TFTP_OK** if the request is to be handled or any other TFTP error code if it is to be rejected.

31.2.0.4 pfnGetData

Application callback function called whenever the TFTP server needs another block of data read from the source file.

Prototype:

```
tTFTPError
pfnGetData(struct _tTFTPConnection *psTFTP)
```

Parameters:

psTFTP points to the TFTP connection instance data for the existing GET request.

Description:

This function, whose pointer was passed to the server in the `psTFTP` structure when the TFTP connection was accepted in `pfnRequest`, is called whenever the server needs a new block of file data to send back to the remote client. The application must copy a block of `psTFTP->ulDataLength` bytes of data from the source file to the buffer pointed to by `psTFTP->pucData`.

Typically, GET requests will read data sequentially from the file but, in some error recovery cases, data previously read may be requested again. The application must, therefore, ensure that the correct block of data is being returned by checking `psTFTP->ulBlockNum` and setting the source file offset correctly based on its value. The required read offset is `(psTFTP->ulBlockNum * TFTP_BLOCK_SIZE)` bytes from the start of the file.

If an error is detected while reading the file, field `psTFTP->pcErrorString` should be set and a value other than **TFTP_OK** returned.

Returns:

Returns **TFTP_OK** if the data was read successfully or any other TFTP error code if an error occurred.

31.2.0.5 pfnPutData

Application callback function called whenever the TFTP server has received data to be written to the destination file.

Prototype:

```
tTFTPError
pfnPutData(struct _tTFTPConnection *psTFTP)
```

Parameters:

psTFTP points to the TFTP connection instance data for the existing PUT request.

Description:

This function, whose pointer was passed to the server in the `psTFTP` structure when the TFTP connection was accepted in `pfnRequest`, is called whenever the server receives a block of data. The application must write a block of `psTFTP->ulDataLength` bytes of data from address `psTFTP->pucData` to the destination file.

Typically, PUT requests will write data sequentially to the file but, in some error recovery cases, data previously written may be received again. The application must, therefore, ensure that the received data is written at the correct position within the file. This position is determined from the fields `psTFTP->ulBlockNum` and `psTFTP->ulDataRemaining`. The byte offset relative

to the start of the file that the data must be written to is given by `((psTFTP->ulBlockNum - 1) * TFTP_BLOCK_SIZE) + psTFTP->ulDataRemaining`.

If an error is detected while writing the file, field `psTFTP->pcErrorString` should be set and a value other than **TFTP_OK** returned.

Returns:

Returns **TFTP_OK** if the data was written successfully or any other TFTP error code if an error occurred.

31.2.0.6 pfnClose

Application callback function called whenever the TFTP connection is being closed.

Prototype:

```
void  
pfnClose(struct _tTFTPConnection *psTFTP)
```

Parameters:

psTFTP points to the TFTP instance data block for the connection which is being closed.

Description:

This function, whose pointer was passed to the server in the `psTFTP` structure when the TFTP connection was accepted in `pfnRequest`, is called whenever the server is about to close the TFTP connection. An application may use it to free any resources allocated to service the connection (file handles, for example).

Returns:

None.

31.3 API Functions

Data Structures

- [_tTFTPConnection](#)

Defines

- [TFTP_BLOCK_SIZE](#)

Enumerations

- [tTFTPError](#)

Functions

- void [TFTPInit](#) (tTFTPRequest pfnRequest)

31.3.1 Data Structure Documentation

31.3.1.1 `_tTFTPConnection`

Definition:

```
typedef struct
{
    unsigned char *pucData;
    unsigned long ulDataLength;
    unsigned long ulDataRemaining;
    tTFTPTransfer pfnGetData;
    tTFTPTransfer pfnPutData;
    tTFTPClose pfnClose;
    unsigned char *pucUser;
    char *pcErrorString;
    udp_pcb *pPCB;
    unsigned long ulBlockNum;
}
_tTFTPConnection
```

Members:

pucData Pointer to the start of the buffer into which GET data should be copied or from which PUT data should be read.

ulDataLength The length of the data requested in response to a single pfnGetData callback or the size of the received data for a pfnPutData callback.

ulDataRemaining Count of remaining bytes to send during a GET request or the byte offset within a block during a PUT request. The application must set this field to the size of the requested file during the tTFTPRequest

pfnGetData Application function which is called whenever more data is required to satisfy a GET request. The function must copy ulDataLength bytes into the buffer pointed to by pucData.

pfnPutData Application function which is called whenever a packet of file data is received during a PUT request. The function must save the data to the target file using ulBlockNum and ulDataRemaining to indicate the position of the data in the file, and return an appropriate error code. Note that several calls to this function may be made for a given received TFTP block since the underlying networking stack may have split the TFTP packet between several packets and a callback is made for each of these. This avoids the need for a 512 byte buffer. The ulDataRemaining is used in these cases to indicate the offset of the data within the current block.

pfnClose Application function which is called when the TFTP connection is to be closed. The function should tidy up and free any resources associated with the connection prior to returning.

pucUser This field may be used by the client to store an application-specific pointer that will be accessible on all callbacks from the TFTP module relating to this connection.

pcErrorString Pointer to an error string which the client must fill in if reporting an error. This string will be sent to the TFTP client in any case where pfnPutData or pfnGetData return a value other than TFTP_OK.

pPCB A pointer to the underlying UDP connection. Applications must not modify this field.

ulBlockNum The current block number for an ongoing TFTP transfer. Applications may read this value to determine which data to return on a pfnGetData callback or where to write incoming data on a pfnPutData callback but must not modify it.

Description:

The TFTP connection control structure. This is passed to a client on all callbacks relating to a given TFTP connection. Depending upon the callback, the client may need to fill in values to various fields or use field values to determine where to transfer data from or to.

31.3.2 Define Documentation

31.3.2.1 TFTP_BLOCK_SIZE

Definition:

```
#define TFTP_BLOCK_SIZE
```

Description:

Data transfer under TFTP is performed using fixed-size blocks. This label defines the size of a block of TFTP data.

31.3.3 Typedef Documentation

31.3.3.1 tTFTPConnection

Definition:

```
typedef struct _tTFTPConnection tTFTPConnection
```

Description:

The TFTP connection control structure. This is passed to a client on all callbacks relating to a given TFTP connection. Depending upon the callback, the client may need to fill in values to various fields or use field values to determine where to transfer data from or to.

31.3.4 Enumeration Documentation

31.3.4.1 tTFTPError

Description:

TFTP error codes. Note that this enum is mapped so that all positive values match the TFTP protocol-defined error codes.

31.3.4.2 enum tTFTPMode

TFTP file transfer modes. This enum contains members defining ASCII text transfer mode (TFTP_MODE_NETASCII), binary transfer mode (TFTP_MODE_OCTET) and a marker for an invalid mode (TFTP_MODE_INVALID).

31.3.5 Function Documentation

31.3.5.1 void TFTPInit (tTFTPRequest *pfnRequest*)

Initializes the TFTP server module.

Parameters:

pfnRequest - A pointer to the function which the server will call whenever a new incoming TFTP request is received. This function must determine whether the request can be handled and return a value telling the server whether to continue processing the request or ignore it.

This function initializes the lwIP TFTP server and starts listening for incoming requests from clients. It must be called after the network stack is initialized using a call to [lwIPInit\(\)](#).

Returns:

None.

32 Micro Standard Library Module

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32.1 Introduction

The micro standard library module provides a set of small implementations of functions normally found in the C library. These functions provide reduced or greatly reduced functionality in order to remain small while still being useful for most embedded applications.

The following functions are provided, along with the C library equivalent:

Function	C library equivalent
<code>usprintf</code>	<code>sprintf</code>
<code>usnprintf</code>	<code>snprintf</code>
<code>uvsnprintf</code>	<code>vsnprintf</code>
<code>ustrnicmp</code>	<code>strnicmp</code>
<code>ustrtoul</code>	<code>strtoul</code>
<code>ustrstr</code>	<code>strstr</code>
<code>ulocaltime</code>	<code>localtime</code>

This module is contained in `utils/ustdlib.c`, with `utils/ustdlib.h` containing the API definitions for use by applications.

32.2 API Functions

Data Structures

- [tTime](#)

Functions

- void [ulocaltime](#) (unsigned long ulTime, [tTime](#) *psTime)
- int [usnprintf](#) (char *pcBuf, unsigned long ulSize, const char *pcString,...)
- int [usprintf](#) (char *pcBuf, const char *pcString,...)
- int [ustrcasecmp](#) (const char *pcStr1, const char *pcStr2)
- int [ustrnicmp](#) (const char *pcStr1, const char *pcStr2, int iCount)
- char * [ustrstr](#) (const char *pcHaystack, const char *pcNeedle)
- unsigned long [ustrtoul](#) (const char *pcStr, const char **ppcStrRet, int iBase)
- int [uvsnprintf](#) (char *pcBuf, unsigned long ulSize, const char *pcString, va_list vaArgP)

32.2.1 Data Structure Documentation

32.2.1.1 tTime

Definition:

```
typedef struct
{
    unsigned short usYear;
    unsigned char ucMon;
    unsigned char ucMday;
    unsigned char ucWday;
    unsigned char ucHour;
    unsigned char ucMin;
    unsigned char ucSec;
}
tTime
```

Members:

usYear The number of years since 0 AD.

ucMon The month, where January is 0 and December is 11.

ucMday The day of the month.

ucWday The day of the week, where Sunday is 0 and Saturday is 6.

ucHour The number of hours.

ucMin The number of minutes.

ucSec The number of seconds.

Description:

A structure that contains the broken down date and time.

32.2.2 Function Documentation

32.2.2.1 localtime

Converts from seconds to calendar date and time.

Prototype:

```
void
localtime(unsigned long ulTime,
           tTime *psTime)
```

Parameters:

ulTime is the number of seconds.

psTime is a pointer to the time structure that is filled in with the broken down date and time.

Description:

This function converts a number of seconds since midnight GMT on January 1, 1970 (traditional Unix epoch) into the equivalent month, day, year, hours, minutes, and seconds representation.

Returns:

None.

32.2.2.2 usnprintf

A simple snprintf function supporting %c, %d, %p, %s, %u, %x, and %X.

Prototype:

```
int
usnprintf(char *pcBuf,
          unsigned long ulSize,
          const char *pcString,
          ...)
```

Parameters:

pcBuf is the buffer where the converted string is stored.

ulSize is the size of the buffer.

pcString is the format string.

... are the optional arguments, which depend on the contents of the format string.

Description:

This function is very similar to the C library `sprintf()` function. Only the following formatting characters are supported:

- %c to print a character
- %d to print a decimal value
- %s to print a string
- %u to print an unsigned decimal value
- %x to print a hexadecimal value using lower case letters
- %X to print a hexadecimal value using upper case letters (not lower case letters as would typically be used)
- %p to print a pointer as a hexadecimal value
- %% to print out a % character

For %d, %p, %s, %u, %x, and %X, an optional number may reside between the % and the format character, which specifies the minimum number of characters to use for that value; if preceded by a 0 then the extra characters will be filled with zeros instead of spaces. For example, "%8d" will use eight characters to print the decimal value with spaces added to reach eight; "%08d" will use eight characters as well but will add zeros instead of spaces.

The type of the arguments after *pcString* must match the requirements of the format string. For example, if an integer was passed where a string was expected, an error of some kind will most likely occur.

The function will copy at most *ulSize* - 1 characters into the buffer *pcBuf*. One space is reserved in the buffer for the null termination character.

The function will return the number of characters that would be converted as if there were no limit on the buffer size. Therefore it is possible for the function to return a count that is greater than the specified buffer size. If this happens, it means that the output was truncated.

Returns:

Returns the number of characters that were to be stored, not including the NULL termination character, regardless of space in the buffer.

32.2.2.3 `usprintf`

A simple `sprintf` function supporting `%c`, `%d`, `%p`, `%s`, `%u`, `%x`, and `%X`.

Prototype:

```
int
usprintf(char *pcBuf,
         const char *pcString,
         ...)
```

Parameters:

pcBuf is the buffer where the converted string is stored.

pcString is the format string.

... are the optional arguments, which depend on the contents of the format string.

Description:

This function is very similar to the C library `sprintf()` function. Only the following formatting characters are supported:

- `%c` to print a character
- `%d` to print a decimal value
- `%s` to print a string
- `%u` to print an unsigned decimal value
- `%x` to print a hexadecimal value using lower case letters
- `%X` to print a hexadecimal value using upper case letters (not upper case letters as would typically be used)
- `%p` to print a pointer as a hexadecimal value
- `%%` to print out a `%` character

For `%d`, `%p`, `%s`, `%u`, `%x`, and `%X`, an optional number may reside between the `%` and the format character, which specifies the minimum number of characters to use for that value; if preceded by a 0 then the extra characters will be filled with zeros instead of spaces. For example, `"%8d"` will use eight characters to print the decimal value with spaces added to reach eight; `"%08d"` will use eight characters as well but will add zeros instead of spaces.

The type of the arguments after *pcString* must match the requirements of the format string. For example, if an integer was passed where a string was expected, an error of some kind will most likely occur.

The caller must ensure that the buffer *pcBuf* is large enough to hold the entire converted string, including the null termination character.

Returns:

Returns the count of characters that were written to the output buffer, not including the NULL termination character.

32.2.2.4 `ustrcasecmp`

Compares two strings without regard to case.

Prototype:

```
int
ustrcasecmp(const char *pcStr1,
             const char *pcStr2)
```

Parameters:

pcStr1 points to the first string to be compared.
pcStr2 points to the second string to be compared.

Description:

This function is very similar to the C library `strcasecmp()` function. It compares two strings without regard to case. The comparison ends if a terminating NULL character is found in either string. In this case, the shorter string is deemed the lesser.

Returns:

Returns 0 if the two strings are equal, -1 if *pcStr1* is less than *pcStr2* and 1 if *pcStr1* is greater than *pcStr2*.

32.2.2.5 `ustrnicmp`

Compares two strings without regard to case.

Prototype:

```
int
ustrnicmp(const char *pcStr1,
           const char *pcStr2,
           int iCount)
```

Parameters:

pcStr1 points to the first string to be compared.
pcStr2 points to the second string to be compared.
iCount is the maximum number of characters to compare.

Description:

This function is very similar to the C library `strnicmp()` function. It compares at most *iCount* characters of two strings without regard to case. The comparison ends if a terminating NULL character is found in either string before *iCount* characters are compared. In this case, the shorter string is deemed the lesser.

Returns:

Returns 0 if the two strings are equal, -1 if *pcStr1* is less than *pcStr2* and 1 if *pcStr1* is greater than *pcStr2*.

32.2.2.6 `ustrstr`

Finds a substring within a string.

Prototype:

```
char *
ustrstr(const char *pcHaystack,
        const char *pcNeedle)
```

Parameters:

pcHaystack is a pointer to the string that will be searched.

pcNeedle is a pointer to the substring that is to be found within *pcHaystack*.

Description:

This function is very similar to the C library `strstr()` function. It scans a string for the first instance of a given substring and returns a pointer to that substring. If the substring cannot be found, a NULL pointer is returned.

Returns:

Returns a pointer to the first occurrence of *pcNeedle* within *pcHaystack* or NULL if no match is found.

32.2.2.7 `ustrtoul`

Converts a string into its numeric equivalent.

Prototype:

```
unsigned long
ustrtoul(const char *pcStr,
         const char **ppcStrRet,
         int iBase)
```

Parameters:

pcStr is a pointer to the string containing the integer.

ppcStrRet is a pointer that will be set to the first character past the integer in the string.

iBase is the radix to use for the conversion; can be zero to auto-select the radix or between 2 and 16 to explicitly specify the radix.

Description:

This function is very similar to the C library `strtoul()` function. It scans a string for the first token (that is, non-white space) and converts the value at that location in the string into an integer value.

Returns:

Returns the result of the conversion.

32.2.2.8 `uvsnprintf`

A simple `vsnprintf` function supporting `%c`, `%d`, `%p`, `%s`, `%u`, `%x`, and `%X`.

Prototype:

```
int
uvsnprintf(char *pcBuf,
            unsigned long ulSize,
            const char *pcString,
            va_list vaArgP)
```

Parameters:

pcBuf points to the buffer where the converted string is stored.

ulSize is the size of the buffer.

pcString is the format string.

vaArgP is the list of optional arguments, which depend on the contents of the format string.

Description:

This function is very similar to the C library `vsnprintf()` function. Only the following formatting characters are supported:

- `%c` to print a character
- `%d` to print a decimal value
- `%s` to print a string
- `%u` to print an unsigned decimal value
- `%x` to print a hexadecimal value using lower case letters
- `%X` to print a hexadecimal value using upper case letters (not upper case letters as would typically be used)
- `%p` to print a pointer as a hexadecimal value
- `%%` to print out a `%` character

For `%d`, `%p`, `%s`, `%u`, `%x`, and `%X`, an optional number may reside between the `%` and the format character, which specifies the minimum number of characters to use for that value; if preceded by a 0 then the extra characters will be filled with zeros instead of spaces. For example, `"%8d"` will use eight characters to print the decimal value with spaces added to reach eight; `"%08d"` will use eight characters as well but will add zeroes instead of spaces.

The type of the arguments after *pcString* must match the requirements of the format string. For example, if an integer was passed where a string was expected, an error of some kind will most likely occur.

The *ulSize* parameter limits the number of characters that will be stored in the buffer pointed to by *pcBuf* to prevent the possibility of a buffer overflow. The buffer size should be large enough to hold the expected converted output string, including the null termination character.

The function will return the number of characters that would be converted as if there were no limit on the buffer size. Therefore it is possible for the function to return a count that is greater than the specified buffer size. If this happens, it means that the output was truncated.

Returns:

Returns the number of characters that were to be stored, not including the NULL termination character, regardless of space in the buffer.

32.3 Programming Example

The following example shows how to use some of the micro standard library functions.

```
unsigned long ulValue;
char pcBuffer[32];
tTime sTime;

//
// Convert the number in pcBuffer (previous read from somewhere) into an
// integer. Note that this supports converting decimal values (such as
// 4583), octal values (such as 036583), and hexadecimal values (such as
// 0x3425).
```

```
//
ulValue = strtoul(pcBuffer, 0, 0);

//
// Convert that integer from a number of seconds into a broken down date.
//
ulocaltime(ulValue, &sTime);

//
// Print out the corresponding time of day in military format.
//
usprintf(pcBuffer, "%02d:%02d", sTime.ucHour, sTime.ucMin);
```

33 UART Standard IO Module

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33.1 Introduction

The UART standard IO module provides a simple interface to a UART that is similar to the standard IO package available in the C library. Only a very small subset of the normal functions are provided; [UARTprintf\(\)](#) is an equivalent to the C library `printf()` function and [UARTgets\(\)](#) is an equivalent to the C library `fgets()` function.

This module is contained in `utils/uartstdio.c`, with `utils/uartstdio.h` containing the API definitions for use by applications.

33.1.1 Unbuffered Operation

Unbuffered operation is selected by not defining **UART_BUFFERED** when building the UART standard IO module. In unbuffered mode, calls to the module will not return until the operation has been completed. So, for example, a call to [UARTprintf\(\)](#) will not return until the entire string has been placed into the UART's FIFO. If it is not possible for the function to complete its operation immediately, it will busy wait.

33.1.2 Buffered Operation

Buffered operation is selected by defining **UART_BUFFERED** when building the UART standard IO module. In buffered mode, there is a larger UART data FIFO in SRAM that extends the size of the hardware FIFO. Interrupts from the UART are used to transfer data between the SRAM buffer and the hardware FIFO. It is the responsibility of the application to ensure that [UARTStdioIntHandler\(\)](#) is called when the UART interrupt occurs; typically this is accomplished by placing it in the vector table in the startup code for the application.

In addition to providing a larger UART buffer, the behavior of [UARTprintf\(\)](#) is slightly modified. If the output buffer is full, [UARTprintf\(\)](#) will discard the remaining characters from the string instead of waiting until space becomes available in the buffer. If this behavior is not desired, [UARTFlushTx\(\)](#) may be called to ensure that the transmit buffer is emptied prior to adding new data via [UARTprintf\(\)](#) (though this will not work if the string to be printed is larger than the buffer).

[UARTPeek\(\)](#) can be used to determine whether a line end is present prior to calling [UARTgets\(\)](#) if a non-blocking operation is required. In cases where the buffer supplied on [UARTgets\(\)](#) fills before a line termination character is received, the call will return with a full buffer.

33.2 API Functions

Functions

- void [UARTEchoSet](#) (tBoolean bEnable)
- void [UARTFlushRx](#) (void)
- void [UARTFlushTx](#) (tBoolean bDiscard)
- unsigned char [UARTgetc](#) (void)
- int [UARTgets](#) (char *pcBuf, unsigned long ulLen)
- int [UARTPeek](#) (unsigned char ucChar)
- void [UARTprintf](#) (const char *pcString,...)
- int [UARTRxBytesAvail](#) (void)
- void [UARTStdioInit](#) (unsigned long ulPortNum)
- void [UARTStdioInitExpClk](#) (unsigned long ulPortNum, unsigned long ulBaud)
- void [UARTStdioIntHandler](#) (void)
- int [UARTTxBytesFree](#) (void)
- int [UARTwrite](#) (const char *pcBuf, unsigned long ulLen)

33.2.1 Function Documentation

33.2.1.1 UARTEchoSet

Enables or disables echoing of received characters to the transmitter.

Prototype:

```
void
UARTEchoSet (tBoolean bEnable)
```

Parameters:

bEnable must be set to **true** to enable echo or **false** to disable it.

Description:

This function, available only when the module is built to operate in buffered mode using **UART_BUFFERED**, may be used to control whether or not received characters are automatically echoed back to the transmitter. By default, echo is enabled and this is typically the desired behavior if the module is being used to support a serial command line. In applications where this module is being used to provide a convenient, buffered serial interface over which application-specific binary protocols are being run, however, echo may be undesirable and this function can be used to disable it.

Returns:

None.

33.2.1.2 UARTFlushRx

Flushes the receive buffer.

Prototype:

```
void
UARTFlushRx(void)
```

Description:

This function, available only when the module is built to operate in buffered mode using **UART_BUFFERED**, may be used to discard any data received from the UART but not yet read using [UARTgets\(\)](#).

Returns:

None.

33.2.1.3 UARTFlushTx

Flushes the transmit buffer.

Prototype:

```
void
UARTFlushTx(tBoolean bDiscard)
```

Parameters:

bDiscard indicates whether any remaining data in the buffer should be discarded (**true**) or transmitted (**false**).

Description:

This function, available only when the module is built to operate in buffered mode using **UART_BUFFERED**, may be used to flush the transmit buffer, either discarding or transmitting any data received via calls to [UARTprintf\(\)](#) that is waiting to be transmitted. On return, the transmit buffer will be empty.

Returns:

None.

33.2.1.4 UARTgetc

Read a single character from the UART, blocking if necessary.

Prototype:

```
unsigned char
UARTgetc(void)
```

Description:

This function will receive a single character from the UART and store it at the supplied address.

In both buffered and unbuffered modes, this function will block until a character is received. If non-blocking operation is required in buffered mode, a call to [UARTRxAvail\(\)](#) may be made to determine whether any characters are currently available for reading.

Returns:

Returns the character read.

33.2.1.5 UARTgets

A simple UART based get string function, with some line processing.

Prototype:

```
int
UARTgets(char *pcBuf,
          unsigned long ulLen)
```

Parameters:

pcBuf points to a buffer for the incoming string from the UART.

ulLen is the length of the buffer for storage of the string, including the trailing 0.

Description:

This function will receive a string from the UART input and store the characters in the buffer pointed to by *pcBuf*. The characters will continue to be stored until a termination character is received. The termination characters are CR, LF, or ESC. A CRLF pair is treated as a single termination character. The termination characters are not stored in the string. The string will be terminated with a 0 and the function will return.

In both buffered and unbuffered modes, this function will block until a termination character is received. If non-blocking operation is required in buffered mode, a call to [UARTPeek\(\)](#) may be made to determine whether a termination character already exists in the receive buffer prior to calling [UARTgets\(\)](#).

Since the string will be null terminated, the user must ensure that the buffer is sized to allow for the additional null character.

Returns:

Returns the count of characters that were stored, not including the trailing 0.

33.2.1.6 UARTPeek

Looks ahead in the receive buffer for a particular character.

Prototype:

```
int
UARTPeek(unsigned char ucChar)
```

Parameters:

ucChar is the character that is to be searched for.

Description:

This function, available only when the module is built to operate in buffered mode using **UART_BUFFERED**, may be used to look ahead in the receive buffer for a particular character and report its position if found. It is typically used to determine whether a complete line of user input is available, in which case ucChar should be set to CR ('\r') which is used as the line end marker in the receive buffer.

Returns:

Returns -1 to indicate that the requested character does not exist in the receive buffer. Returns a non-negative number if the character was found in which case the value represents the position of the first instance of *ucChar* relative to the receive buffer read pointer.

33.2.1.7 UARTprintf

A simple UART based printf function supporting %c, %d, %p, %s, %u, %x, and %X.

Prototype:

```
void
UARTprintf(const char *pcString,
           ...)
```

Parameters:

pcString is the format string.

... are the optional arguments, which depend on the contents of the format string.

Description:

This function is very similar to the C library `fprintf()` function. All of its output will be sent to the UART. Only the following formatting characters are supported:

- %c to print a character
- %d to print a decimal value
- %s to print a string
- %u to print an unsigned decimal value
- %x to print a hexadecimal value using lower case letters
- %X to print a hexadecimal value using upper case letters (not lower case letters as would typically be used)
- %p to print a pointer as a hexadecimal value
- %% to print out a % character

For %s, %d, %u, %p, %x, and %X, an optional number may reside between the % and the format character, which specifies the minimum number of characters to use for that value; if preceded by a 0 then the extra characters will be filled with zeros instead of spaces. For example, “%8d” will use eight characters to print the decimal value with spaces added to reach eight; “%08d” will use eight characters as well but will add zeroes instead of spaces.

The type of the arguments after *pcString* must match the requirements of the format string. For example, if an integer was passed where a string was expected, an error of some kind will most likely occur.

Returns:

None.

33.2.1.8 UARTRxBytesAvail

Returns the number of bytes available in the receive buffer.

Prototype:

```
int
UARTRxBytesAvail(void)
```

Description:

This function, available only when the module is built to operate in buffered mode using **UART_BUFFERED**, may be used to determine the number of bytes of data currently available in the receive buffer.

Returns:

Returns the number of available bytes.

33.2.1.9 UARTStdioInit

Initializes the UART console.

Prototype:

```
void
UARTStdioInit(unsigned long ulPortNum)
```

Parameters:

ulPortNum is the number of UART port to use for the serial console (0-2)

Description:

This function will initialize the specified serial port to be used as a serial console. The serial parameters will be set to 115200, 8-N-1. An application wishing to use a different baud rate may call [UARTStdioInitExpClk\(\)](#) instead of this function.

This function or [UARTStdioInitExpClk\(\)](#) must be called prior to using any of the other UART console functions: [UARTprintf\(\)](#) or [UARTgets\(\)](#). In order for this function to work correctly, [SysCtlClockSet\(\)](#) must be called prior to calling this function.

It is assumed that the caller has previously configured the relevant UART pins for operation as a UART rather than as GPIOs.

Returns:

None.

33.2.1.10 UARTStdioInitExpClk

Initializes the UART console and allows the baud rate to be selected.

Prototype:

```
void
UARTStdioInitExpClk(unsigned long ulPortNum,
                    unsigned long ulBaud)
```

Parameters:

ulPortNum is the number of UART port to use for the serial console (0-2)

ulBaud is the bit rate that the UART is to be configured to use.

Description:

This function will initialize the specified serial port to be used as a serial console. The serial parameters will be set to 8-N-1 and the bit rate set according to the value of the *ulBaud* parameter.

This function or [UARTStdioInit\(\)](#) must be called prior to using any of the other UART console functions: [UARTprintf\(\)](#) or [UARTgets\(\)](#). In order for this function to work correctly, [SysCtlClockSet\(\)](#) must be called prior to calling this function. An application wishing to use 115,200 baud may call [UARTStdioInit\(\)](#) instead of this function but should not call both functions.

It is assumed that the caller has previously configured the relevant UART pins for operation as a UART rather than as GPIOs.

Returns:

None.

33.2.1.11 UARTStdioIntHandler

Handles UART interrupts.

Prototype:

```
void
UARTStdioIntHandler(void)
```

Description:

This function handles interrupts from the UART. It will copy data from the transmit buffer to the UART transmit FIFO if space is available, and it will copy data from the UART receive FIFO to the receive buffer if data is available.

Returns:

None.

33.2.1.12 UARTTxBytesFree

Returns the number of bytes free in the transmit buffer.

Prototype:

```
int
UARTTxBytesFree(void)
```

Description:

This function, available only when the module is built to operate in buffered mode using **UART_BUFFERED**, may be used to determine the amount of space currently available in the transmit buffer.

Returns:

Returns the number of free bytes.

33.2.1.13 UARTwrite

Writes a string of characters to the UART output.

Prototype:

```
int
UARTwrite(const char *pcBuf,
          unsigned long ulLen)
```

Parameters:

pcBuf points to a buffer containing the string to transmit.

ulLen is the length of the string to transmit.

Description:

This function will transmit the string to the UART output. The number of characters transmitted is determined by the *ulLen* parameter. This function does no interpretation or translation of any characters. Since the output is sent to a UART, any LF (/n) characters encountered will be replaced with a CRLF pair.

Besides using the *ulLen* parameter to stop transmitting the string, if a null character (0) is encountered, then no more characters will be transmitted and the function will return.

In non-buffered mode, this function is blocking and will not return until all the characters have been written to the output FIFO. In buffered mode, the characters are written to the UART transmit buffer and the call returns immediately. If insufficient space remains in the transmit buffer, additional characters are discarded.

Returns:

Returns the count of characters written.

33.3 Programming Example

The following example shows how to use the UART standard IO module to write a string to the UART “console”.

```
//  
// Configure the appropriate pins as UART pins; in this case, PA0/PA1 are  
// used for UART0.  
//  
SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA);  
GPIOPinTypeUART(GPIO_PORTA_BASE, GPIO_PIN_0 | GPIO_PIN_1);  
  
//  
// Initialize the UART standard IO module.  
//  
UARTStdioInit(0);  
  
//  
// Print a string.  
//  
UARTprintf("Hello world!\n");
```

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