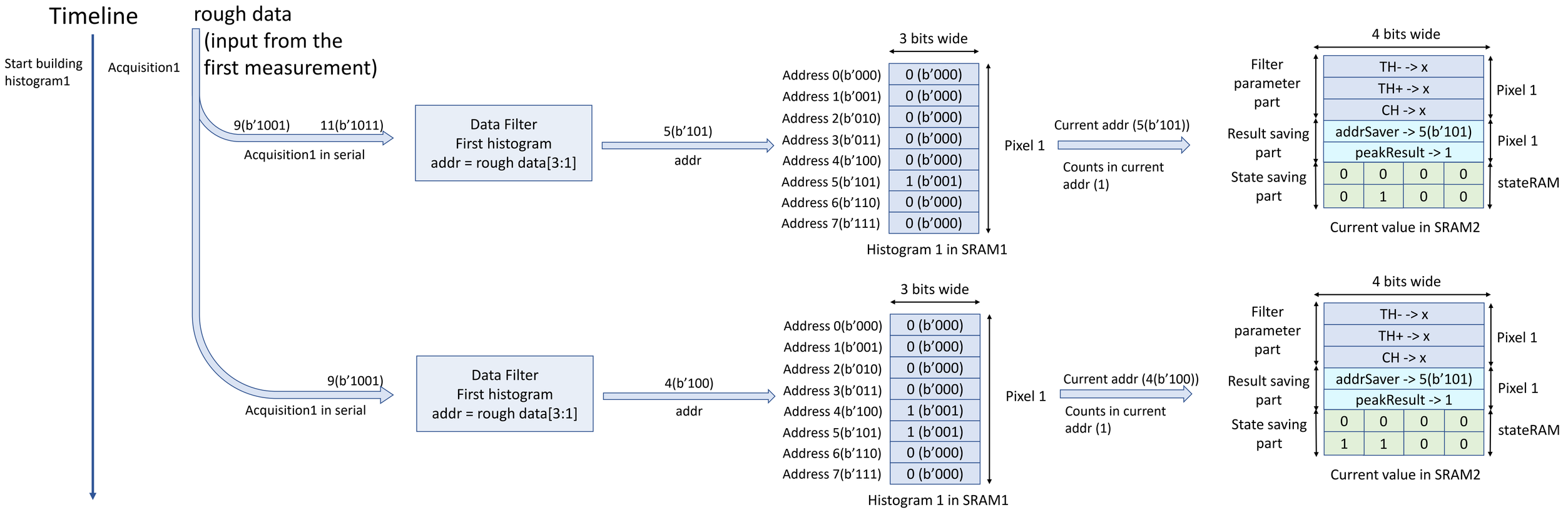
Timeline
Start building histogram1
Acquisition1
rough data (input from the first measurement)
9(b'1001)
11(b'1011)
Acquisition1 in serial
Data Filter
First histogram
addr = rough data[3:1]
5(b'101)
addr
3 bits wide
Address 0(b'000) 0 (b'000)
Address 1(b'001) 0 (b'000)
Address 2(b'010) 0 (b'000)
Address 3(b'011) 0 (b'000)
Address 4(b'100) 0 (b'000)
Address 5(b'101) 1 (b'001)
Address 6(b'110) 0 (b'000)
Address 7(b'111) 0 (b'000)
Histogram 1 in SRAM1
Pixel 1
Current addr (5(b'101))
Counts in current addr (1)
4 bits wide
Filter parameter part
Result saving part
State saving part
TH- -> x
TH+ -> x
CH -> x
addrSaver -> 5(b'101)
peakResult -> 1
0 0 0 0
0 1 0 0
Pixel 1
Pixel 1
stateRAM
Current value in SRAM2
9(b'1001)
Acquisition1 in serial
Data Filter
First histogram
addr = rough data[3:1]
4(b'100)
addr
3 bits wide
Address 0(b'000) 0 (b'000)
Address 1(b'001) 0 (b'000)
Address 2(b'010) 0 (b'000)
Address 3(b'011) 0 (b'000)
Address 4(b'100) 1 (b'001)
Address 5(b'101) 1 (b'001)
Address 6(b'110) 0 (b'000)
Address 7(b'111) 0 (b'000)
Histogram 1 in SRAM1
Pixel 1
Current addr (4(b'100))
Counts in current addr (1)
4 bits wide
Filter parameter part
Result saving part
State saving part
TH- -> x
TH+ -> x
CH -> x
addrSaver -> 5(b'101)
peakResult -> 1
0 0 0 0
1 1 0 0
Pixel 1
Pixel 1
stateRAM
Current value in SRAM2