		Rev 0.1	MRD				
<u> </u>							
ives out the "DA	TAout" bit to the	Kilinx JTAG progran	nmer				
	\						
connects to the	e Xilinx JTAG prog	grammer.					
E to at a sinte fa	n the LTAC Detain			4 :Daha (4 4)			
· · · · · · · · · · · · · · · · · · ·		<u> </u>			non around nine	to sook other. F	\
odd numbered	pins are ground,	even pins are the si	gnais. Do NOT s	inort any or these	non-ground pins	to each other. E	ver.
4 tost points fo	ur the ITAG data o	utoute from the last	1 1 iRobe (5, 8)				
		•		hort any of these	non-ground nins	to each other F	ver
odd Hambered	pino are ground,	even pins are the si	grais. Bortor c	Tion any or those	Tion ground pins	l cach other. L	VOI.
4 bypass point	s to optionally shu	nt the data from iBo	obs 1 4 to the ne	xt iBob (2 5) or iu	Imper back to the	programming po	nd
· Sypaco po				(2.1.0) (2.1.0) (3.1)			
4 bypass point	s to optionally shu	nt the data from iBo	obs 58 to the ne	xt iBob (68) or b	ack to the progra	mming pod	
						l l	
0.1" pitch	eg Kobiconn 11	51-8000 same par	t number at www	.mouser.com			
is used to deliv	er the final iBob's	JTAG data out pin	to the Xilinx prog	rammer when the	ere are less than 8	8 iBobs in use.	
		<u>.</u>					
Sample crimp	terminal part num	ber is Molex 098658	81211 but there a	re many others th	nat will work		
iBob 1	iBob 2	iBob 3	iBob 4	iBob 5	iBob 6	iBob 7	iBob 8
first							last
J2	J3	J4	J5	J6	J7	J8	J9
		1 to 2	3 to 4	5 to 6	7 to 8		
J13 0 ohm shu	ınts	1 to 2	3 to 4	5 to 6	7 to 8		
jumper		Not used					
						J8	J9: not use
				5 to 6	7 to 8		
J13 0 ohm shu	ınts		3 to 4				
jumper		J13-5 to J13-8					
						J8: not used	J9: not use
				5 to 6	7 to 8		
-	ınts		3 to 4				
jumper		J13-3 to J13-8					
10	10	14	le le	10	17(10	10
						J8: not used	J9: not use
			3 to 4	5 10 6	7 10 8		
	ints						
Jumper		J13-1 t0 J13-8					
12	12	14	15	I6: not used	I7: not used	19: not used	J9: not use
					37. Hot useu	Jo. Hot useu	39. Hot use
			3 10 4	3 10 0			
	1110						
Jampoi		5.1.1.00700					
J2	J3	J4	J5: not used	J6: not used	J7: not used	J8: not used	J9: not use
		none					
' '							
J2	J3	J4: not used	J5: not used	J6: not used	J7: not used	J8: not used	J9: not use
J11 0 ohm shu	ints	1 to 2					
J13 0 ohm shu	ints	none					
jumper		J11-3 to J13-8					
J2	J3: not used	J4: not used	J5: not used	J6: not used	J7: not used	J8: not used	J9: not use
+		none					
J13 0 ohm shu	ints	none					
jumper		J11-1 to J13-8					
Julipel			1	1	1	1	1
iBobs connecte		er board even if the	•				
iBobs connecte		er board even if the d even if those iBob	•		AG chain.		
	ives out the "DA connectors connects to the odd numbered 4 test points for odd numbered 4 bypass point 4 bypass point 4 bypass point 5 about 4" lo Sample crimp iBob 1 first 12 J11 0 ohm shu jumper J2 J11 0 ohm shu jumper J	ives out the "DATAout" bit to the xiconnectors connectors connects to the Xilinx JTAG process 5 test points for the JTAG Datain odd numbered pins are ground, 4 test points for the JTAG data condend to odd numbered pins are ground, 4 bypass points to optionally shuth of the xiconomic odd numbered pins are ground, 4 bypass points to optionally shuth of the xiconomic odd numbered pins are ground, 4 bypass points to optionally shuth of the xiconomic odd numbered pins are ground, 5 test points for the JTAG data of the xiconomic odd numbered pins are ground, 4 bypass points to optionally shuth of the xiconomic odd numbered pins are ground, 4 bypass points to optionally shuth of the xiconomic odd numbered pins are ground, 5 test points for the JTAG Datain odd numbered pins are ground, 4 test points for the JTAG Datain odd numbered pins are ground, 5 test points for the JTAG Datain odd numbered pins are ground, 6 test points for the JTAG Datain odd numbered pins are ground, 6 test points for the JTAG Datain odd numbered pins are ground, 6 test points for the JTAG Datain odd numbered pins are ground, 7 test points for the JTAG Datain odd numbered pins are ground, 8 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test points for the JTAG Datain odd numbered pins are ground, 9 test poi	programmer drives out the "DATAin" bit to the first inves out the "DATAout" bit to the Xilinx JTAG programmer connectors connects to the Xilinx JTAG programmer. 5 test points for the JTAG Datain signal and the out odd numbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and unmbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the signal and the out odd numbered pins are ground, even pins are the si	programmer drives out the "DATAin" bit to the first iBob. ves out the "DATAout" bit to the Kilinx JTAG programmer connectors connects to the Xilinx JTAG programmer. 5 test points for the JTAG Datain signal and the outputs from the first odd numbered pins are ground, even pins are the signals. Do NOT's 4 test points for the JTAG data outputs from the last 4 iBobs (58) odd numbered pins are ground, even pins are the signals. Do NOT's 4 test points for the JTAG data outputs from the last 4 iBobs (58) odd numbered pins are ground, even pins are the signals. Do NOT's 4 bypass points to optionally shunt the data from iBobs 14 to the ner 4 bypass points to optionally shunt the data from iBobs 58 to the ner 5 test points to optionally shunt the data from iBobs 58 to the ner 6 bypass points to optionally shunt the data from iBobs 58 to the ner 7 bypass points to optionally shunt the data from iBobs 58 to the ner 8 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shunt the data from iBobs 58 to the ner 9 bypass points to optionally shun	programmer drives out the "DATAin" bit to the first IBob. ves out the "DATAou" bit to the Xilinx JTAG programmer connectors connects to the Xilinx JTAG programmer. 5 test points for the JTAG Datain signal and the outputs from the first 4 iBobs (14) odd numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of number detection in the last 4 iBobs (58) and of numbered pins are ground, even pins are the signals. Do NOT short any of these detection of number and number is detection in the last 4 iBobs (58) and number and number is detection in the last 4 iBobs (58) and number and number is detection in the last 4 iBobs (58) and number and number and number at www.mouser.com 1.1 by pass points to optionally shunt the data from iBobs 58 to the next iBob (68) or be supported by the section of the section o	programmer drives out the "DATAIn" bit to the first IBob. ves out the "DATAout" bit to the Xilinx JTAG programmer Jonnectors Stest points for the JTAG programmer. 5 test points for the JTAG Datain signal and the outputs from the first 4 IBobs (14) odd numbered pins are ground, even pins are the signals. Do NOT short any of these non-ground pins 4 test points for the JTAG data outputs from the last 4 IBobs (58) odd numbered pins are ground, even pins are the signals. Do NOT short any of these non-ground pins 4 test points for the JTAG data outputs from IBobs 14 to the next IBob (25) or jumper back to the 4 bypass points to optionally shunt the data from IBobs 14 to the next IBob (68) or back to the progra 4 bypass points to optionally shunt the data from IBobs 58 to the next IBob (68) or back to the progra 5 test points to optionally shunt the data from IBobs 58 to the next IBob (68) or back to the progra 6 the programmer when the programmer when there are less than it is about 4" long 24 AWG with receptacle crimp terminal for 0.025" square pins at both ends 6 the programmer when there are less than it is about 4" long 24 AWG with receptacle crimp terminal for 0.025" square pins at both ends 6 the programmer when there are less than it is about 4" long 24 AWG with receptacle crimp terminal for 0.025" square pins at both ends 6 the programmer when there are less than it is about 4" long 24 AWG with receptacle crimp terminal for 0.025" square pins at both ends 6 the programmer when there are many others that will work 1 to 2	programmer drives out the "DATAou" bit to the XIIInx JTAG programmer connects to the XIIInx JTAG programmer. 5 test points for the JTAG Datain signal and the outputs from the first 4 iBobs (1, 4) odd numbered pins are ground, even pins are the signals. Do NOT short any of these non-ground pins to each other. E 4 test points for the JTAG Datain signal and the outputs from the first 4 iBobs (1, 4) odd numbered pins are ground, even pins are the signals. Do NOT short any of these non-ground pins to each other. E 4 test points for the JTAG data outputs from the last 4 iBobs (5, 8) odd numbered pins are ground, even pins are the signals. Do NOT short any of these non-ground pins to each other. E 4 bypass points to optionally shout the data from iBobs 1. 4 to the next iBob (2, 5) or jumper back to the programming pod 4 bypass points to optionally shout the data from iBobs 5. 8 to the next iBob (2, 5) or jumper back to the programming pod 4 bypass points to optionally shout the data from iBobs 5. 8 to the next iBob (2, 5) or jumper back to the programming pod 4 bypass points to optionally shout the data from iBobs 5. 8 to the next iBob (2, 5) or jumper back to the programming pod 4 bypass points to optionally shout the data from iBobs 5. 8 to the next iBob (6, 8) or back to the programming pod 4 bypass points to optionally shout the data from iBobs 5. 8 to the next iBob (6, 8) or back to the programming pod 5 to the programming pod 6 to the programming