```
1    NET "Clock" LOC = "p1"|IOSTANDARD=LVCMOS33; # CMOD-P35 - GCK2, Global Clock Input
2
3    #System level constraints
4    Net Clock TNM_NET = Clock;
5    Timespec TS_Clock = PERIOD Clock 10us; #adjust to suit Clock, 10us->100kHz
6
7    #Reset pin
8    NET "Reset" LOC = "p30"|IOSTANDARD=LVCMOS33; # CMOD-P18 - GCK2, Global Set/Reset
9    Net "Reset" TIG;
10
11   NET "Train" LOC = "p33"|IOSTANDARD=LVCMOS33; # CMOD-P24 - GTS0, Global Tristate Control
12   NET "CarNS" LOC = "p34"|IOSTANDARD=LVCMOS33; # CMOD-P25 - GTS1, Global Tristate Control
13
14   NET "PedNS" LOC = "p37"|IOSTANDARD=LVCMOS33; # CMOD-P27 - I/O
15
16   NET "HLights<1>" LOC = "p39"|IOSTANDARD=LVCMOS33; # CMOD-P29 - I/O
17   NET "HLights<0>" LOC = "p40"|IOSTANDARD=LVCMOS33; # CMOD-P30 - I/O
18
19   NET "VLights<1>" LOC = "p41"|IOSTANDARD=LVCMOS33; # CMOD-P31 - I/O
20   NET "VLights<0>" LOC = "p42"|IOSTANDARD=LVCMOS33; # CMOD-P32 - I/O
21
22   NET "Motor<0>" LOC = "p21"|IOSTANDARD=LVCMOS33; # CMOD-P9 - I/O
23   NET "Motor<1>" LOC = "p20"|IOSTANDARD=LVCMOS33; # CMOD-P10 - I/O
24   NET "Motor<2>" LOC = "p19"|IOSTANDARD=LVCMOS33; # CMOD-P11 - I/O
25   NET "Motor<3>" LOC = "p18"|IOSTANDARD=LVCMOS33; # CMOD-P12 - I/O
```