

Summary

Design Name	Traffic
Fitting Status	Successful
Software Version	P.20131013
Device Used	XC2C64A-7-VQ44
Date	5-24-2021, 5:35PM

RESOURCES SUMMARY

Macrocells Used	Pterms Used	Registers Used	Pins Used	Function Block Inputs Used
48/64 (75%)	129/224 (58%)	35/64 (55%)	13/33 (40%)	76/160 (48%)

PIN RESOURCES

Signal Type	Required	Mapped	Pin Type	Used	Total
Input	3	3	I/O	9	26
Output	8	8	GCK/IO	1	3
Bidirectional	0	0	GTS/IO	2	4
GCK	1	1	GSR/IO	1	1
GTS	0	0	DGE/IO	0	-1
GSR	1	1			

GLOBAL RESOURCES

Signal mapped onto global clock net (GCK2)	Clock
Signal mapped onto global output enable net (GSR)	Reset

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