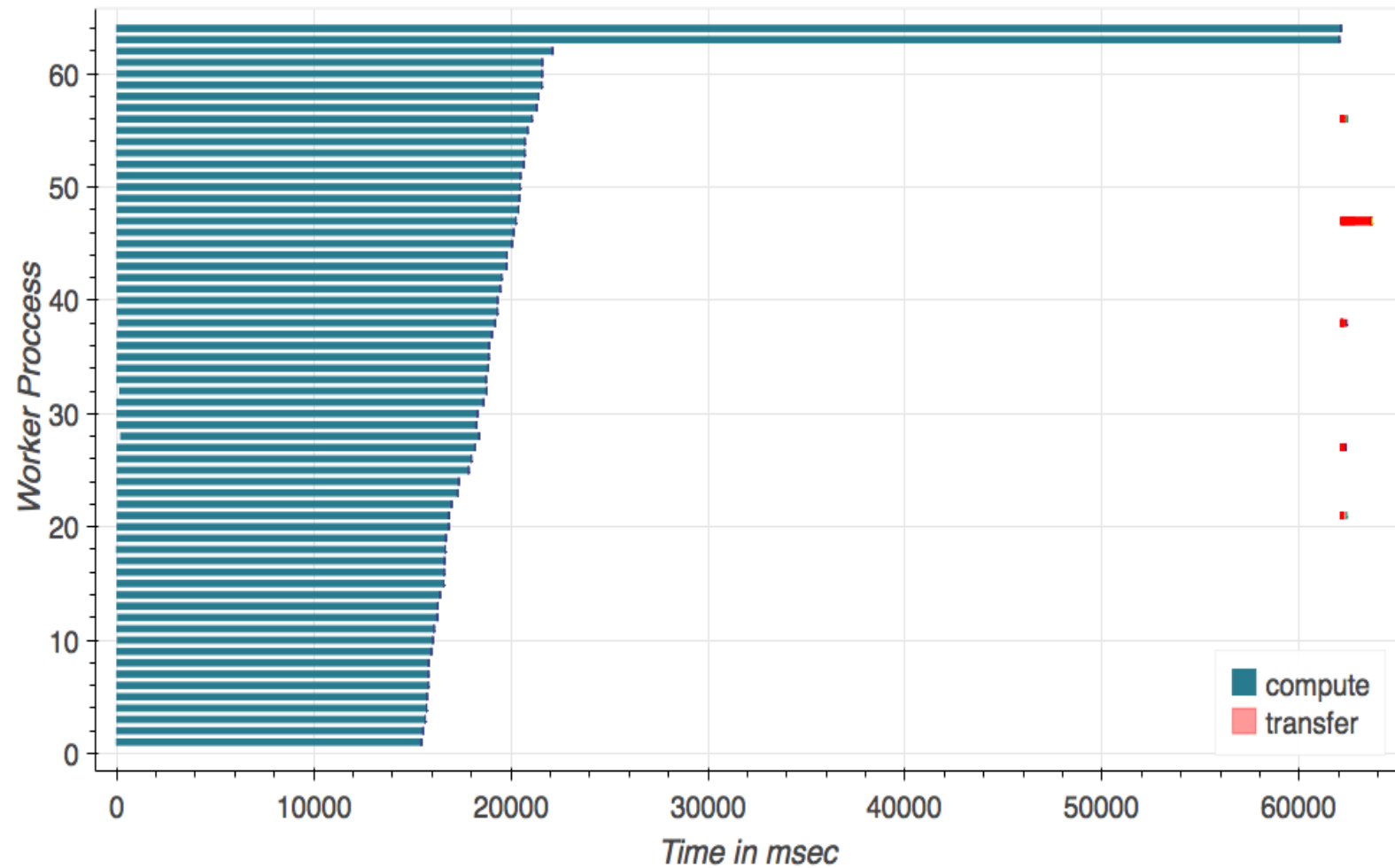


A**XTC 300x 64 blocks 64 cores.****B****XTC 300x 192 blocks 64 cores**