Lecture 2 — Interrupts & System Calls

Jeff Zarnett jzarnett@uwaterloo.ca

Department of Electrical and Computer Engineering
University of Waterloo

August 10, 2019

ECE 252 Fall 2019 1/35

Computer Organization

A regular program like a word processor need not be concerned with the underlying hardware of the computer.

The OS must be aware of the details and manage them.

What is a program? Instructions and data.

ECE 252 Fall 2019 2/3

To execute a program we need:

- 1 Main Memory
- 2 System Bus
- 3 Processor

Of course, this is the minimal set.

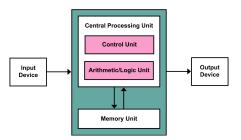


Image credit: Wikipedia user Kapooht

ECE 252 Fall 2019 3/35

Central Processing Unit (CPU)

The processor (CPU) is the brain of the computer.

Fetch instructions, decode them, execute them.

Fetch-decode-execute cycle repeated until the program finishes.

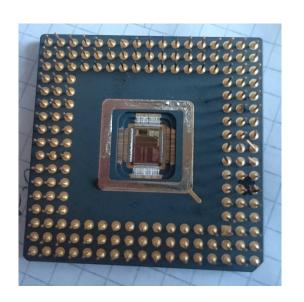
Different steps may be completed in parallel (pipeline).

Processors' largest unit is the word.

32-bit computer \rightarrow 32-bit word. 64-bit computer \rightarrow 64-bit word.

ECE 252 Fall 2019 4/35

IBM 686 CPU



ECE 252 Fall 2019 5/35

Central Processing Unit (CPU)

CPU instructions are specific to the processor.

Written assembly? You know the books.

Some operations only available in supervisor mode. Attempting to run it in user mode is an error.

ECE 252 Fall 2019 6/35

Central Processing Unit (CPU)

CPUs have storage locations: registers.

They may store data or instructions.

Management of registers is partly the role of the OS.

Let us examine a few of the critical registers.

ECE 252 Fall 2019 7/3

CPU Registers

A few of the registers in a typical CPU:

- **Program Counter** Next instruction.
- Status Register Array of bits to indicate flags.
- Instruction Register Instruction most recently fetched.
- Stack Pointer Top of the stack.
- General Purpose Registers Store data, addresses, etc.

ECE 252 Fall 2019 8 / 35

OBJECTION!!!



ECE 252 Fall 2019 9/35

The CPU needs data, but it takes a variable amount of time to get it.

Sometimes this means the equivalent of walking a book from Ottawa.

In the meantime, I should do something else.

Polling: check periodically if the book has arrived.

Interrupts: get a notification when the book is here.

If someone knocks on my door, I pause what I'm doing and get the book.

ECE 252 Fall 2019 10 / 35

Sources of Interrupts

We can put interrupts into four categories, based on their origin:

- 1 Program.
- 2 Timer.
- Input/Output.
- 4 Hardware Failure.

ECE 252 Fall 2019 11/35

Interrupts are a way to improve processor utilization.

CPU time is valuable!

When an interrupt take place, the CPU might ignore it (rarely).

More commonly: we need to handle it in some way.

Analogy: professor in a lecture; student has a question.

ECE 252 Fall 2019 12 / 35

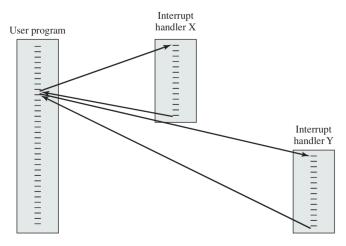
The OS: stores the state, handles the interrupt, and restores the state.

Sometimes the CPU is in the middle of something uninterruptible. Interrupts may be disabled (temporarily).

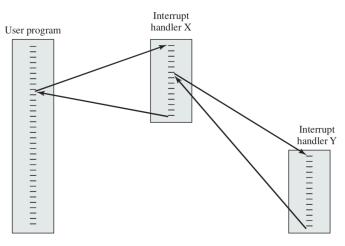
Interrupts can have different priorities.

ECE 252 Fall 2019 13/3

We may also have multiple interrupts in a short period of time:



(a) Sequential interrupt processing



(b) Nested interrupt processing

Or nested. Or a combination.

Storing and Restoring State

The OS must store the program state when an interrupt occurs.

The state must be stored.

State: values of registers.

Push them onto the stack.

Interrupt finished: restore the state (pop off the stack).

Then execution continues.

ECE 252 Fall 2019 16/35

Multiprogramming

That is saving and restoring the same program.

Why not restore a different program?

The Operating System might! It's not up to us.

ECE 252 Fall 2019 17/35

Invoking System Calls

Some services run automatically, without user intervention.

In other cases, we want specifically to invoke them. How?

ECE 252 Fall 2019 18 / 35

Admiral Ackbar Says:



ECE 252 Fall 2019 19/35

Operating systems run on the basis of interrupts.

A trap is a software-generated interrupt.

Generated by an error (invalid instruction) or user program request.

ECE 252 Fall 2019 20 / 35

Trap

If it is an error, the OS will decide what to do.
Usual strategy: give the error to the program.

The program can decide what to do if it can handle it.

Often times, the program doesn't handle it and just dies.

ECE 252 Fall 2019 21/3

User Mode and Kernel Mode

Already we saw user mode vs. supervisor (kernel) mode instructions.

Supervisor mode allows all instructions and operations.

Even something seemingly simple like reading from disk or writing to console output requires privileged instructions.

These are common operations, but they involve the OS every time.

ECE 252 Fall 2019 22 / 3

User Mode and Kernel Mode

Modern processors track what mode they are in with the mode bit.

At boot up, the computer starts up in kernel mode as the operating system is started and loaded

User programs are always started in user mode.

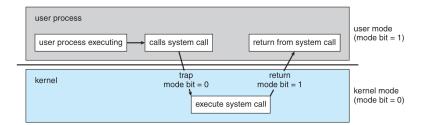
When a trap or interrupt occurs, and the operating system takes over, the mode bit is set to kernel mode.

When it is finished the system goes back to user mode before the user program resumes.

ECE 252 Fall 2019 23/3

Example: Text Editor Printing

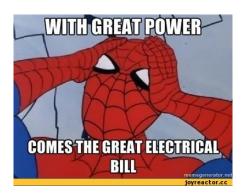
Suppose a text editor wants to output data to a printer.



ECE 252 Fall 2019 24/35

Why do we have user and supervisor modes, anyway?

Uncle Ben to Spiderman:



ECE 252 Fall 2019 25/35

Actually, though: "with great power comes great responsibility".

Same as why we have user accounts and administrator accounts.

To protect the system & its integrity against errant and malicious users.

ECE 252 Fall 2019 26 / 35

Multiple programs might be trying to use the same I/O device at once.

Program 1 tries to read from disk. This takes some time.

If Program 2 wants to read from the same disk, the operating system forces Program 2 to wait its turn.

Without the OS, it would be up to the author(s) of Program 2 to check and wait patiently for it to become available.

Works if everyone plays nicely.

Without enforcement of the rules, a program will do something nasty.

ECE 252 Fall 2019 27/3

There is a definite performance trade-off.

Switching from user to kernel mode takes time.

The performance hit is worth it for the security.

ECE 252 Fall 2019 28 / 35

Example: Reading from Disk

Here's the function we use for reading data from a file:

```
ssize_t read( int file_descriptor, void *buffer, size_t count );
```

read takes three parameters:

- 11 the file (a file descriptor, from a previous call to open);
- where to read the data to: and
- how many bytes to read.

Example:

```
int bytesRead = read( file, buffer, numBytes );
```

Note that read returns the number of bytes successfully read.

ECE 252 Fall 2019 29 / 35

They Elected Me To Lead, Not To Read

This is a system call, and system calls have documentation.

Finding and reading this information is a key skill for systems programming.

Google (or other search engine of your choice) is your friend.

Good sources: man7.org, linux.die.net, or the website of the code library!

ECE 252 Fall 2019 30 / 35

Example: Reading from Disk

In preparation for a call to read the parameters are pushed on the stack. This is the normal way in which a procedure is called in C(++).

read is called; the normal instruction to enter another function.

The read function will put its identifier in a predefined location.

Then it executes the trap instruction, activating the OS.

ECE 252 Fall 2019 31/35

Example: Reading from Disk

The OS takes over and control switches to kernel mode.

Control transfers to a predefined memory location within the kernel.

The trap handler examines the request: it checks the identifier.

Now it knows what system call request handler should execute: read.

That routine executes.

When it is finished, control will be returned to the read function. Exit the kernel and return to user mode.

read finishes and returns, and control goes back to the user program.

ECE 252 Fall 2019 32/

System Call Complex Example

```
#include <stdio.h>
#include < stdlib . h>
#include < unistd.h>
#include < string . h>
#include <fcntl.h>
void readfile( int fd ):
int main( int argc, char** argv ) {
    if ( argc != 2 ) {
        printf("Usage: .%s..<filename >\n", argv[0]);
        return -1:
    int fd = open( argv[1], O_RDONLY );
    if ( fd == -1 ) {
        printf("Unable_to_open_file!_%s_is_invalid_name?\n", arqv[1] );
        return -1:
    readfile( fd );
    close(fd):
    return 0;
```

ECE 252 Fall 2019 33 / 35

System Call Complex Example

```
void readfile( int fd ) {
    int buf_size = 256;
    char* buffer = malloc( buf_size );
    while ( 1 ) {
        memset( buffer, 0, buf_size );
        int bytes_read = read( fd, buffer, buf_size - 1 );
        if ( bytes_read == 0 ) {
                  break;
        }
        printf("%s", buffer);
    }
    printf("\nEnd_of_File.\n");
    free( buffer );
}
```

ECE 252 Fall 2019 34/35

System Call Summary

The steps, arranged chronologically, when invoking a system call are:

- 1 The user program pushes arguments onto the stack.
- 2 The user program invokes the system call.
- 3 The system call puts its identifier in the designated location.
- 4 The system call issues the trap instruction.
- The OS responds to the interrupt and examines the identifier in the designated location.
- 6 The OS runs the system call handler that matches the identifier.
- When the handler is finished, control exits the kernel and goes back to the system call (in user mode).

8 The system call returns control to the user program.

ECE 252 Fall 2019 35 / 35