Zsófia Ádám

Computer scientist · PhD Student

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Education and Degrees

- 2023- **Software Engineering PhD**, Budapest University of Technology and Economics, Department of Artificial Intelligence and Systems Engineering.
- 2022–2023 **Software Engineering MSc**, Budapest University of Technology and Economics, Thesis: Extending the Capabilities of the CEGAR Model Checking Algorithm **E**.
- 2018–2022 **Software Engineering BSc**, *Budapest University of Technology and Economics*, Thesis: Efficient Techniques for Formal Verification of C Programs **(a)**.

Experience

- 2023, 2024 LMU, Munich, Germany, Research Stay.
 - Summer I spent two months working on validation of correctness proofs of hardware and software modelcheckers at the Software Systems Laboratory under the supervision of Prof. Dr. Dirk Beyer, Nian-Ze Lee and Po-Chun Chien.
 - 2022 CERN, Meyrin, CH, CERN Summer Student.
 - Summer I learned about and worked on PLC verification, mainly requirement formalization as part of the Industrial Control Systems Group in the Beam Department for two months.
 - 2021 **thyssenkrupp Components Technology Hungary**, *Budapest, HU*, Software Engineering Intern Summer at thyssenkrupp.

Developing rules for C verification with static analyzer based on the in-house coding guidelines.

Skills and Interests

- Research model checking, formal methods, CEGAR, tool development, model transformations for verification, portfolios and algorithm selection
- Development Java, Kotlin, C/C++, git, CI, Python, Bash
 - Languages Hungarian (native), English (advanced), German (intermediate)

Selected Publications

- TACAS Btor2-Cert: A Certifying Hardware-Verification Framework Using Software Analyzers,
 - 2024 *Zs. Adám*, et al.
- TACAS ConcurrentWitness2Test: Test-Harnessing the Power of Concurrency (Competition
- (SV-COMP) **Contribution)**,
 - 2024 L. Bajczi, et al.
- TACAS EmergenTheta: Verification Beyond Abstraction Refinement (Competition Contribution),
- (SV-COMP) **□**
 - 2024 L. Bajczi, et al.
 - NFM 2023 From Natural Language Requirements to the Verification of Programmable Logic Controllers,
 - Zs. Ádám, et al.
 - FormaliSE C for yourself: comparison of front-end techniques for formal verification,
 - 2022 L. Bajczi, <u>Zs. Ádám</u>, Hajdu, V. Molnár.

TACAS Theta: portfolio of CEGAR-based analyses with dynamic algorithm selection (Competition

SV-COMP Contribution),

2022 <u>Zs. Ádám</u>, et al.

ORCID **D**0000-0003-2354-1750

MTMT Publication list on MTMT, 10077295

Dpen Source Contributions

Theta Algorithmic improvement of CEGAR, researching portfolio strategies, development of a C software Contributor model checking frontend.

Btor2-Cert Certified verification of hardware circuits utilizing software model checkers. Translation of software Contributor proofs back to the hardware circuit.

PLCverif Integrated the Formal Requirement Elicitation Tool (FRET) into the PLC verification tool PLCverif Contributor developed at CERN.

Gazer Added features and benchmarked Gazer, a BMC verification tool developed at ftsrg Contributor

Academic Services

- 2024 iFM Subreviewer
- 2023 FormaliSE Subreviewer
- 2022 ISSRE-W Subreviewer
- 2021, 2022, SV-COMP (TACAS) Subreviewer

2023, 2024

- 2024 SEFM Artifact Evaluation PC
- 2024 CAV Artifact Evaluation Subreviewer

T Awards and Scholarships

- 2023 "DKÖP" Doctoral Excellence Fellowship Programme Scholarship
- 2023 "ÚNKP" Research Scholarship
- 2023 First place at the National Scientific Students' Associations Conference (Formal Methods Category)
- 2022 First place at the Scientific Students' Associations Conference (Embedded Systems Category)
- 2021 Second place at the Scientific Students' Associations Conference (Software Category)
- 2022 "ÚNKP" Research Scholarship
- 2021–2023 National Academic Scholarship
- 2021–2023 Scholarship of the Faculty of BME-VIK

Teaching

Courses Software Techniques (in German and Hungarian) \cdot Software and Systems Verification course \cdot Systems Modeling \cdot Software Project Laboratory \cdot Systems Engineering \cdot Basics of Programming 1 (in German) \cdot Digital Technology

Student Advised 1 BSc Student (successfully graduated) Advisor



2017–2019 **Skool**, *Budapest*, *HU*, Mentor & Programming Tutor.

Teaching young girls on introductory programming workshops to motivate them to take part in IT related fields.

2023–2024 Girls' STEM Day, Budapest, HU, Presenter.

I was a presenter at the university faculty's open lab program.

2017, 2018, Researcher's Night, Budapest, HU, Presenter at Demos.

2024 I presented demos at the demonstration organized by my research group (ftsrg) at the university.