SPRINT

Feb 26th, 2018

Working on

* Currently working on LEA and printing out appropriate source and destination (including mode and register)
* Currently working on printing the address before the instruction

Backlog:

* Added case check incase of long operand. Might be 1 or 2 more that specifies the data
* Aiming to finish all opcode that start with 4
* Checking invalid address (aligned, non aligned)
* looping until user input correct address

FINISHED:

* NOP
* RTS