Code: CST202	Computer Organization and Microprocessors	Credit: 03
		L-T-P: (3-0-0)
Course Content	Organization of Computer Systems – CPU, Memory and I/O organization, Instruction encoding and addressing modes. Vonneumann versus Harvard Architecture, RISC and CISC architectures. Introduction to microprocessors, control unit, and interrupt system design. Design of hardware and software for microprocessor applications. Assembly language programming. Microprocessor system case studies – x86, IA, ARM.	
Important Text Books/References	1. Patterson and Hennessy: Computer Organization and Design, Morgan Kaufmann. 2. Hamacher and Zaky: Computer Organization, McGraw Hill. 3. Pal Chaudhuri: Computer Organization and Design, PHI. 4. Hayes: Computer Architecture and Organization, McGraw Hill. 5. Barry B. Brey: The Intel microprocessors. Pearson 6. Douglas V. Hall: Microprocessors and Interfacing, McGraw Hill.	