



Adarsh Misra

"Pre-final Year Engineering Student | Tech Enthusiast"

Contact

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www

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Skills

1. Verilog / System Verilog
2. Python
3. Artificial Intelligence and Machine Learning
4. C/C++/DSA

"Motivated Pre-final Year B.E. Student with research experience at IIIT Allahabad. Proficient in VLSI design using Verilog and Cadence, with a foundational understanding of Python and Machine Learning. Passionate about bridging the gap between digital hardware and artificial intelligence to solve complex engineering problems."

Work History

2025-06 -

2025-07

Summer Intern / Trainee Site Engineer

IIIT Allahabad, Uttar Pradesh, Prayagraj, India

- Completed intensive training on VLSI design flows, gaining hands-on proficiency in industry-standard tools, including Xilinx Vivado and Cadence.
- Designed and verified digital logic circuits using Verilog HDL, validating functionality through simulation and waveform analysis.
- Executed analog circuit design projects using Cadence, ensuring designs met technical specifications.
- Documented project outcomes in detailed technical reports, and presented final results to mentors and senior staff.

Education

Expected
in 2027-06

Bachelor of Engineering (B.E.): Electronics (VLSI Design & Technology)

Thapar Institute of Engineering & Technology - Patiala, Punjab, India

- Cumulative GPA: 8.02 / 10.00
- Relevant Coursework: VLSI Design, Digital Logic Design (Verilog), CMOS Analog Integrated Circuits, Microprocessors & Microcontrollers, Data Structures & Algorithms (Python).
- Key Academic Project: Designing an AI-based SAR 8-bit ADC (Analog-to-Digital Converter), focusing on optimizing power efficiency and conversion speed using mixed-signal design techniques.

Certifications

5. HTML/CSS/Javascript	2025-07	[Summer Training Program on VLSI Design (STPVD)], [IIITA] - [16th June to 13th July]
6. MatLab	2024-07	[Certificate of Completion of Learn C++ Programming Beginner to Advance- Deep Dive in C++, Instructor Abdul Bari], [Udemy]
7. Cadence Virtuoso / Layout Design	2025-12	[Certificate of Completion of Mastering DSA using C and C++, Instructor Abdul Bari], [Udemy]
8. Mentor Graphics	2025-07	[Certificate of Completion of Machine Learning Essentials- Master core ML concepts], [Udemy]
9. Intel/Altera Quartus II ModelSim FPGA Designing	2024-04	[Geometrical Shape Detection and Recognition using Python in Image Processing, ELC Activity]
10. Creativity and innovation		
11. Team collaboration		
12. Idea development and brainstorming		
13. Adaptability and flexibility		
14. Problem-solving		
15. Leadership potential		

Languages

English

Hindi

"Speedcubing & Strategy Games (Chess), Hardware/Software Coding, and Outdoor Sports."

"Engaging in competitive strategy games and logic puzzles to enhance problem-solving agility, while maintaining a balanced lifestyle through outdoor athletics and continuous technical upskilling."