

#### GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA

(TRAINING AND PLACEMENT CELL)
PUNJAB GOVERNMENT AIDED STATUS
An Autonomous college under UGC Act- 1956 [2(f) and 12(B)] - NBA
Accredited (http://www.gndec.ac.in)

Ref.No. PTP/2023/2103943 ECE Dated: 20/03/2023 15:33:24

To

SCL MOHALI Ajit Singh Nagar, Punjab

## SUBJECT: REQUEST FOR 4-WEEKS INDUSTRIAL TRAINING OF B.TECH/MBA/MCA/BCA 1st/2nd/3rd YEAR STUDENTS

Sir,

Greetings from GNDEC, Ludhiana.

Guru Nanak Dev Engineering College has emerged as one of the most prestigious engineering institute of North India over the 64 years of its inception and is conducting B.Tech. in seven disciplines as well as M.Tech., MBA and PhD. for meeting the research requirement of the technical field.

Since practical training is equal in importance to theoretical foundation, the course curriculum is so designed that the students get exposure to practical aspects of their respective engineering branch. We are in a process of enrolling the students of our institute to various Industrial Organizations for INDUSTRIAL TRAINING (4 WEEKS) which is an essential component of their degree programme.

#### The programme will be as under:

- To get familiar with the setup and working of the organisation.
- Working on the given project, 2<sup>nd</sup> week onwards.
- Submission of Final Report.
- Submission of Daily Diary at the end of training.

We recommend our graduating student

Email Id adarshmishra2608@gmail.com

Mr./Ms. ADARSH MISHRA

Phone no. 919554395931

Roll no. 2103943

**Branch ECE** 

to undergo 4 weeks Industrial training at your esteemed organization starting from June, 2023

We would highly appreciate it if the student can be accommodated for the training programme. Our students are sincere and hard working and we are sure that they will put in their best efforts during the training program. Looking for confirmation from your side.

Yours Sincerely

Prof. G.S. Sodhi

Training & Placement Officer

(\*This document is computer generated and doesn't require signature)

Address: Training and Placement Cell, GNDEC, Gill Road, Ludhiana - 141006 E-mail: tpo@gndec.ac.in Cell: 9915794339 Phone No. 0161 – 5064510

#### **RESUME**

#### ADARSH MISHRA

Bachelor of Technology – Electronics and Communication Engineering

2<sup>nd</sup> Year

Guru Nanak Dev Engineering College, Ludhiana

Contact: 9554395931

Email: adarshmishra2608@gmail.com

Reference: Dr. Navneet Kaur,

Assistant Professor,

Department of ECE, GNDEC Ludhiana,

Email: navneetkaur@gndec.ac.in

Mobile: 8283844114

#### **CAREER OBJECTIVE:**

Looking for a promising & challenging opportunity that will enable me to provide the best of my technical, analytical & professional skills. An opportunity that can sharpen my current skills and knowledge and where I can have a good scope for learning and implementing new technologies.

#### **OUALIFICATIONS:**

Course	Name Of Institute	University/Board	Year	Aggregate %
B. Tech (ECE)	Guru Nanak Dev Engineering College, Ludhiana	I.K. Gujral Punjab Technical University, Jalandhar	2021- Present	8.3 CGPA (Till 2 <sup>nd</sup> Sem.)
B.Sc. (Hons) (Online)	Indian Institute of Technology, Madras	Indian Institute of Technology, Madras	2022- Present	6.0 GPA
12 <sup>th</sup>	Universal Public School, Baburi, Chandauli	CBSE	2018- 2020	72.4 %
10 <sup>th</sup>	St. Francis School, Ramnagar Varanasi	CISCE	2017- 2018	78.6 %

#### TECHNICAL SKILLS

- Language known
  - C, C++, Python Programming.
- Platform
  - > Spyder, VS Code, Anaconda, Replit, Arduino
- Core Subject Knowledge
  - ➤ Intricate knowledge of Electronic Devices & Analog Circuits (Amplifiers, JFET, MOSFET, FET, MOS Capacitors, Oscillators, OP-AMP)
  - > Strong knowledge of Digital Electronics (Logic Gates, Multiplexer, Demultiplexer, Flip-Flops, DACs, ADCs)
  - ➤ Sufficient knowledge of Signals & Linear Control Systems.
  - ➤ Strong Knowledge of Computer Architecture

#### Tools

LaTeX, MATLAB, MS OFFICE (Word, Excel, PowerPoint, Outlook)

#### TRAINING / INTERNSHIP:

Attended one-month industrial training in "BASIC OF ELECTRONICS" at Guru Nanak Dev Eng. College, Ludhiana. In this one-month training, we have learned to implement various circuits using 'ARDUINO' such as LED Interfacing, LCD Interfacing, etc. We learned about the "Embedded System" and worked on a breadboard, interfacing with a servo motor, seven-segment display, sensors, etc.

#### **PROJECT:**

• **Project Title:** HYDRAULIC EXCAVATOR

Designed a working model of a Hydraulic Excavator which works on the principle of Pascal's Law, Research Oriented.

• **Project Title:** ULTRASONIC SENSOR

Designed a security device, the system sends an alarm if it detects any movement in its defined range (0-5cm).

*Components used:* Arduino UNO, Resistor 221-ohm, Buzzer, Breadboard, Ultrasonic Sensor - HC-SR04 (Generic)

Project Title: UNO DIGITAL CLOCK

Designed a simple digital clock with the help of a 16x2 LCD

**Component Used:** Solderless Breadboard Half Size, Pushbutton switch 12mm, Standard LCD - 16x2 White on Blue

#### **EXTRACURRICULAR ACTIVITIES:**

- Got selected for the IIT Roorkee Virtual Lab Development team.
- Finalist research paper in the "IDEAZ": ECE event organized by Cognizance, IIT Roorkee 2023.
- Finalist in the "Privatiza" event organized by Cognizance, IIT Roorkee 2022, where a case study on "privatization" was presented.
- Participated in BUILD'23 global hackathon, where idea on Healthcare was presented and we were offered funding from a Chinese MNC (official sponsor) of \$300k for 7% equity.)
- Working as event management head in the Causmic Club, GNDEC Ludhiana.
- Worked as peer-to-peer learning mentor for 1<sup>st</sup> year B.Tech students.
- Served the role of core organizer of paradox 2022 a fest organized at IIT Madras.
- Organizer of Paradox in SAAVAN 2022 fest organized at IIT Madras
- Attended student development program on "Programming in C" at IIT Kanpur
- Organized event Apex 2022 under SCIE society.
- Member of IETE, SCIE.
- Executive at CML.
- Presented a paper at Guru Granth Sahib University on the topic "The Effect Of Data Privacy On Cloud Computing In Commerce" in Research Meet 2023.
- Organized an event "Science In Our Daily Life" in 2022.
- Selected for a scholarship for academic excellence.
- Working as mess & hostel committee member in 2<sup>nd</sup> year hostel.

<b>HOBBIES:</b>
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- Playing cricket.
- Watching Movie
- Programming

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**Father's Name:** Arvind Kumar Mishra

Mother's Name: Anju Mishra

**Date of Birth:** 07-07-2002

Gender: Male

Email ID: adarshmishra2608@gmail.com

Nationality: Indian

Permanent Address: Vill- Lakhesar, P.O - Shahpur, Sikrara, Jaunpur - 221008

#### **DECLARATION**:

I hereby declare that the above information is true to the best of my knowledge and belief.

Place: Ludhiana ADARSH MISHRA

Date:

## COUNCIL FOR THE INDIAN SCHOOL CERTIFICATE EXAMINATIONS, NEW DELHI INDIAN CERTIFICATE OF SECONDARY EDUCATION (CLASS - X) - YEAR 2018

No.TF 20122336



1185313/046

#### STATEMENT OF MARKS

Name ADARSH MISHRA

of ST. FRANCIS SCHOOL, RAMNAGAR, VARANASI

UNIQUE ID 6686297

Son of

Smt ANJU MISHRA

Shri ARVIND KUMAR MISHRA

#### **SUBJECTS**

External Examination

ENGLISH

HINDI

HISTORY, CIVICS & GEOGRAPHY (HCS-A, GEO-C)

MATHEMATICS

SCIENCE (PHY-C, CHE-C, BIO-C)

COMPUTER APPLICATIONS

#### Percentage Marks

79 SEVEN NINE

94 NINE FOUR

75 SEVEN FIVE

80 EIGHT ZERO

60 SIX ZERO

81 EIGHT ONE

#### Internal Assessment

SUPW & COMMUNITY SERVICE

Date of birth as certified by the (in words) Seventh July Two Thousand Two Head of the School at the time of (in figures) 07.07.2002 registration

RESULT - PASS CERTIFICATE AWARDED

Date of declaration of Result - 14.05.2018

Note: 1. The pass mark for each subject is 33%

2. No divisions are awarded.

Grade

A



क्रम संख्या / S.No.SSCE/2020/

0655339



रजि. नं. Regn.No.

# केन्द्रीय माध्यमिक शिक्षा बोर्ड CENTRAL BOARD OF SECONDARY EDUCATION अंक विवरणिका MARKS STATEMENT

सीनियर स्कूल सर्टिफिकेट परीक्षा, 2020 SENIOR SCHOOL CERTIFICATE EXAMINATION, 2020



परीक्षार्थी का नाम Name of Candidate

ADARSH MISHRA

अनुक्रमांक Roll No.

23615918

माता का नाम

Mother's Name ANJU MISHRA

पिता/संरक्षक का नाम

Father's/Guardian's Name ARVIND KUMAR MISHRA

विद्यालय School

70278 UNIVERSAL PUB SCH PO BABURI CHANDAULI UP

विषय कोड SUB. CODE			प्राप्तांक 1	MARKS O	BTAINED	000	
	विषय SUBJECT	लिखित THEORY	आं. मू. I A/ प्रा. PR.	योग TOTAL	योग (शब्दों में) TOTAL (IN WORDS)	स्थितीय ग्रेड POSITIONAL GRADE	
301	ENGLISH CORE	071	020	091	NINETY ONE	A2	
042	PHYSICS	039	030	069	SIXTY NINE	C1	
043	CHEMISTRY	038	030	068	SIXTY EIGHT	C1	
041	MATHEMATICS	041	020	061	SIXTY ONE	C2	
048	PHYSICAL EDUCATION	045	029	074	SEVENTY FOUR	C1	
034	HIND MUSIC.VOCAL	002	056	058 RT	FIFTY EIGHT	E	
500	WORK EXPERIENCE				THA BEARD	A1	
502	HEALTH & PHYSICAL EDUCATION					A2	
503	GENERAL STUDIES				Block Annual Control	A2	

संक्षिप्तियों का अर्थ : Abbreviations

AB : अनुपस्थित Absent

RP: प्रयोगात्मक में पुनरावृत्ति Repeat in Practical RT: लिखित में पुनरावृत्ति Repeat in Theory

ER : अनिवार्य पुनरावृत्ति सभी विषयो में

Essential Repeat in all subjects

दिल्ली Delhi

दिनांक Dated: 13-07-2020

परिणाम Result PASS

परीक्षा नियंत्रक Controller of Examinations

## ਆਈ.ਕੇ.ਗੁਜਰਾਲ ਪੰਜਾਬ ਟੈਕਨੀਕਲ ਯੂਨੀਵਰਸਿਟੀ S.No. of EDP

1001712097

### I.K. Gujral Punjab Technical University

#### Punjab Technical University

Regn. cum Roll No: 2103943 Name of the College/Institute: **Guru Nanak Dev Engineering** College, Ludhiana

(Punjab Govt. Aided Status) An Autonomous College Under UGC Act-1956 [2(f) and 12(B)]





## rade Sheet

Bachelor of Technology(Civil Engineering), FIRST Semester, Nov-2021

Name : ADARSH MISHRA

Father's Name : ARVIND KUMAR MISHRA

Mother's Name : ANJU MISHRA

Subject Code	Subject	Туре	Credits	Grade
BSC-104	Mathematics-II	Theory	4	A+
BSC-105	Chemistry	Theory	4	A+
BSC-106	Chemistry Lab	Practical	1	0
ESC-104	Programming for Problem Solving	Theory	4	A+
ESC-105	Programming for Problem Solving Laboratory	Practical	1	B+
ESC-106	Manufacturing Practices	Practical	2	Α
HSMC-101	English	Theory	3	Α
HSMC-102	English Laboratory	Practical	1	0

Credits Registered in the Semester :20

Semester Grade Point Average (SGPA): 8.75

**Notification Result Date** : 06.07.2022

: JALANDHAR Date of Issue : 06.07.2022

E.D.P. CELL

Prepared By

Checked & Verified By

Officer Incharge

## ਆਈ.ਕੇ.ਗੁਜਰਾਲ ਪੰਜਾਬ ਟੈਕਨੀਕਲ ਯੂਨੀਵਰਸਿਟੀ

1001796177

## I.K. Gujral Punjab Technical University

Formerly

#### Punjab Technical University

Regn. cum Roll No: 2103943
Name of the College/Institute:
Guru Nanak Dev Engineering

College,Ludhiana

(Punjab Govt. Aided Status) An Autonomous College Under UGC Act-1956 [2(f) and 12(B)]





## Grade Sheet

Bachelor of Technology(Civil Engineering), SECOND Semester, April-2022

Name : ADARSH MISHRA

Father's Name : ARVIND KUMAR MISHRA

Mother's Name : ANJU MISHRA

Subject Code	Subject	Туре	Credits	Grade
BSC-101	Physics	Theory	4	A
BSC-102	Physics Lab	Practical	,1	0
BSC-103	Mathematics-I	Theory	4	B+
ESC-101	Basic Electrical Engineering	Theory	4	A
ESC-102	Basic Electrical Engineering Laboratory	Practical	1	0
ESC-103	Engineering Graphics and Design	Theory	3	B+
HSMC-103	Principles of Engineering Economics and Management	Theory	3	Α
MPD-101	Mentoring and Professional Development	Practical	1	A+

Credits Registered in the Semester :21 Semester Grade Point Average (SGPA) : 7.90

Notification Result Date : 06.10.2022

Place : JALANDHAR
Date of Issue : 06.10.2022

E.D.P. CELL

Prepared By

Checked & Verified By

Officer Incharge

Controller of Examinations

## PROVISIONAL RESULT End Semester Examinations, Dec, 2022

Name	ADARSH MISHRA
Father's Name	ARVIND KUMAR MISHRA
University Roll No.	2103943
Course (Branch)	B.Tech. (Electronics and Communication Engineering)
College Roll No.	2117107

Semester	Subject Code	M code / Paper ID	Subject Title	Theory / Practical	Result Type	Internal Obtained Marks	Internal Max. Marks	External Obtained Marks	External Max. Marks
3	BSEC-101	16030 / 16030	Engineering Mathematics - III	Т	Regular	38	40	55	60
3	MCEC-101	16036 / 16036	Environmental Sciences	Т	Regular	44	50		
3	PCEC-101	16031 / 16031	Electronic Devices	Т	Regular	31	40	31	60
3	PCEC-102	16032 / 16032	Network Analysis and Synthesis	Т	Regular	35	40	39	60
3	PCEC-103	16033 / 16033	Signals and Systems	Т	Regular	33	40	42	60
3	PCEC-104	16034 / 16034	Digital Electronics	Т	Regular	34	40	45	60
3	PCEC-105	16035 / 16035	Computer Architecture	Т	Regular	29	40	41	60
3	LPCEC-101	16037 /	Electronic Devices - Laboratory	Р	Regular	27	30	17	20
3	LPCEC-102	16038 /	Digital Electronics - Laboratory	Р	Regular	25	30	16	20
3	TR-101	16039 /	Training-I	P	Regular	53	60	36	40