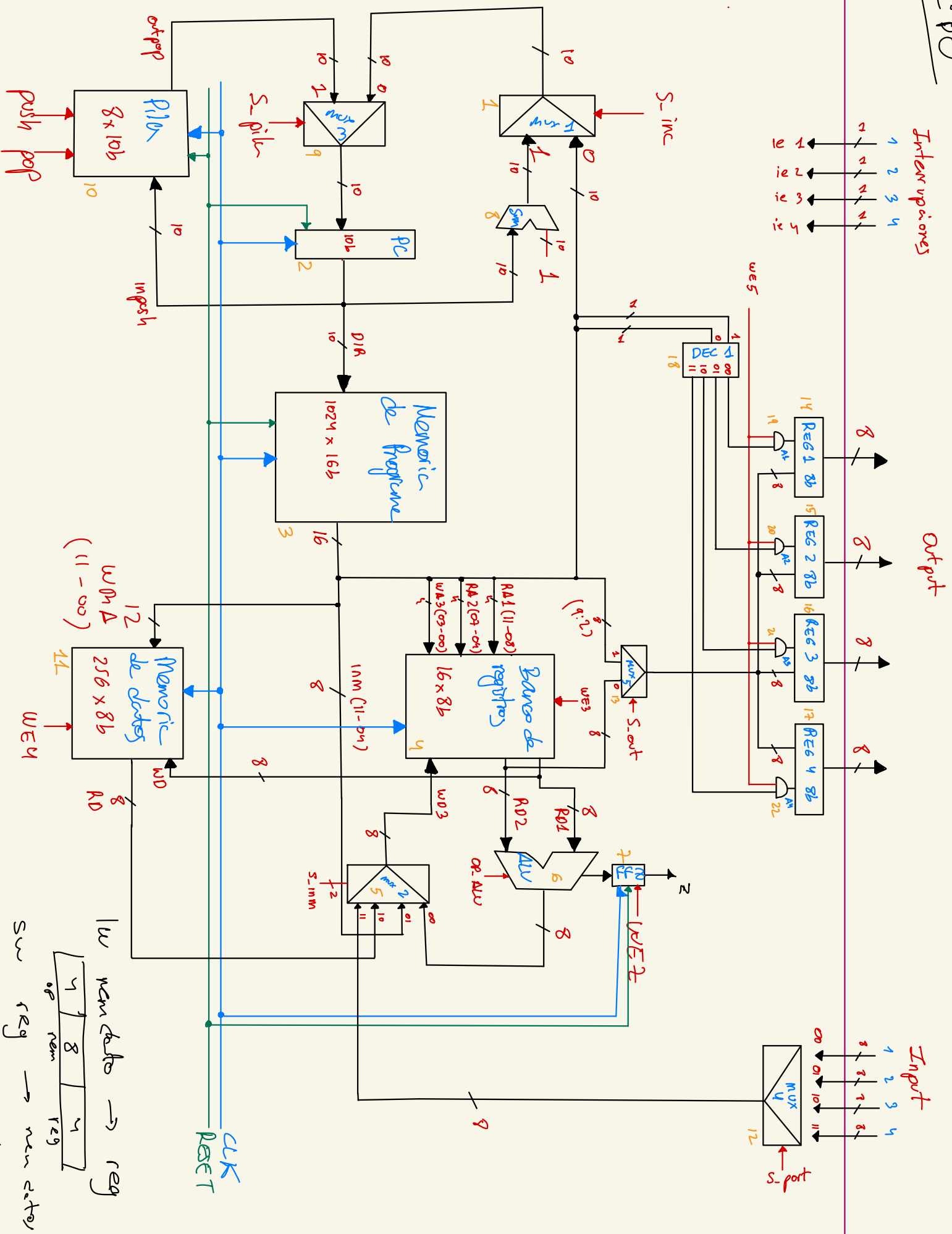


Interupciones



1. $\text{mem_data} \rightarrow \text{reg}$

$\begin{array}{|c|c|c|c|} \hline 1 & 8 & & 1 \\ \hline \end{array}$
 $\begin{array}{c} \text{op} \\ \text{mem} \end{array}$
 $\begin{array}{c} \text{reg} \end{array}$

$\text{sum} \leftarrow \text{reg} \rightarrow \text{mem_data}$

$\begin{array}{|c|c|c|c|} \hline 1 & 1 & 1 & 8 \\ \hline \end{array}$
 $\begin{array}{c} \text{op} \\ \text{reg} \end{array}$
 $\begin{array}{c} \text{mem} \end{array}$