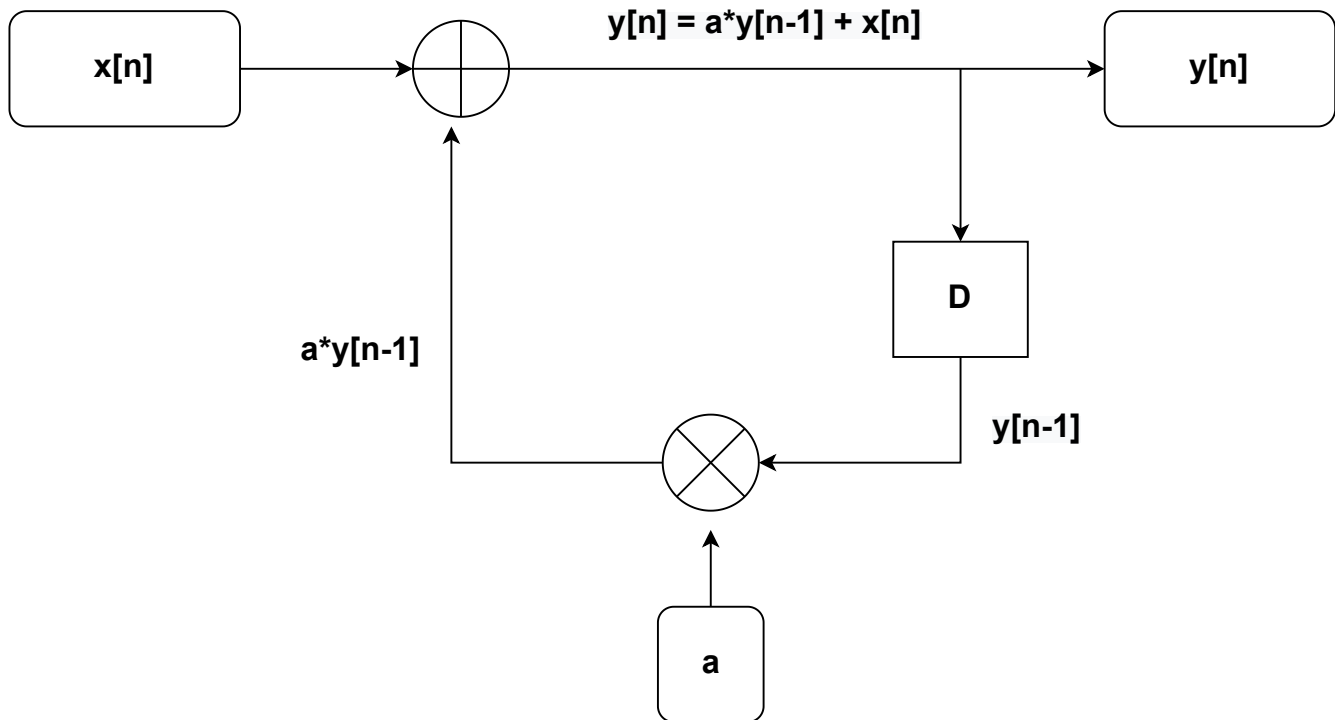
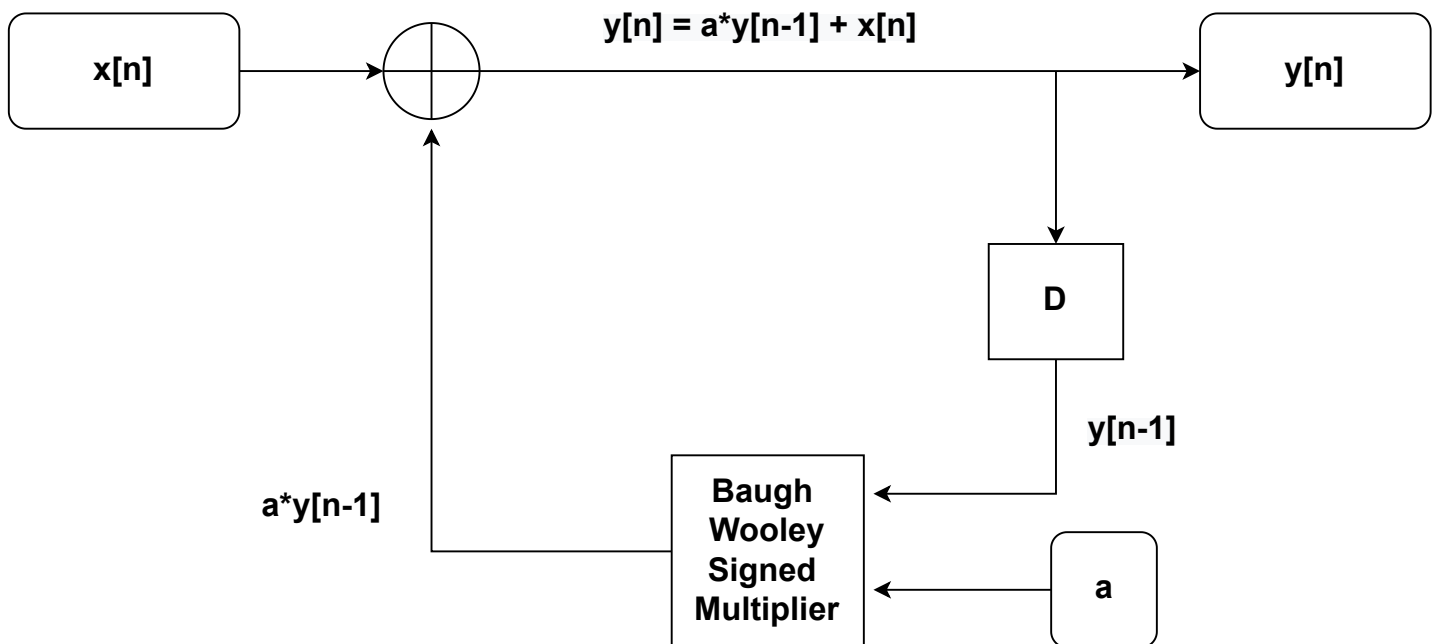


## IIR Filter Example

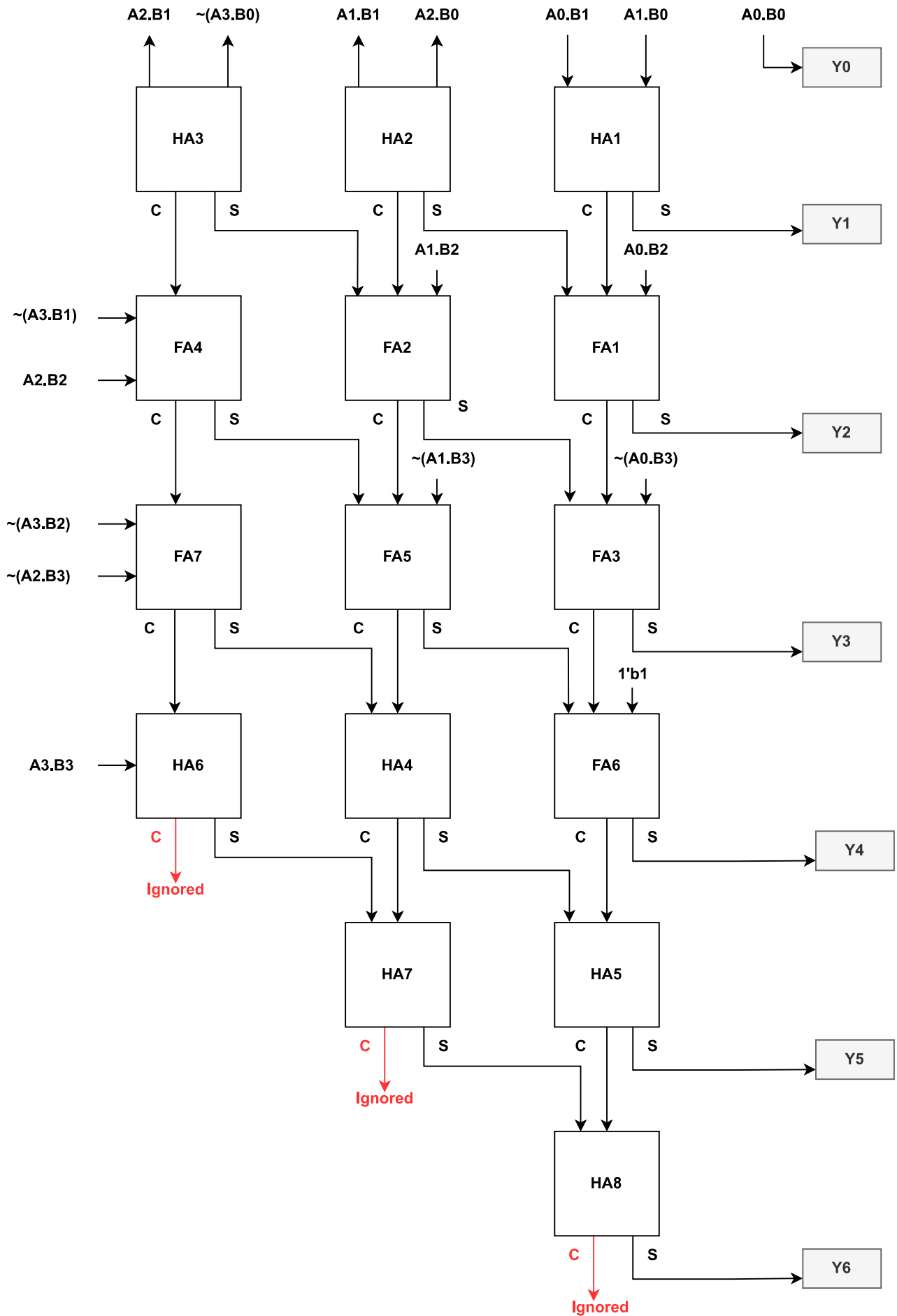


## Using Baugh Wooley Signed Multiplier in IIR Filter

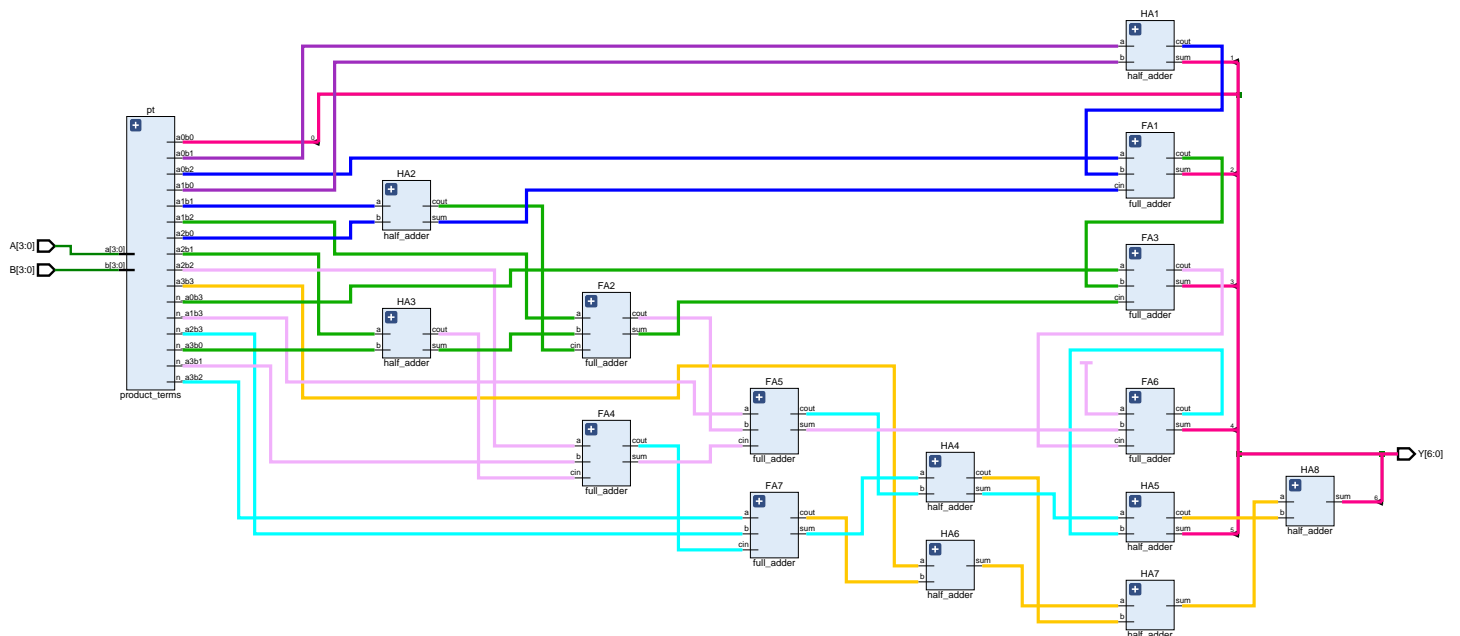


# Baugh Wooley Signed Multiplier Architecture

Y -> 7 bit Product



## Baugh Wooley Signed Multiplier Schematic ( in Xilinx Vivado)



## IIR Filter Schematic ( in Xylinx

