Qubit Simulation Module

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## Model Assumptions

* Substrate
  + Centered on (0,0)
  + For flip chip both chips are centered on (0,0), but can be rectangles of different sizes.
* Ground
  + Covers the full substrate
  + Separated from nodes via a spacing given by the node’s “boundary” parameters
* Model
  + Capacitances are, by and large, not generally removed even for distant components. That said, some models, such as the lumping of the resonator, rely on some capacitances to be negligible. Typically we want to operate in the regime where the following capacitances are negligible:
    - CR[N]Pad1\_R[N]Pad2
    - CR[N]Pad1\_R[M]Pad1,2, N!=M
    - CR[N]Pad2\_R[M]Pad1,2, N!=M

# For Developers