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student@studentVM:~/fpga_trn/OpenCL/OCL_17_1
File Edit View Search Terminal Help
[student@studentVM OCL_17_1]$ aoc -march=emulator -board=a10gx SimpleKernel.cl
aoc: Running OpenCL parser....
/home/student/fpga_trn/OpenCL/OCL_17_1/SimpleKernel.cl:3:42: warning: declaring kernel argument with no
'restrict' may lead to low kernel performance
void SimpleKernel(__global const float * in, __global const float * in2, __global float * out, uint N)
                                     ^
/home/student/fpga_trn/OpenCL/OCL_17_1/SimpleKernel.cl:3:69: warning: declaring kernel argument with no
'restrict' may lead to low kernel performance
void SimpleKernel(__global const float * in, __global const float * in2, __global float * out, uint N)
                                     ^
/home/student/fpga_trn/OpenCL/OCL_17_1/SimpleKernel.cl:3:91: warning: declaring kernel argument with no
'restrict' may lead to low kernel performance
void SimpleKernel(__global const float * in, __global const float * in2, __global float * out, uint N)
                                     ^
3 warnings generated.
aoc: Compiling for Emulation ....
[student@studentVM OCL_17_1]$

```

This is because the AOC compiler performs dependency analysis to optimize the pipelined circuit created. Multiple pointers can technically point to the same area, which prevents the compiler from creating the optimal circuit. Using the “restrict” keyword lets the compiler know that pointers won’t point to the same area and that it can treat pointers as separate and make optimizations accordingly.

*To resolve the warning, simply add the **restrict** keyword to each of the pointers in the kernel.*

*e.g. `__global const float * restrict in`*

Remember if you change the kernel code, you will need to rerun the aoc compiler on the kernel file.

Step 2. Write the host code that launches the kernel

1. Reopen **main.cpp** in Eclipse.
2. Find “`#define EXERCISE1`” around line 13 in **main.c** and comment it out.

Commenting enables the portion of the code that launches the kernel verifies the results.

3. Find the comment “Exercise 2 Step 2.3”. Right beneath it, write the code that would create the `cl::Program` object from the `aocx` binary. Use the context and devices list created from the first lab.

If we were not running in emulation mode, the variable named “mybinaries” would store the information from the `.aocx` file used to program the FPGA. However, in the emulation mode, the `aocx` file is just a software library that can be dynamically linked with the host code.

4. After the comment “Exercise 2 Step 2.4”, call the function that **builds** the program from the program created in the previous two steps.

For Intel FPGA, this is only done as a formality.