



8. Next, click on **Miscellaneous** (under **Libraries**), and observe the Linker flags (which are command line options) that have been set up.
9. In the terminal window, type “aocl link-config”

```
[student@studentVM OCL_17_1]$ aocl link-config
-L/opt/intelFPGA_pro/17.1/hld/board/a10_ref/linux64/lib -L/opt/intelFPGA_pro/17.1/hld/host/linux64/lib -Wl,--no-as-needed -lalteracl -laltera_a10_ref_mmd -lelf
```

This command gives you the configuration you need to use for the linker settings. Observe that they are the same things set up in the Eclipse project.

10. Click **Cancel** at the bottom of the Property window to close the window.